

# HPM-SRSDE

Intel® dual 4th Gen. Xeon® Scalable Processor Proprietary  
Server Board with Intel®C741 Chipset and IPMI2.0

## User's Manual



1<sup>st</sup> Ed –07 September 2023

## FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

- (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.
- (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

## Notice

This guide is designed for experienced users to setup the system within the shortest time. For detailed information, please always refer to the electronic user's manual.

## Copyright Notice

Copyright © 2023 Avalue Technology Inc., ALL RIGHTS RESERVED.

No part of this document may be reproduced, copied, translated, or transmitted in any form or by any means, electronic or mechanical, for any purpose, without the prior written permission of the original manufacturer.

## Trademark Acknowledgement

Brand and product names are trademarks or registered trademarks of their respective owners.

## Disclaimer

Avalue Technology Inc. reserves the right to make changes, without notice, to any product, including circuits and/or software described or contained in this manual in order to improve design and/or performance. Avalue Technology assumes no responsibility or liability for the use of the described product(s), conveys no license or title under any patent, copyright, or masks work rights to these products, and makes no representations or warranties that

these products are free from patent, copyright, or mask work right infringement, unless otherwise specified. Applications that are described in this manual are for illustration purposes only. Avalue Technology Inc. makes no representation or warranty that such application will be suitable for the specified use without further testing or modification.

### **Life Support Policy**

Avalue Technology's PRODUCTS ARE NOT FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE PRIOR WRITTEN APPROVAL OF Avalue Technology Inc.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into body, or (b) support or sustain life and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### **A Message to the Customer**

#### ***Avalue Customer Services***

Each and every Avalue's product is built to the most exacting specifications to ensure reliable performance in the harsh and demanding conditions typical of industrial environments. Whether your new Avalue device is destined for the laboratory or the factory floor, you can be assured that your product will provide the reliability and ease of operation for which the name Avalue has come to be known.

Your satisfaction is our primary concern. Here is a guide to Avalue's customer services. To ensure you get the full benefit of our services, please follow the instructions below carefully.

#### ***Technical Support***

We want you to get the maximum performance from your products. So if you run into technical difficulties, we are here to help. For the most frequently asked questions, you can easily find answers in your product documentation. These answers are normally a lot more detailed than the ones we can give over the phone. So please consult the user's manual first.

To receive the latest version of the user's manual; please visit our Web site at:

<http://www.avalue.com.tw/>

## HPM-SRSDE User's Manual

### *Product Warranty*

Avalue warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for two years from the date of purchase.

This warranty does not apply to any products which have been repaired or altered by persons other than repair personnel authorized by Avalue, or which have been subject to misuse, abuse, accident or improper installation. Avalue assumes no liability under the terms of this warranty as a consequence of such events. Because of Avalue's high quality-control standards and rigorous testing, most of our customers never need to use our repair service. If any of Avalue's products is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time, and freight. Please consult your dealer for more details. If you think you have a defective product, follow these steps:

1. Collect all the information about the problem encountered. (For example, CPU type and speed, Avalue's products model name, hardware & BIOS revision number, other hardware and software used, etc.) Note anything abnormal and list any on-screen messages you get when the problem occurs.
2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information available.
3. If your product is diagnosed as defective, obtain an RMA (return material authorization) number from your dealer. This allows us to process your good return more quickly.
4. Carefully pack the defective product, a complete Repair and Replacement Order Card and a photocopy proof of purchase date (such as your sales receipt) in a shippable container. A product returned without proof of the purchase date is not eligible for warranty service.
5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

# Content

<b>1. Getting Started</b>	<b>9</b>
1.1 Safety Precautions	9
1.2 Packing List	10
1.3 Document Amendment History	11
1.4 Manual Objectives	12
1.5 System Specifications	13
1.6 Architecture Overview—Block Diagram	20
<b>2. Hardware Configuration</b>	<b>21</b>
2.1 Product Overview	22
2.2 Jumper and Connector List	23
2.3 Setting Jumpers & Connectors	26
2.3.1 Flash Security Override (JPFLASHSEC)	26
2.3.2 ME FW update (JPME1)	26
2.3.3 Force PWRON setting (JPALLPWRON1)	27
2.3.4 Clear CMOS (JPBAT1)	27
2.3.5 Boot UART5 setting (JPBOOT_UART5)	28
2.3.6 SATA or PCIE select (JPSATASEL)	28
2.3.7 CPLD JTAG header (JCPLD_JTAG1)	29
2.3.8 System fan connector 1 (SYS_FAN1)	29
2.3.9 System fan connector 2 (SYS_FAN2)	30
2.3.10 System fan connector 3 (SYS_FAN3)	30
2.3.11 System fan connector 4 (SYS_FAN4)	31
2.3.12 System fan connector 5 (SYS_FAN5)	31
2.3.13 System fan connector 6 (SYS_FAN6)	32
2.3.14 CPU fan connector 1 (CPU_FAN1)	32
2.3.15 CPU fan connector 2 (CPU_FAN2)	33
2.3.16 SPI connector (JSPI1)	33
2.3.17 Serial port 2 connector (JCOM2)	34
2.3.18 BMC_UART5 debug connector (JCOM5)	34
2.3.19 Serial General Purpose I/O connector (JSGPIO1)	35
2.3.20 ATX 12V power connector 1 (ATX12V1)	35
2.3.21 ATX 12V power connector 2 (ATX12V2)	36
2.3.22 ATX 12V power connector 3 (ATX12V3)	36
2.3.23 ATX power connector (ATXPWR1)	37
2.3.24 Power supply PMBus connector (JPMBUS1)	37
2.3.25 Front Panel USB2.0 connector (JUSB1)	38
2.3.26 Front Panel USB3.1 connector (JUSB2)	38

## HPM-SRSDE User's Manual

2.3.27	Front Panel connector 1 (JFP1).....	39
2.3.28	Front Panel connector 2 (JFP2).....	39
2.3.29	Inlet Thermal Sensor (JINLET_SER1).....	40
2.3.30	Outlet Thermal Sensor (JOUTLET_SER1).....	40
2.3.31	HDD Backplane thermal Sensor (JHDD_SER1) .....	41
2.3.32	CASE OPEN connector (JCASE_OPEN1).....	41
2.3.33	VROC Header (JRAID_KEY1).....	42
2.3.34	CPU PCIE HP SMB connector (JPEHPSMB1) .....	42
2.3.35	AZALIA connector (JAUDIO1) .....	43
2.3.36	SMBUS VR connector (JVR_PRG1) .....	43
2.3.37	ESPI connector (JESPI1).....	44
2.4	Processor Installation SOP .....	45
<b>3</b>	<b>BIOS Setup .....</b>	<b>52</b>
3.1	Introduction .....	53
3.2	Starting Setup .....	53
3.3	Using Setup .....	54
3.4	Getting Help.....	55
3.5	In Case of Problems.....	55
3.6	BIOS setup.....	56
3.6.1	Main Menu.....	56
3.6.1.1	System Language.....	56
3.6.1.2	System Date .....	56
3.6.1.3	System Time.....	56
3.6.2	Advanced Menu .....	57
3.6.2.1	Trusted Computing .....	57
3.6.2.2	ACPI Settings .....	58
3.6.2.3	AST2600 Super IO Configuration.....	59
3.6.2.3.1	Serial Port 1 Configuration .....	59
3.6.2.3.2	Serial Port 2 Configuration .....	60
3.6.2.4	Serial Port Console Redirection .....	60
3.6.2.5	Option ROM Dispatch Policy .....	61
3.6.2.6	USB Configuration .....	63
3.6.2.7	Network Stack Configuration .....	64
3.6.2.8	NVMe Configuration .....	64
3.6.3	Platform Config.....	65
3.6.3.1	PCH-IO Configuration.....	65
3.6.3.1.1	PCI Express Configuration .....	66
3.6.3.1.2	SATA And RST Configuration .....	71
3.6.3.1.3	USB Configuration .....	74
3.6.3.1.4	HD Audio Configuration.....	75

- 3.6.3.2 Server ME Configuration ..... 75
- 3.6.4 Socket Config ..... 76
  - 3.6.4.1 Processor Configuration ..... 76
    - 3.6.4.1.1 Per-Socket Configuration ..... 77
      - 3.6.4.1.1.1 CPU Socket 0 Configuration..... 77
      - 3.6.4.1.1.2 CPU Socket 1 Configuration..... 78
    - 3.6.4.2 Memory Configuration ..... 78
      - 3.6.4.2.1 Memory Topology ..... 79
    - 3.6.4.3 IIO Configuration..... 79
      - 3.6.4.3.1 Socket0 Configuration ..... 80
        - 3.6.4.3.1.1 Port DMI..... 84
        - 3.6.4.3.1.2 Port 1A(PCIe Slot6), Port 2A(PCIe Slot4), Port 3A(PCIe Slot2), Port 4A(X550), Port 4C(M.2), Port 4E(SAS1), Port 4G(SAS1), Port 5A(SAS2), Port 5C(SAS2), Port 5E(SAS3), Port 5G(SAS3)..... 85
      - 3.6.4.3.2 Socket1 Configuration ..... 86
        - 3.6.4.3.2.1 Port 1A(PCIe Slot7), Port 2A(PCIe Slot5), Port 3A(PCIe Slot3), Port 5A(PCIe Slot1)..... 89
      - 3.6.4.3.3 Intel VT for Directed I/O (VT-d)..... 91
      - 3.6.4.3.4 Intel VMD technology ..... 91
        - 3.6.4.3.4.1 Intel VMD for Volume Management Device on Socket 0 ..... 92
        - 3.6.4.3.4.2 Intel VMD for Volume Management Device on Socket 1 ..... 92
    - 3.6.4.4 Advanced Power Management Configuration..... 93
      - 3.6.4.4.1 CPU P State Control ..... 93
      - 3.6.4.4.2 CPU C State Control..... 94
- 3.6.5 Server Mgmt ..... 95
  - 3.6.5.1 System Event Log..... 97
  - 3.6.5.2 Bmc self test log ..... 98
  - 3.6.5.3 BMC network configuration ..... 98
  - 3.6.5.4 BMC User Settings ..... 99
    - 3.6.5.4.1 BMC Add User Details ..... 99
    - 3.6.5.4.2 BMC Delete User Details ..... 100
    - 3.6.5.4.3 BMC Change User Settings..... 100
- 3.6.6 Security..... 101
  - 3.6.6.1 Secure Boot..... 101
- 3.6.7 Boot ..... 102
- 3.6.8 Save and exit..... 104
  - 3.6.8.1 Save Changes and Exit ..... 104
  - 3.6.8.2 Discard Changes and Exit ..... 104
  - 3.6.8.3 Save Changes and Reset..... 105
  - 3.6.8.4 Discard Changes and Reset..... 105
  - 3.6.8.5 Save Changes ..... 105
  - 3.6.8.6 Discard Changes ..... 105

## HPM-SRSDE User's Manual

3.6.8.7	Restore Defaults .....	105
3.6.8.8	Save as User Defaults .....	105
3.6.8.9	Restore User Defaults .....	105
<b>4.</b>	<b>Drivers Installation.....</b>	<b>106</b>
4.1	Install Chipset Driver .....	107
4.2	Install VGA Driver.....	108
4.3	Install Audio Driver .....	110
4.4	Install Ethernet Driver.....	111
4.5	Install QuickAssist Technology Driver .....	112
4.6	Install VROC Driver .....	113
<b>5.</b>	<b>Mechanical Drawing .....</b>	<b>115</b>



# 1. Getting Started

## 1.1 Safety Precautions

### Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

### Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

Risk of Explosion if Battery is replaced by an Incorrect Type. Dispose of Used Batteries According to the Instructions.

### Français:

### Attention!



Débranchez le câble d'alimentation de votre châssis chaque fois que vous travaillez avec le matériel. Ne faites pas de connexion lorsque le système est allumé. Les composants électroniques sensibles peuvent être endommagés par les surtensions soudaines. Seule les personnels expérimentés de l'électronique peuvent ouvrir le châssis du PC.

### Précaution!



Il faut toujours mettre à la masse pour éliminer l'électricité statique avant de toucher la carte CPU. Les appareils électroniques modernes sont très sensibles aux électricité statique. Pour des raisons de sécurité, utilisez un bracelet électrostatique. Placez tous les composants électroniques sur une surface antistatique ou dans un sac antistatique quand ils ne sont pas dans le châssis.

Risque d'explosion si la batterie est remplacée par un type incorrect. Jetez les piles usagées selon les instructions

### Warning!



Class I Equipment. This equipment must be earthed. The power plug must be connected to a properly wired earth ground socket outlet. An improperly wired socket outlet could place hazardous voltages on accessible metal parts.

### Warning!



#### IT Room

Suitable for installation in Information Technology Rooms in accordance with Article 645 of the National Electrical Code and NFPA 75.

### Warning!



#### RAL

The device can only be used in a fixed location such as a lab or a machine room. When you install the device, ensure that the protective earthing connection of the socket-outlet is verified by a skilled person.

### Warning!



#### For RTC battery, current statement in the manual is acceptable.

There is danger of explosion if the battery is mishandled or incorrectly replaced. Replace only with the same type of battery. Do not disassemble it or attempt to recharge it outside the system. Do not crush, puncture, dispose of in fire, short the external contacts, or expose to water or other liquids. Dispose of the battery in accordance with local regulations and instructions from your service provider.

## 1.2 Packing List

- 1 x HPM-SRSDE motherboard
- 1 x I/O Shield
- 2 x LGA4677 CPU carrier-E1B

### 1.3 Document Amendment History

Revision	Date	By	Comment
1 <sup>st</sup>	September 2023	Avalue	Initial Release

### 1.4 Manual Objectives

This manual describes in details Avalue Technology HPM-SRSDE Single Board.

We have tried to include as much information as possible but we have not duplicated information that is provided in the standard IBM Technical References, unless it proved to be necessary to aid in the understanding of this board.

We strongly recommend that you study this manual carefully before attempting to set up HPM-SRSDE or change the standard configurations. Whilst all the necessary information is available in this manual we would recommend that unless you are confident, you contact your supplier for guidance.

Please be aware that it is possible to create configurations within the CMOS RAM that make booting impossible. If this should happen, clear the CMOS settings, (see the description of the Jumper Settings for details).

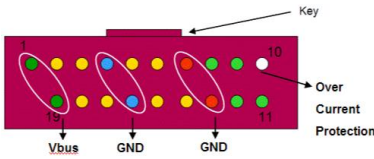
If you have any suggestions or find any errors regarding this manual and want to inform us of these, please contact our Customer Service department with the relevant details.

## 1.5 System Specifications

System																																									
<b>CPU</b>	Dual Intel LGA4677 Socket supports 4th Gen. Intel Xeon Scalable Processor (Max. TDP at 270W)																																								
<b>BIOS</b>	AMI UEFI BIOS																																								
<b>System Chipset</b>	Intel C741 Chipset (DMI x8)																																								
<b>System Memory</b>	<p>12 x DDR5 4800/ 4400 MHz RDIMM Up to 3TB</p> <p>Sapphire Rapids-SP Memory Support</p> <table border="1"> <thead> <tr> <th rowspan="2">Type</th> <th rowspan="2">Ranks Per DIMM and Data Width</th> <th rowspan="2">DIMM Capacity (GB)</th> <th colspan="2">Speed (MT/s) ; Voltage (V); DIMM Per Channel (DPC)</th> </tr> <tr> <th>1DPC*</th> <th>2DPC</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> <td>16 Gb</td> <td colspan="2">1.1V</td> </tr> <tr> <td rowspan="5">RDIMM</td> <td>SRx8 (RC D)</td> <td>16 GB</td> <td rowspan="5">4800</td> <td rowspan="5">4400</td> </tr> <tr> <td>SRx4 (RC C)</td> <td>32 GB</td> </tr> <tr> <td>SRx4 (RC F) 9x4</td> <td>32 GB</td> </tr> <tr> <td>DRx8 (RC E)</td> <td>32 GB</td> </tr> <tr> <td>DRx4 (RC A)</td> <td>64 GB</td> </tr> <tr> <td></td> <td>DRx4(RC B) 9x4</td> <td>64 GB</td> <td></td> <td></td> </tr> <tr> <td>RDIMM-3DS</td> <td>(4R/8R) x4 (RC A)</td> <td>2H- 128 GB 4H- 256 GB</td> <td></td> <td></td> </tr> <tr> <td>LRDIMM / LRDIMM-3DS</td> <td>NA</td> <td>NA</td> <td>Not Supported</td> <td>Not Supported</td> </tr> </tbody> </table> <p>*1DPC applies to 1 SPC or 2 SPC implementations (SPC – Sockets Per Channel). For additional information on memory population rules please check the Platform Design Guide Document - 610826</p>	Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)	Speed (MT/s) ; Voltage (V); DIMM Per Channel (DPC)		1DPC*	2DPC			16 Gb	1.1V		RDIMM	SRx8 (RC D)	16 GB	4800	4400	SRx4 (RC C)	32 GB	SRx4 (RC F) 9x4	32 GB	DRx8 (RC E)	32 GB	DRx4 (RC A)	64 GB		DRx4(RC B) 9x4	64 GB			RDIMM-3DS	(4R/8R) x4 (RC A)	2H- 128 GB 4H- 256 GB			LRDIMM / LRDIMM-3DS	NA	NA	Not Supported	Not Supported
Type	Ranks Per DIMM and Data Width				DIMM Capacity (GB)	Speed (MT/s) ; Voltage (V); DIMM Per Channel (DPC)																																			
		1DPC*	2DPC																																						
		16 Gb	1.1V																																						
RDIMM	SRx8 (RC D)	16 GB	4800	4400																																					
	SRx4 (RC C)	32 GB																																							
	SRx4 (RC F) 9x4	32 GB																																							
	DRx8 (RC E)	32 GB																																							
	DRx4 (RC A)	64 GB																																							
	DRx4(RC B) 9x4	64 GB																																							
RDIMM-3DS	(4R/8R) x4 (RC A)	2H- 128 GB 4H- 256 GB																																							
LRDIMM / LRDIMM-3DS	NA	NA	Not Supported	Not Supported																																					
<b>Watchdog Timer</b>	System reset event 0~6553 second.																																								
<b>H/W Status Monitor</b>	Temperature. Fan. Voltage. Case open. (1 x 2.5mm pitch Box Wafer, Pinrex 753-71-02TW07 or equivalent)																																								
<b>RAID</b>	Intel VMD and Virtual RAID on CPU(VROC) 1 x Intel VROC header																																								
<b>TPM</b>	TPM 2.0 NuvoTon NPCT750AADYX or equivalent TCM Nationz Z32H330TC or equivalent (Optional)																																								
<b>Other</b>	IPMI 2.0 with AST 2600 BMC controller onboard.																																								
<b>Expansion Slot</b>																																									
<b>PCIe</b>	7 x PCIe Gen5 x16 slots Slot 1, PCIe Gen5 x16 from 2nd CPU Slot 2, PCIe Gen5 x16 from 1st CPU Slot 3, PCIe Gen5 x16 from 2nd CPU Slot 4, PCIe Gen5 x16 from 1st CPU Slot 5, PCIe Gen5 x16 from 2nd CPU Slot 6, PCIe Gen5 x16 from 1st CPU																																								

## HPM-SRSDE User's Manual

	Slot 7, PCIe Gen5 x16 from 2nd CPU (This slot is closest to CPU socket)			
<b>Storage</b>				
<b>M.2</b>	1 x M.2 M-Key Slot to support 1 x PCIe 5.0 x4 NVMe SSD from 1st CPU 2242/2260/2280/22110 form factor			
<b>SATA</b>	5 x SATA III Supports up to 6.0 Gb/s 1 x Mini-SAS HD 4i (from PCH for 4 xSATA or 1 x4 NVMe interface) 1 x 7pin SATA connector			
<b>Other</b>	3 x Slim SAS 8i (SFF-8654) connector (from 1st CPU)			
<b>Edge I/O</b>				
<b>COM</b>	1 x DB-9 male connector (Connector : DB-9(male) and DB-15(female) dual port right angle)			
<b>LAN</b>	5 x RJ45 (Including MGMT, LAN1, 2, 3, and 4) MGMT port : Dedicated IPMI function access LAN 1 : 1GbE Ethernet port, LAN1 shared with IPMI function access (Connector : 1 x 1G Base-T RJ45 module jack over 2 x USB 3.1 Gen1 stacked receptacle) LAN 2 : 2.5GbE Ethernet port (Connector : 1 x 2.5G Base-T RJ45 module jack over 2 x USB 3.1 Gen1 stacked receptacle) LAN 3 and 4 : 2 x 10GbE Ethernet ports (Optional) (Connector : 1 x 2X1 10G Base-T RJ45 module jack)			
<b>USB 3.1</b>	4 x USB 3.1 type A ports (Connector : 1 x 1G Base-T RJ45 module jack over 2 x USB 3.1 Gen1 stacked receptacle) (Connector : 1 x 2.5G Base-T RJ45 module jack over 2 x USB 3.1 Gen1 stacked receptacle) 2 x USB 3.1 type A ports (Connector : USB 3.0 type A double stacked USB receptacle)			
<b>VGA</b>	1 x DB-15 female connector (Connector : DB-9(male) and DB-15(female) dual port right angle)			
<b>Onboard I/O</b>				
<b>COM</b>	1 x RS232 ports (1 x 2.0mm pitch Box Header) Pin definition: Follow Avalue standard.			
<b>USB 2.0</b>	2x USB 2.0 type A receptacle 2 x USB 2.0 ports (1 x USB 2.0 2.54mm pitch Box Header) Pin definition:			
	VCC	Pin 1	Pin 2	VCC
	USB0-	Pin 3	Pin 4	USB1-
	USB0+	Pin 5	Pin 6	USB1+

	GND	Pin 7	Pin 8	GND																																																												
	Key	Pin 9	Pin 10	No Connection																																																												
<b>USB 3.1</b>	2 x USB 3.1 Gen1 ports (1 x USB 3.1 Gen1 2.0mm pitch Box Header (Pinrex 52X-8020GB52 or equivalent))																																																															
	Pin definition :  <table border="1"> <thead> <tr> <th>Pin No.</th> <th>Signal</th> <th>Description</th> </tr> </thead> <tbody> <tr><td>1</td><td>Vbus</td><td>Power</td></tr> <tr><td>2</td><td>IntA_P1_SSRX-</td><td>USB3 ICC Port1 SuperSpeed Rx-</td></tr> <tr><td>3</td><td>IntA_P1_SSRX+</td><td>USB3 ICC Port1 SuperSpeed Rx+</td></tr> <tr><td>4</td><td>GND</td><td>GND</td></tr> <tr><td>5</td><td>IntA_P1_SSTX-</td><td>USB3 ICC Port1 SuperSpeed Tx-</td></tr> <tr><td>6</td><td>IntA_P1_SSTX+</td><td>USB3 ICC Port1 SuperSpeed Tx+</td></tr> <tr><td>7</td><td>GND</td><td>GND</td></tr> <tr><td>8</td><td>IntA_P1_D-</td><td>USB3 ICC Port1 D- (USB2 Signal D-)</td></tr> <tr><td>9</td><td>IntA_P1_D+</td><td>USB3 ICC Port1 D+ (USB2 Signal D+)</td></tr> <tr><td>10</td><td>ID</td><td>Over Current Protection</td></tr> <tr><td>11</td><td>IntA_P2_D+</td><td>USB3 ICC Port2 D+ (USB2 Signal D+)</td></tr> <tr><td>12</td><td>IntA_P2_D-</td><td>USB3 ICC Port2 D- (USB2 Signal D-)</td></tr> <tr><td>13</td><td>GND</td><td>GND</td></tr> <tr><td>14</td><td>IntA_P2_SSTX+</td><td>USB3 ICC Port2 SuperSpeed Tx+</td></tr> <tr><td>15</td><td>IntA_P2_SSTX-</td><td>USB3 ICC Port2 Super Speed Tx-</td></tr> <tr><td>16</td><td>GND</td><td>GND</td></tr> <tr><td>17</td><td>IntA_P2_SSRX+</td><td>USB3 ICC Port2 SuperSpeed Rx+</td></tr> <tr><td>18</td><td>IntA_P2_SSRX-</td><td>USB3 ICC Port2 SuperSpeed Rx-</td></tr> <tr><td>19</td><td>Vbus</td><td>Power</td></tr> </tbody> </table>					Pin No.	Signal	Description	1	Vbus	Power	2	IntA_P1_SSRX-	USB3 ICC Port1 SuperSpeed Rx-	3	IntA_P1_SSRX+	USB3 ICC Port1 SuperSpeed Rx+	4	GND	GND	5	IntA_P1_SSTX-	USB3 ICC Port1 SuperSpeed Tx-	6	IntA_P1_SSTX+	USB3 ICC Port1 SuperSpeed Tx+	7	GND	GND	8	IntA_P1_D-	USB3 ICC Port1 D- (USB2 Signal D-)	9	IntA_P1_D+	USB3 ICC Port1 D+ (USB2 Signal D+)	10	ID	Over Current Protection	11	IntA_P2_D+	USB3 ICC Port2 D+ (USB2 Signal D+)	12	IntA_P2_D-	USB3 ICC Port2 D- (USB2 Signal D-)	13	GND	GND	14	IntA_P2_SSTX+	USB3 ICC Port2 SuperSpeed Tx+	15	IntA_P2_SSTX-	USB3 ICC Port2 Super Speed Tx-	16	GND	GND	17	IntA_P2_SSRX+	USB3 ICC Port2 SuperSpeed Rx+	18	IntA_P2_SSRX-	USB3 ICC Port2 SuperSpeed Rx-	19	Vbus
Pin No.	Signal	Description																																																														
1	Vbus	Power																																																														
2	IntA_P1_SSRX-	USB3 ICC Port1 SuperSpeed Rx-																																																														
3	IntA_P1_SSRX+	USB3 ICC Port1 SuperSpeed Rx+																																																														
4	GND	GND																																																														
5	IntA_P1_SSTX-	USB3 ICC Port1 SuperSpeed Tx-																																																														
6	IntA_P1_SSTX+	USB3 ICC Port1 SuperSpeed Tx+																																																														
7	GND	GND																																																														
8	IntA_P1_D-	USB3 ICC Port1 D- (USB2 Signal D-)																																																														
9	IntA_P1_D+	USB3 ICC Port1 D+ (USB2 Signal D+)																																																														
10	ID	Over Current Protection																																																														
11	IntA_P2_D+	USB3 ICC Port2 D+ (USB2 Signal D+)																																																														
12	IntA_P2_D-	USB3 ICC Port2 D- (USB2 Signal D-)																																																														
13	GND	GND																																																														
14	IntA_P2_SSTX+	USB3 ICC Port2 SuperSpeed Tx+																																																														
15	IntA_P2_SSTX-	USB3 ICC Port2 Super Speed Tx-																																																														
16	GND	GND																																																														
17	IntA_P2_SSRX+	USB3 ICC Port2 SuperSpeed Rx+																																																														
18	IntA_P2_SSRX-	USB3 ICC Port2 SuperSpeed Rx-																																																														
19	Vbus	Power																																																														
<b>CPU/System FAN</b>	2 x 4 Pin CPU Fan Header (4 Pin PWM)																																																															
	6 x 4 Pin Chassis Fan Header (4 Pin PWM, 2 for front fans and 4 for rear fans)																																																															
<b>Buzzer</b>	1 x onboard buzzer																																																															
<b>Front Panel</b>	1 x front panel connector (2.54 mm Pitch)																																																															
	Pin	Function	Pin	Function																																																												
	1-3	HDD LED	2-4	POWER LED																																																												
	5-7	RESET BUTTON	6-8	POWER BUTTON																																																												
	9-11	STATUS LED	10-12	LAN1 ACT LED																																																												
	13-15	UID LED	14-16	STBY POWER LED																																																												
	17-19	UID BUTTON	18-20	LAN2-X ACT LED																																																												
	Notes: LAN2-X ACT LED, "X" means the max number of Ethernet ports.																																																															
<b>RTC Battery</b>	1 x Horizontal Socket Type CMOS Battery Holder with CR2450																																																															
<b>Clear CMOS</b>	1 x Clear CMOS header (1 x 2.0 mm pitch Header)																																																															

## HPM-SRSDE User's Manual

<b>Audio</b>	1 x Avalue HD audio interface (1 x 6x2 2.0mm pitch wafer connector)									
	Signal	Pin	Pin	Signal						
	ACZ_VCC3	1	2	GND						
	ACZ_SYNC	3	4	ACZ_BITCLK						
	ACZ_SDOUT	5	6	ACZ_SDIN0						
	ACZ_SDIN1	7	8	ACZ_RST#						
	ACZ_5VSB	9	10	GND-Chassis						
	GND	11	12	NC						
<b>Display</b>										
<b>Graphic Chipset</b>	1 x VGA port (DB15 on edge I/O) AST2600 BMC controller									
<b>Spec. &amp; Resolution</b>	1920 x 1200@60Hz 32bpp									
<b>Audio</b>										
<b>Audio Codec</b>	ALC888S through Avalue HD Audio daughter board.									
<b>Ethernet</b>										
<b>LAN Chipset</b>	1 x Intel I210AT 1 x Intel I226-LM 1 x Intel X550-AT2 (Optional)									
<b>LAN Spec.</b>	1 x 1G Base-T Ethernet Controller 1 x 2.5G Base-T Ethernet controller 1 x Dual 10G Base-T Ethernet controller (Optional)									
<b>Mechanical &amp; Environmental</b>										
<b>Power Requirement</b>	1 x Std. 24 pin ATX Connector									
	3 x 8 Pin SSI 12V Connectors									
	Connector current rating information.									
	<b>MAXIMUM CURRENT RATING(Amperes)</b> <b>Wire-to-Wire and Wire-to- Board</b>									
	<b>Brass</b>					<b>Phosphor Bronze</b>				
	Ckt.Size	2 & 3	4 - 6	7 - 10	12 - 24	Ckt.Size	2 & 3	4 - 6	7 - 10	12 - 24
	Wire					Wire				
	AWG #16	9	8	7	6	AWG #16	8	7	6	5
	AWG #18	9	8	7	6	AWG #18	8	7	6	5
	AWG #20	7	6	5	5	AWG #20	6	5	4	4
AWG #22	5	4	4	4	AWG #22	4	3	3	3	
AWG #24	4	3	3	3	AWG #24	3	2	2	2	
AWG #26	3	2	2	2	AWG #26	2	1	1	1	
AWG #28	2	1	1	1	AWG #28	1	1	1	1	
<b>ACPI</b>	Yes									
<b>Power Mode</b>	H/W: ATX power well design only BMC: AT (Default)									
<b>Operating Temp.</b>	0 °C to 50 °C to support up to 250W TDP CPU 0 °C to 40 °C to support up to 270W TDP CPU									



<b>Storage Temp.</b>	-40 °C to 85 °C
<b>Operating Humidity</b>	40°C 95% non-condensing
<b>Size (L x W)</b> (Please consult product engineers for the production feasibility if the size is larger than 410x360mm or smaller than 80x70mm)	Proprietary form factor 12" x 16.452" (304.8mm x 417.88mm) PCB thickness is 2.86mm
<b>Weight</b>	2.2KG
<b>Vibration Test</b>	<p>Follow Avalue standard test.</p> <p>Random Vibration Operation</p> <ol style="list-style-type: none"> <li>1 Test PSD : 0.00454G<sup>2</sup>/Hz , 1.5 Grms</li> <li>2 System condition : operation mode</li> <li>3 Test frequency : 5~500 Hz</li> <li>4 Test axis : X,Y and Z axis</li> <li>5 Test time : 30 minutes per each axis</li> <li>6 IEC60068-2-64 Test Fh</li> <li>6 Storage : mSATA</li> </ol> <p>Random vibration test (Non-operation)</p> <ol style="list-style-type: none"> <li>1 PSD: 0.00808G<sup>2</sup>/Hz , 2.0 Grms</li> <li>2 Non-Operation mode</li> <li>3 Test Frequency : 5-500Hz</li> <li>4 Test Axis : X,Y and Z axis</li> <li>5 30 min. per each axis</li> <li>6 IEC 60068-2-64 Test:Fh</li> </ol> <p>Package Vibration Test:</p> <ol style="list-style-type: none"> <li>1 Test PSD : 0.026G<sup>2</sup>/Hz , 2.16 Grms</li> <li>2 Test frequency : 5~500 Hz</li> <li>3 Test axis : X,Y and Z axis</li> <li>4 Test time : 30 minutes per each axis</li> <li>5 IEC 60068-2-64 Test Fh</li> </ol>
<b>Drop Test</b>	<p>Follow Avalue standard test.</p> <p>Reference ISTA 2A, Method : IEC-60068-2-32 Test:Ed</p> <p>Test Ea : Drop Test</p> <ol style="list-style-type: none"> <li>1 Test phase : One corner, three edges, six faces</li> <li>2 Test high : 96.5cm</li> <li>3 Package weight : 5Kg</li> <li>4 Test drawing</li> </ol>

## HPM-SRSDE User's Manual

<b>OS Information</b>	Windows : Windows 10 IoT Enterprise LTSC 2021. Windows 11 IoT Enterprise. Windows Server IoT 2019 with VT-d disabled. Windows Server IoT 2022. Linux : Ubuntu 20.04 LTS or later Red Hat Enterprise Linux (RHEL) 8.2 and later
-----------------------	---



**Note:** Specifications are subject to change without notice.

**\*Only Install CPU1**

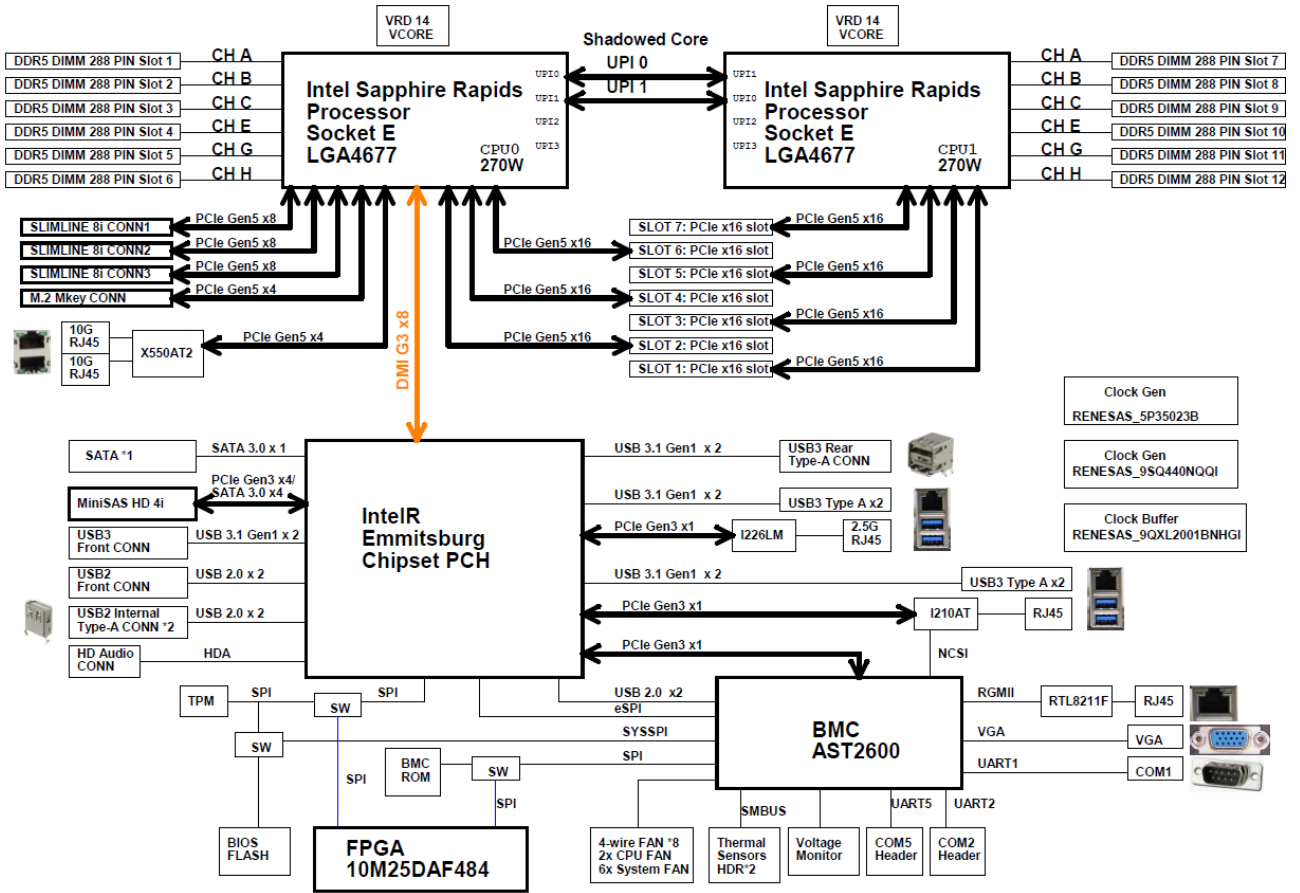
DIMM Qty	DIMM No.					
	DIMM1	DIMM2	DIMM3	DIMM4	DIMM5	DIMM6
1	V					
		V				
				V		
2	V				V	
			V	V		
4	V		V	V	V	
6	V	V	V	V	V	V

**\*Install CPU1 and CPU2**

DIMM Qty	DIMM No.											
	DIMM1	DIMM2	DIMM3	DIMM4	DIMM5	DIMM6	DIMM7	DIMM8	DIMM9	DIMM10	DIMM11	DIMM12
1	V											
		V										
				V								
2	V						V					
	V							V				
	V									V		
		V					V					
		V						V				
		V								V		
				V	V		V				V	
4	V				V		V				V	
	V				V				V	V		
			V	V			V			V	V	
			V	V					V	V		
6	V		V	V	V		V			V	V	
	V		V	V	V				V	V	V	
	V				V		V		V	V	V	
			V	V			V		V	V	V	
8	V		V	V	V		V		V	V	V	
	V	V	V	V	V	V	V				V	
	V	V	V	V	V	V			V	V		
	V				V		V	V	V	V	V	V
			V	V			V	V	V	V	V	V
10	V	V	V	V	V	V	V		V	V	V	
	V		V	V	V		V	V	V	V	V	V
12	V	V	V	V	V	V	V	V	V	V	V	V

## 1.6 Architecture Overview—Block Diagram

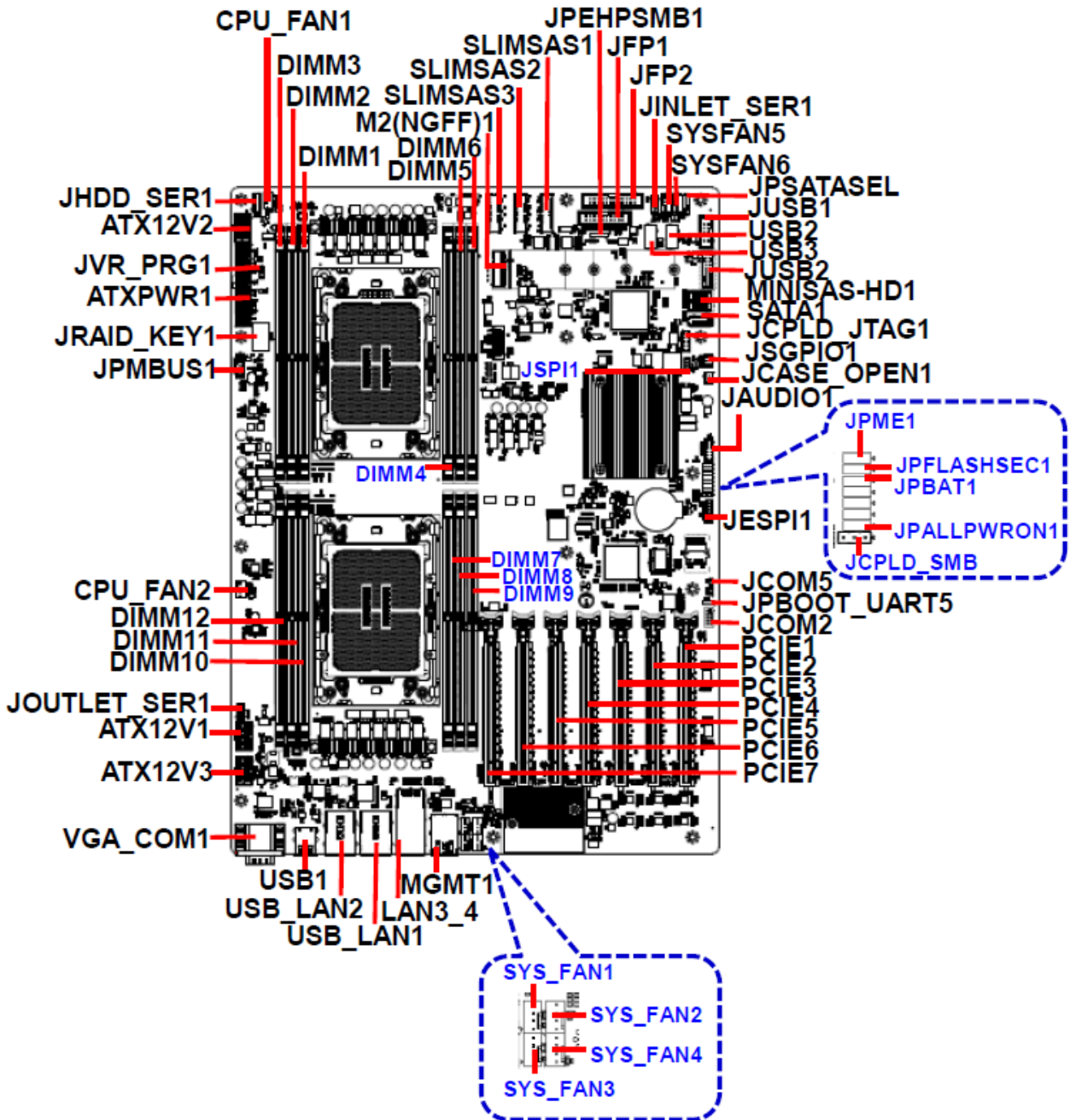
The following block diagram shows the architecture and main components of HPM-SRSDE.



# 2. Hardware Configuration

---

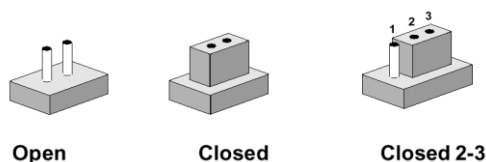
2.1 Product Overview



## 2.2 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board's jumpers and connectors.

### Jumpers

Label	Function	Note
<b>JPFLASHSEC1</b>	Flash Security Override	3 x 1 header, pitch 2.00mm
<b>JPME1</b>	ME FW update	3 x 1 header, pitch 2.00mm
<b>JPALLPWRON1</b>	Force PWRON setting	3 x 1 header, pitch 2.00mm
<b>JPBAT1</b>	Clear CMOS	3 x 1 header, pitch 2.00mm
<b>JPBOOT_UART5</b>	Boot UART5 setting	3 x 1 header, pitch 2.00mm
<b>JPSATASEL</b>	SATA or PCIE select	3 x 1 header, pitch 2.00mm

### Connectors

Label	Function	Note
<b>SYS_FAN1-6</b>	System fan connector 1-6	4 x 1 wafer, pitch 2.54mm
<b>CPU_FAN1-2</b>	CPU fan connector 1-2	4 x 1 wafer, pitch 2.54mm

## HPM-SRSDE User's Manual

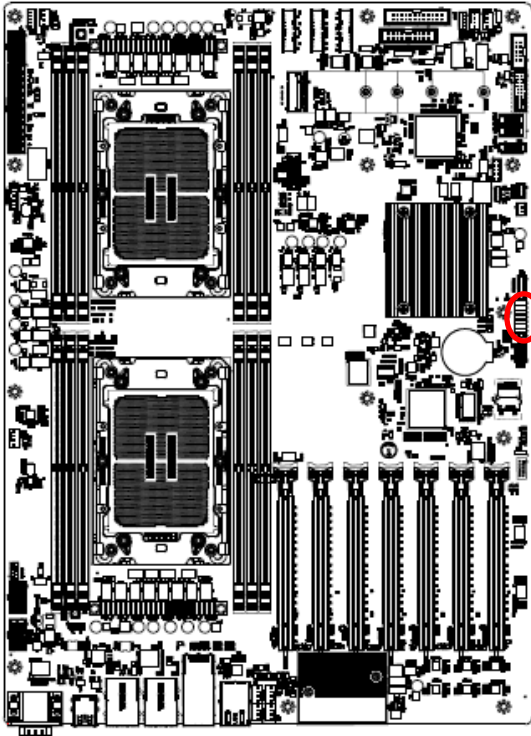
<b>VGA_COM1</b>	Serial port 1 connector VGA connector	
<b>JCOM2</b>	Serial port 2 connector	5 x 2 wafer, pitch 2.00mm
<b>JCOM5</b>	BMC_UART5 debug connector	4 x 1 header, pitch 2.54mm
<b>MGMT1</b>	MGMT port	
<b>JSGPIO1</b>	Serial General Purpose I/O connector	3 x 2 wafer, pitch 2.00mm
<b>PCIE1</b>	PCIe Gen5 x16	
<b>PCIE2</b>	PCIe Gen5 x16	
<b>PCIE3</b>	PCIe Gen5 x16	
<b>PCIE4</b>	PCIe Gen5 x16	
<b>PCIE5</b>	PCIe Gen5 x16	
<b>PCIE6</b>	PCIe Gen5 x16	
<b>PCIE7</b>	PCIe Gen5 x16 (The slot closest to CPU)	
<b>JFP1</b>	Front Panel connector 1	10 x 2 wafer, pitch 2.54mm
<b>JFP2</b>	Front Panel connector 2	12 x 2 wafer, pitch 2.54mm
<b>USB_LAN1</b>	2 x USB3.1 Gen1 connector 1 x RJ-45 Ethernet (LAN1 Share IPMI Port)	
<b>USB_LAN2</b>	2 x USB3.1 Gen1 connector 1 x RJ-45 Ethernet	
<b>LAN3_4</b>	2 x RJ-45 Ethernet	
<b>USB1</b>	2 x USB3.1 Gen1 connector	
<b>USB2/3</b>	2 x USB2.0 connector	
<b>JUSB1</b>	2 x USB2.0 connector	5 x 2 wafer, pitch 2.54mm
<b>JUSB2</b>	2 x USB3.1 Gen1 connector	10 x 2 wafer, pitch 2.00mm
<b>JSPI1</b>	SPI connector	4 x 2 header, pitch 2.00mm
<b>JESPI1</b>	ESPI connector	6 x 2 header, pitch 2.00mm
<b>SATA1</b>	Serial ATA connector	
<b>MINISAS-HD1</b>	Mini-SAS HD 4i (from PCH for 4 xSATA or 1 x4 NVMe interface)	
<b>SLIMSAS1-3</b>	3 x SlimSAS 8i connector for 6 x4 NVMe	
<b>JRAID_KEY1</b>	VROC Header	4 x 1 header, pitch 2.00mm
<b>DIMM1-12</b>	12 x DDR5 RDIMM socket	
<b>JVR_PRG1</b>	SMBUS VR connector	3 x 1 header, pitch 2.54mm



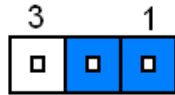
<b>JCASE_OPEN1</b>	CASE OPEN connector	2 x 1 wafer, pitch 2.50mm
<b>ATX12V1</b>	ATX 12V power connector 1	4 x 2 wafer, pitch 4.20mm
<b>ATX12V2</b>	ATX 12V power connector 2	4 x 2 wafer, pitch 4.20mm
<b>ATX12V3</b>	ATX 12V power connector 3	4 x 2 wafer, pitch 4.20mm
<b>ATXPWR1</b>	ATX power connector	12 x 2 wafer, pitch 4.20mm
<b>JPMBUS1</b>	Power supply PMBus connector	5 x 1 wafer, pitch 2.54mm
<b>JINLET_SER1</b>	Inlet Thermal Sensor	4 x 1 wafer, pitch 2.00mm
<b>JOUTLET_SER1</b>	Outlet Thermal Sensor	4 x 1 wafer, pitch 2.00mm
<b>JHDD_SER1</b>	HDD Backplane thermal Sensor	5 x 1 wafer, pitch 2.00mm
<b>JPEHPSMB1</b>	CPU PCIE HP SMB connector	5 x 1 header, pitch 2.00mm
<b>JAUDIO1</b>	AZALIA connector	6 x 2 header, pitch 2.00mm
<b>M2(NGFF)1</b>	M.2 M-Key PCIe 5.0 x4 NVMe SSD	
<b>JCPLD_JTAG1</b>	CPLD JTAG header	5 x 2 header, pitch 2.54mm

## 2.3 Setting Jumpers & Connectors

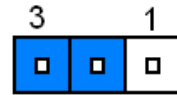
### 2.3.1 Flash Security Override (JPFLASHSEC)



Disable\*

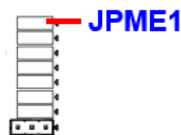
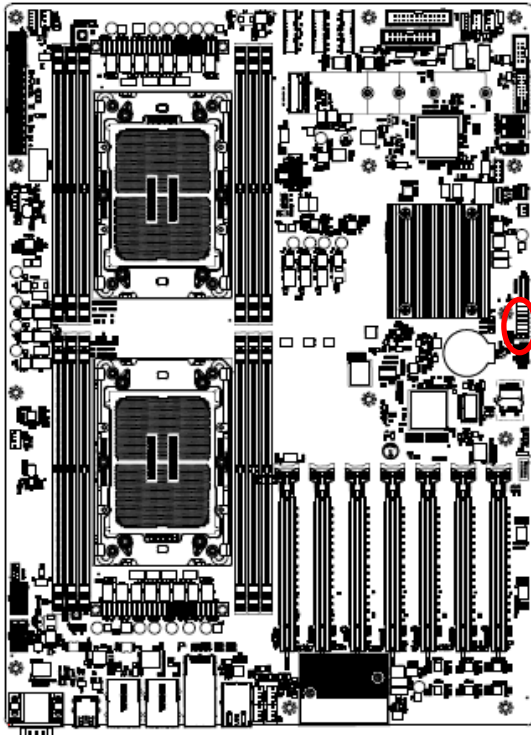


Enable

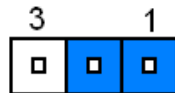


\* Default

### 2.3.2 ME FW update (JPME1)



Normal\*

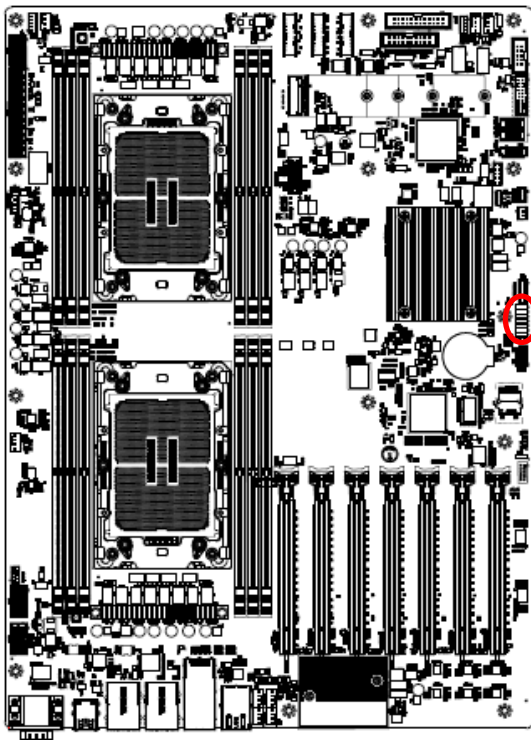


ME Force Update

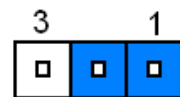


\* Default

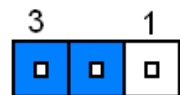
### 2.3.3 Force PWRON setting (JPALLPWRON1)



Normal Operation\*

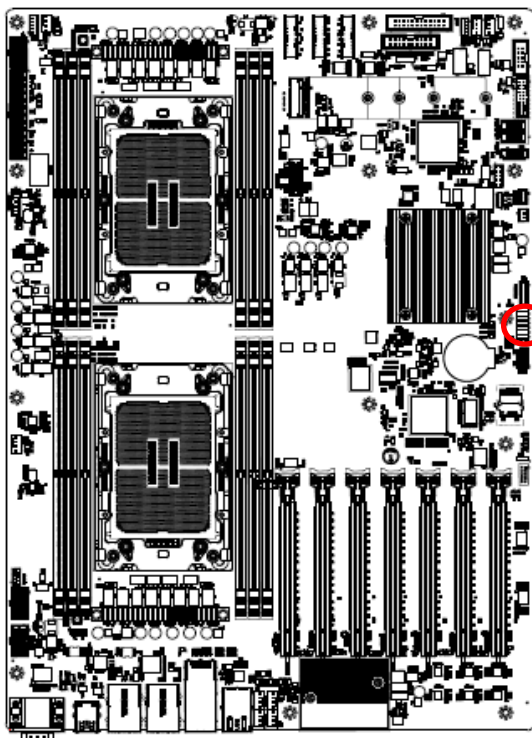


Enable Force PWR-ON

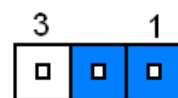


\* Default

### 2.3.4 Clear CMOS (JPBAT1)



Normal RTC Reset\*

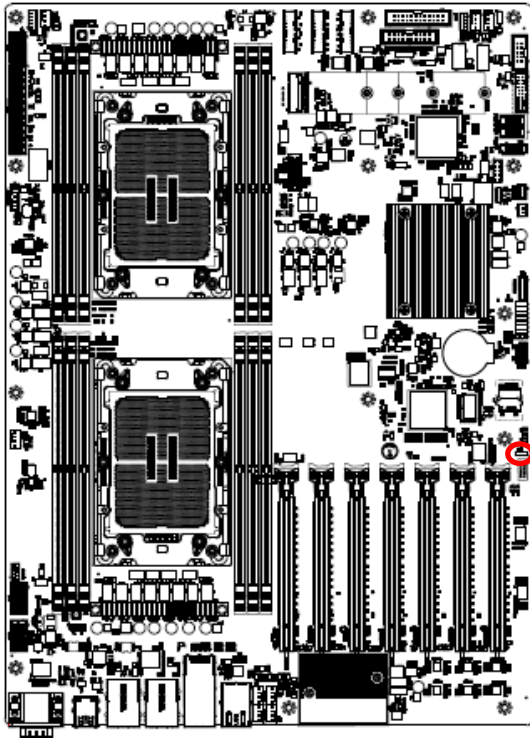


Clear RTC REGISTERS

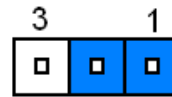


\* Default

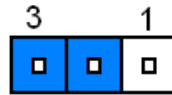
### 2.3.5 Boot UART5 setting (JPBOOT\_UART5)



Disable\*

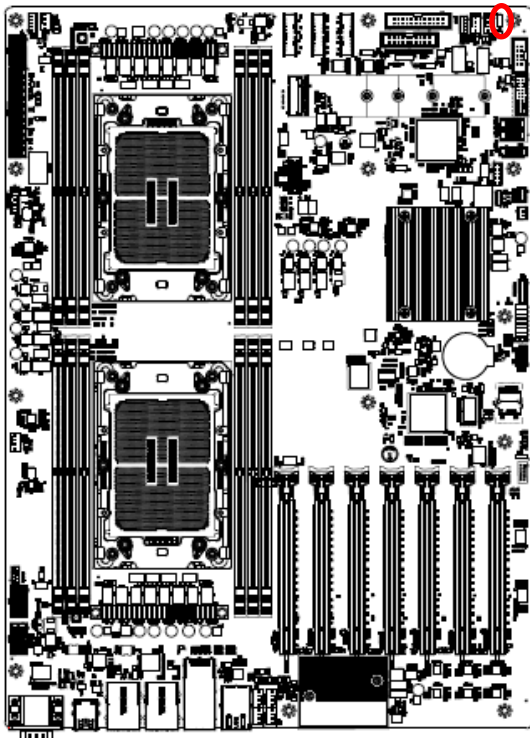


Enable BOOT FROM UART5

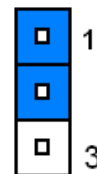


\* Default

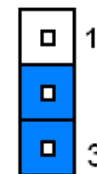
### 2.3.6 SATA or PCIE select (JPSATASEL)



PCIE\*

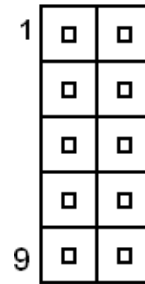
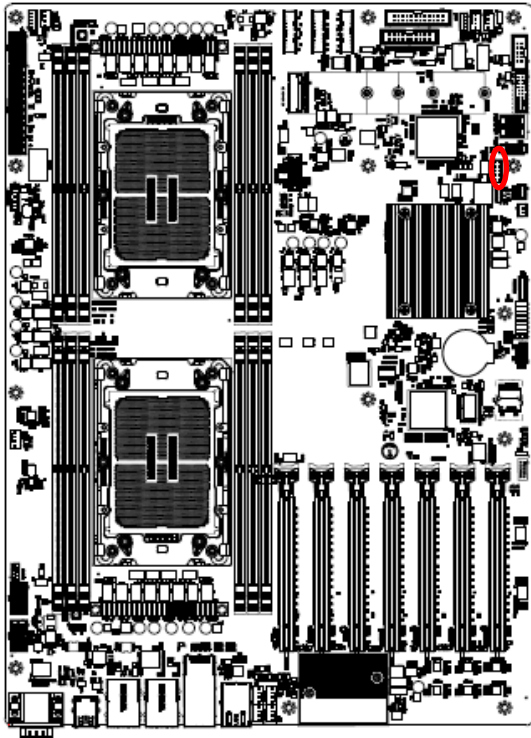


SATA



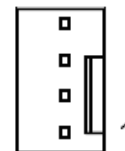
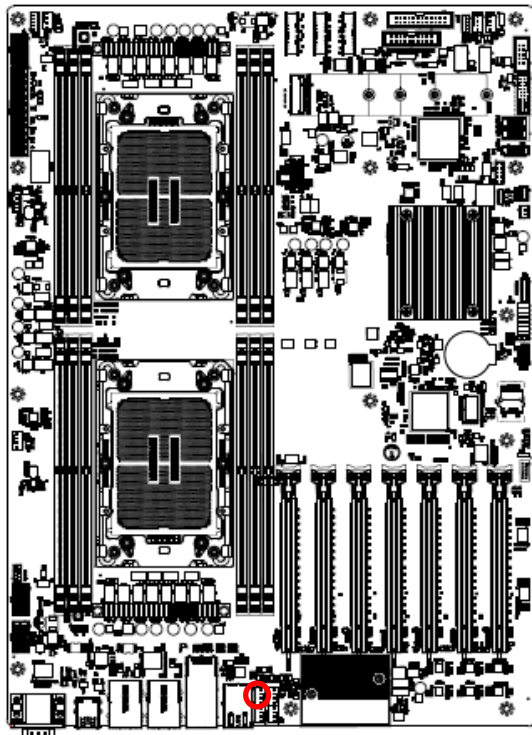
\* Default

### 2.3.7 CPLD JTAG header (JCPLD\_JTAG1)



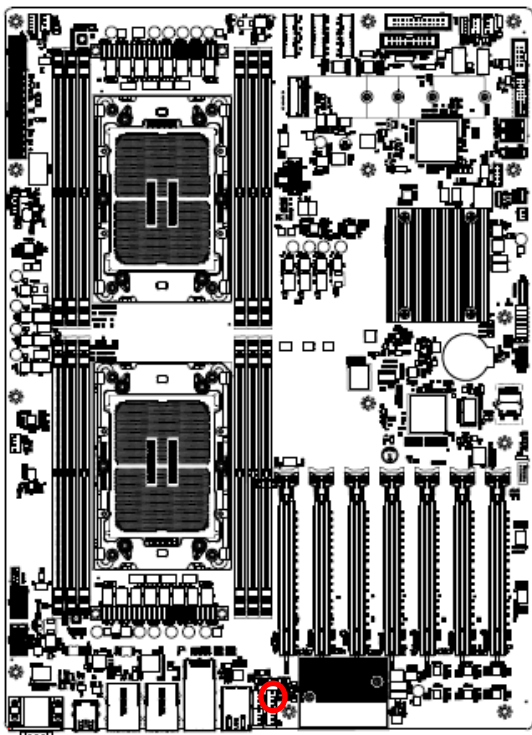
Signal	PIN	PIN	Signal
JTAG_TCK	1	2	GND
JTAG_TDO	3	4	+3.3VSB
JTAG_TMS	5	6	NC
NC	7	8	NC
JTAG_TDI	9	10	GND

### 2.3.8 System fan connector 1 (SYS\_FAN1)



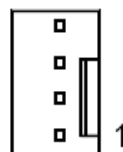
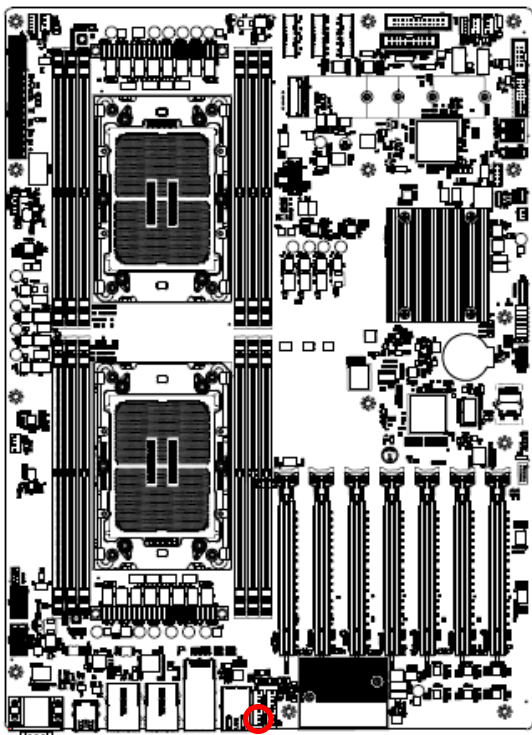
Signal	PIN
SYS_PWM1	4
FAN_TACH1	3
+12V	2
GND	1

2.3.9 System fan connector 2 (SYS\_FAN2)



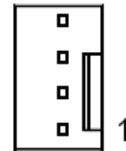
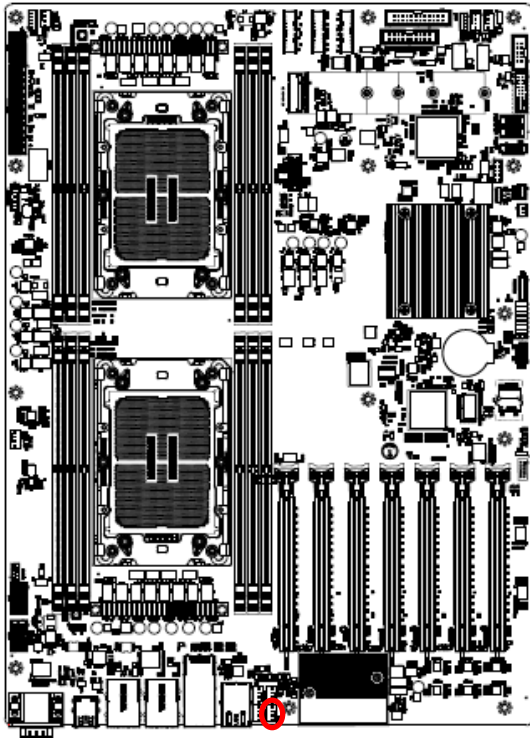
Signal	PIN
SYS_PWM2	4
FAN_TACH2	3
+12V	2
GND	1

2.3.10 System fan connector 3 (SYS\_FAN3)



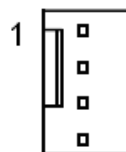
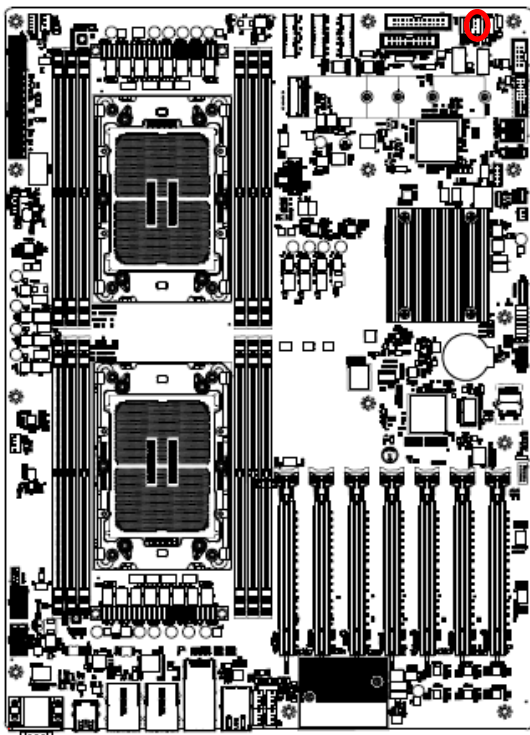
Signal	PIN
SYS_PWM3	4
FAN_TACH3	3
+12V	2
GND	1

### 2.3.11 System fan connector 4 (SYS\_FAN4)



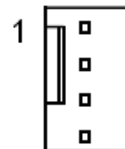
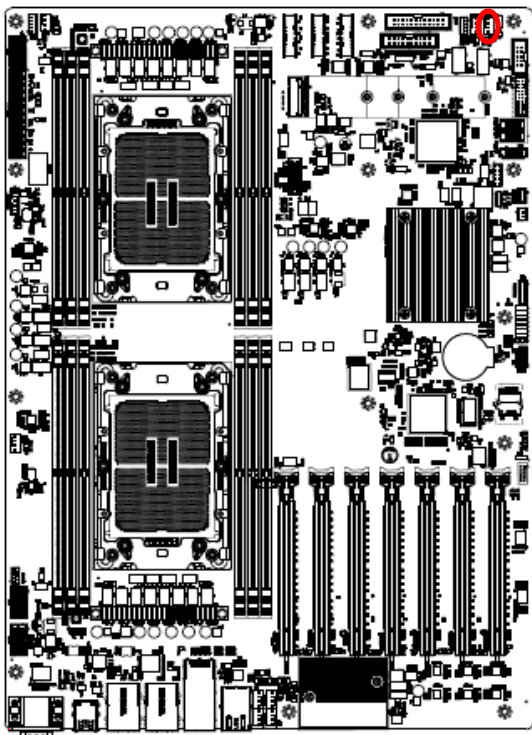
Signal	PIN
SYS_PWM4	4
FAN_TACH4	3
+12V	2
GND	1

### 2.3.12 System fan connector 5 (SYS\_FAN5)



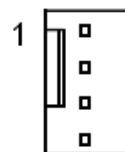
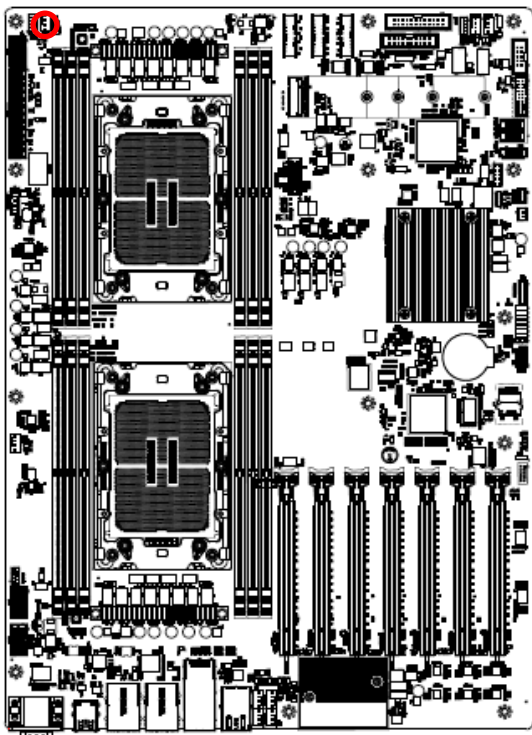
Signal	PIN
GND	1
+12V	2
FAN_TACH5	3
SYS_PWM5	4

2.3.13 System fan connector 6 (SYS\_FAN6)



Signal	PIN
GND	1
+12V	2
FAN_TACH6	3
SYS_PWM6	4

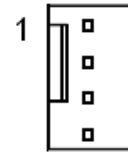
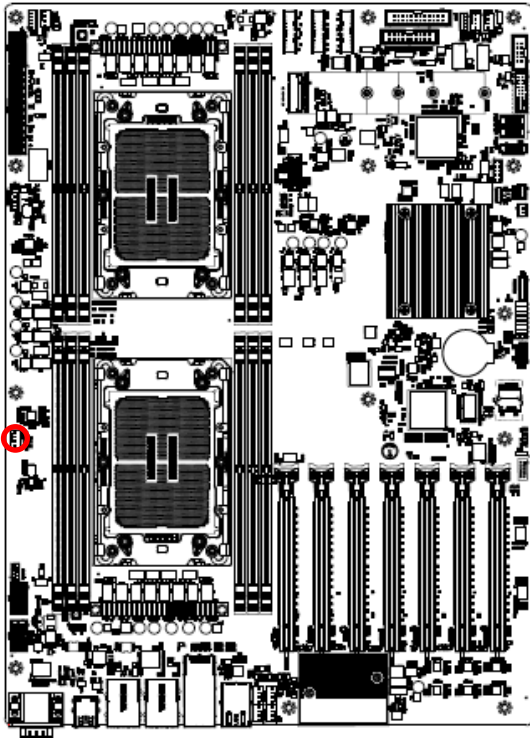
2.3.14 CPU fan connector 1 (CPU\_FAN1)



Signal	PIN
GND	1
+12V	2
FAN_TACH0	3
CPU0_PWM	4

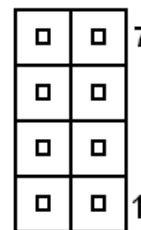
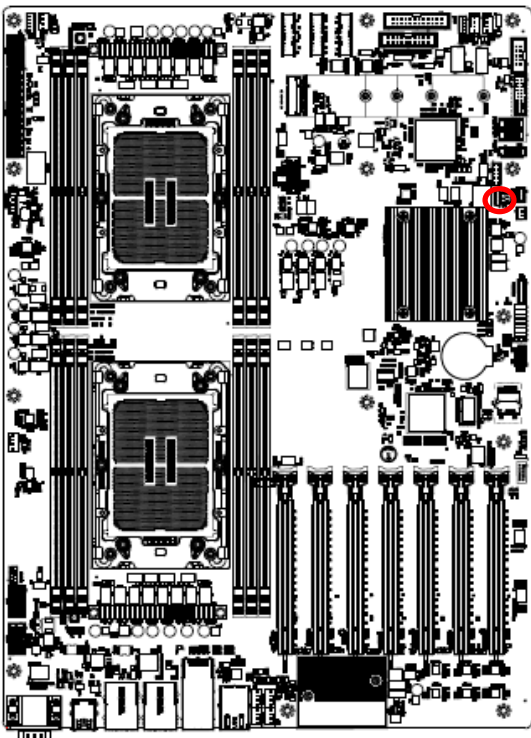


### 2.3.15 CPU fan connector 2 (CPU\_FAN2)



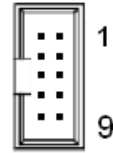
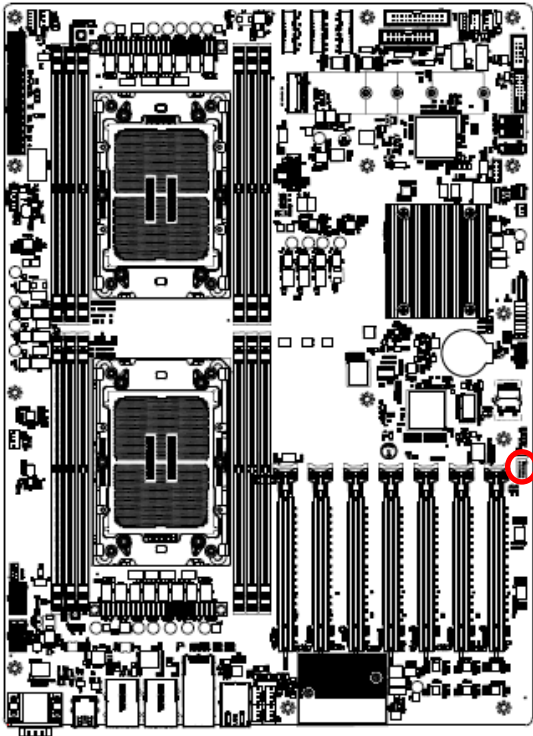
Signal	PIN
GND	1
+12V	2
FAN_TACH7	3
CPU1_PWM	4

### 2.3.16 SPI connector (JSPI1)



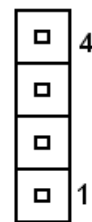
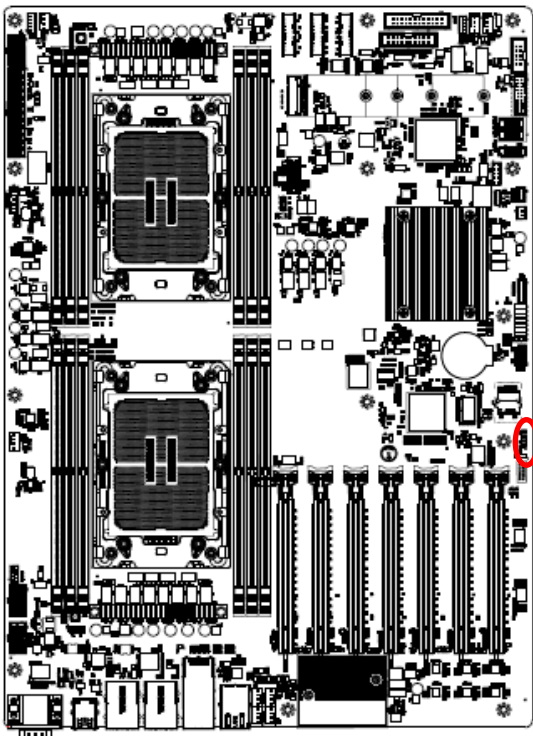
Signal	PIN	PIN	Signal
SPI_IO2	8	7	SPI_IO3
SPI_MOSI	6	5	SPI_MISO
SPI_CLK	4	3	SPI_CS#
GND	2	1	+3.3VSB

2.3.17 Serial port 2 connector (JCOM2)



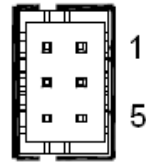
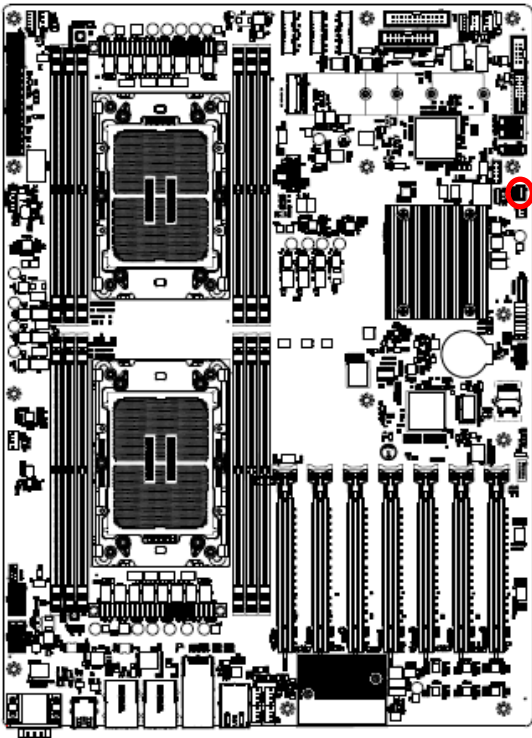
Signal	PIN	PIN	Signal
COM_RXD2	2	1	COM_DCD#2
COM_DTR#2	4	3	COM_TXD2
COM_DSR#2	6	5	GND
COM_CTS#2	8	7	COM_RTS#2
NC	10	9	COM_RI#2

2.3.18 BMC\_UART5 debug connector (JCOM5)



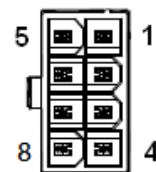
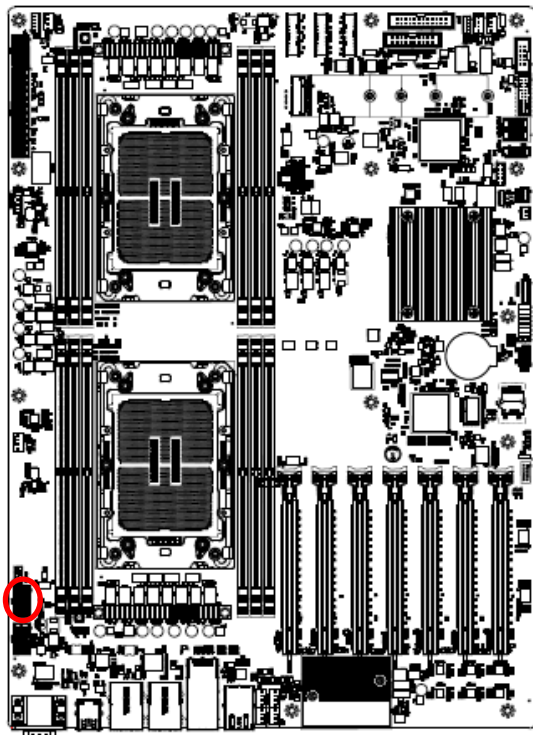
Signal	PIN
+3.3VSB	4
GND	3
UART5_RX	2
UART5_TX	1

### 2.3.19 Serial General Purpose I/O connector (JSGPIO1)



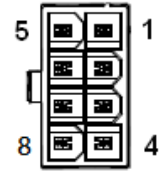
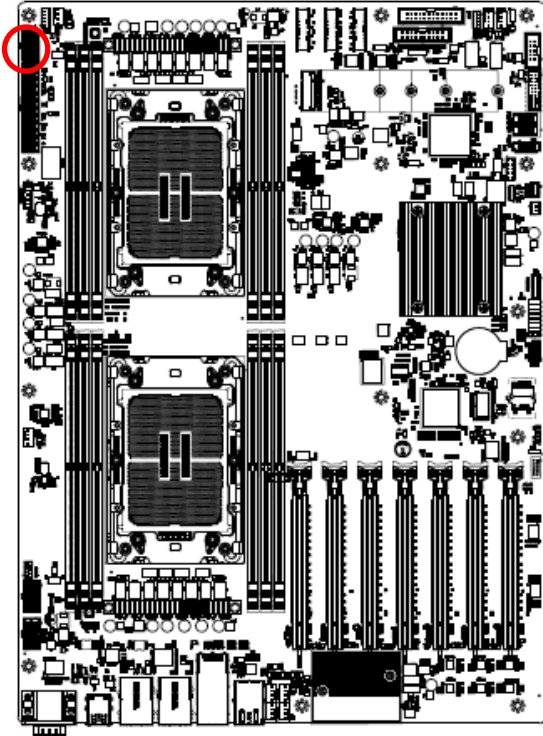
Signal	PIN	PIN	Signal
GND	2	1	GND
SGPIO_DATAOUT	4	3	SGPIO_LOAD
NC	6	5	SGPIO_CLOCK

### 2.3.20 ATX 12V power connector 1 (ATX12V1)



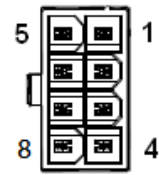
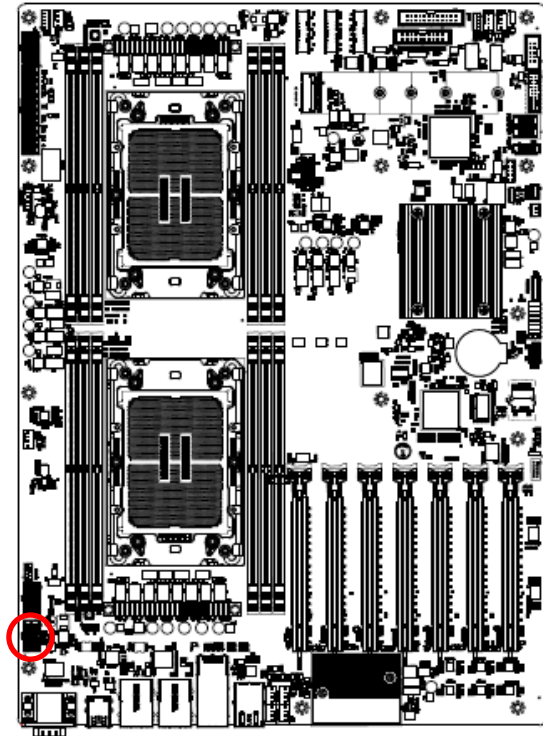
Signal	PIN	PIN	Signal
+12V	5	1	GND
+12V	6	2	GND
+12V	7	3	GND
+12V	8	4	GND

2.3.21 ATX 12V power connector 2 (ATX12V2)



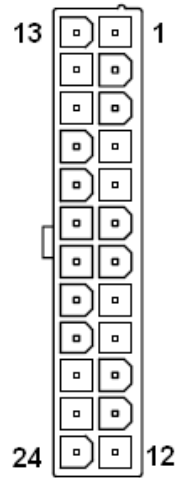
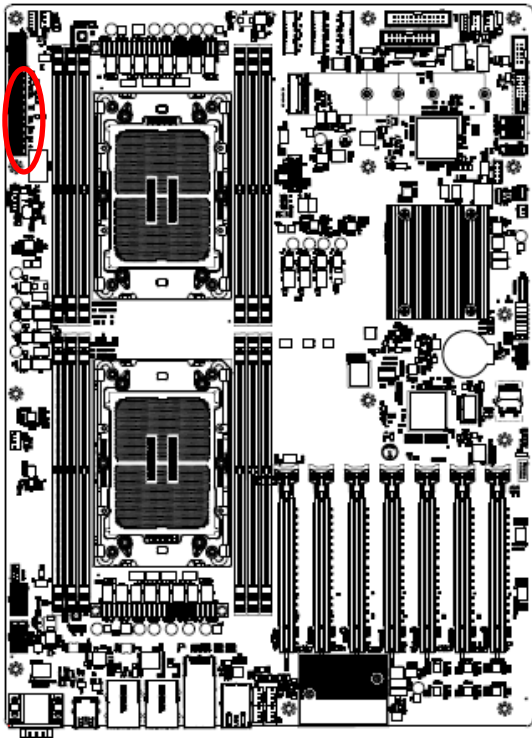
Signal	PIN	PIN	Signal
+12V	5	1	GND
+12V	6	2	GND
+12V	7	3	GND
+12V	8	4	GND

2.3.22 ATX 12V power connector 3 (ATX12V3)



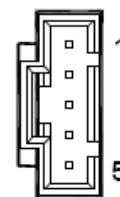
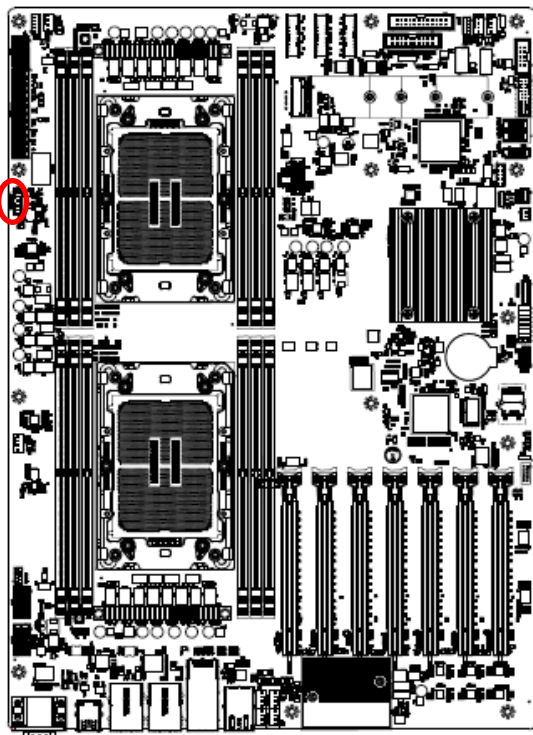
Signal	PIN	PIN	Signal
+12V	5	1	GND
+12V	6	2	GND
+12V	7	3	GND
+12V	8	4	GND

### 2.3.23 ATX power connector (ATXPWR1)



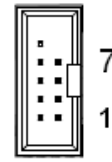
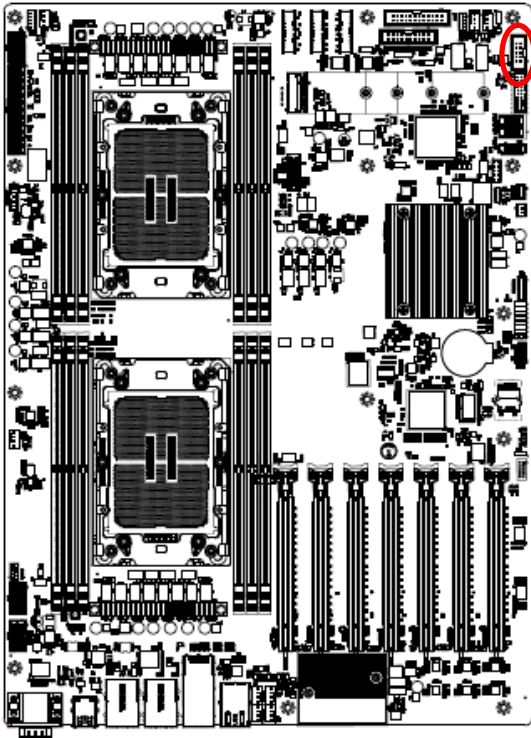
Signal	PIN	PIN	Signal
+3.3V	13	1	+3.3V
-12V	14	2	+3.3V
GND	15	3	GND
PSON#	16	4	+5V
GND	17	5	GND
GND	18	6	+5V
GND	19	7	GND
NC	20	8	PSU_PWRGD
+5V	21	9	+V5SB
+5V	22	10	+12V
+5V	23	11	+12V
GND	24	12	+3.3V

### 2.3.24 Power supply PMBus connector (JPMBUS1)



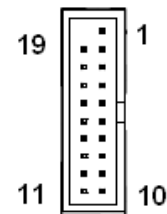
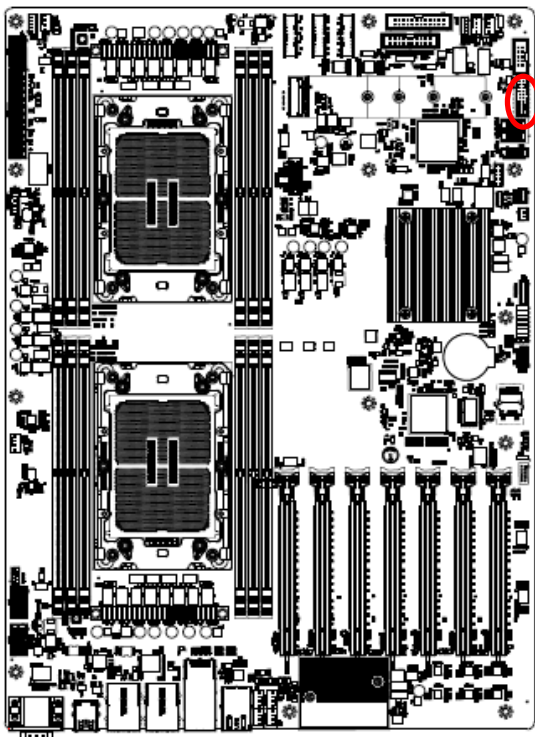
Signal	PIN
SMB_PSU_SCL	1
SMB_PSU_SDA	2
SMB_PSU_ALERT#	3
GND	4
NC	5

2.3.25 Front Panel USB2.0 connector (JUSB1)



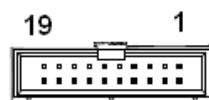
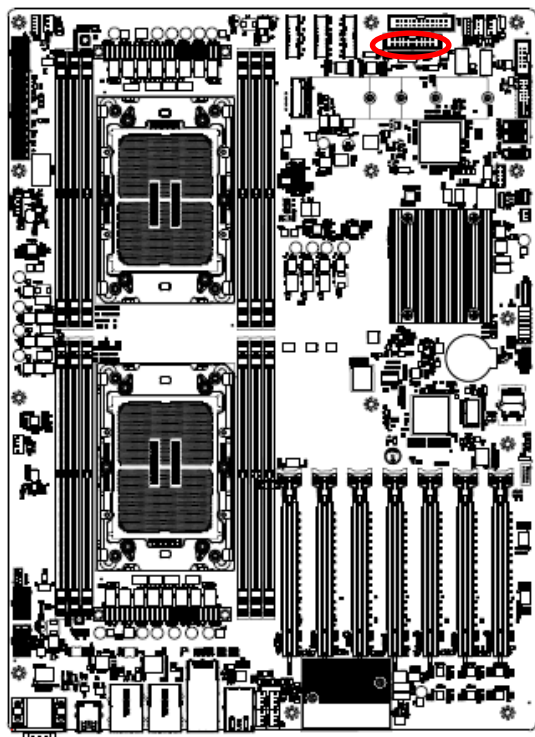
Signal	PIN	PIN	Signal
NC	10		
GND	8	7	GND
USB_PP8	6	5	USB_PP9
USB_PN8	4	3	USB_PN9
+5V	2	1	+5V

2.3.26 Front Panel USB3.1 connector (JUSB2)



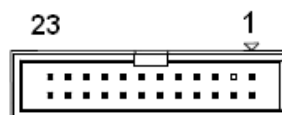
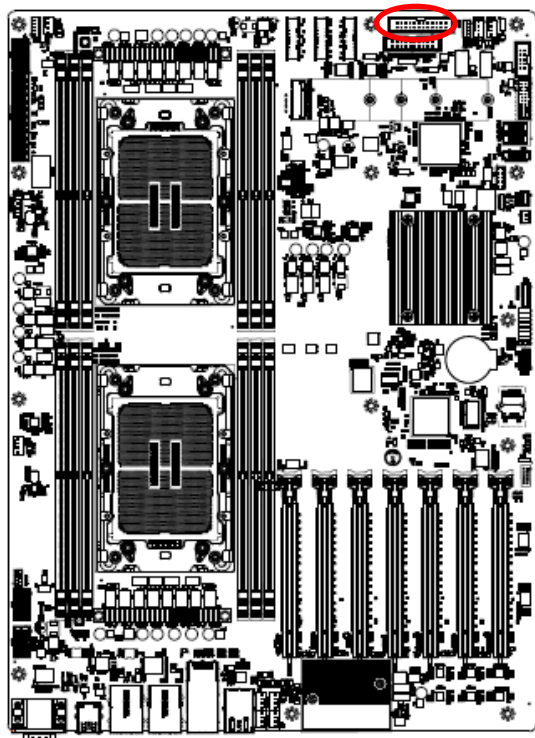
Signal	PIN	PIN	Signal
		1	+5V
+5V	19	2	USB3_RN6
USB3_RN7	18	3	USB3_RP6
USB3_RP7	17	4	GND
GND	16	5	USB3_TN6
USB_TN7	15	6	USB3_TP6
USB_TP7	14	7	GND
GND	13	8	USB3_PN11
USB_PN13	12	9	USB3_PP11
USB_PP13	11	10	USB_OC2#

### 2.3.27 Front Panel connector 1 (JFP1)



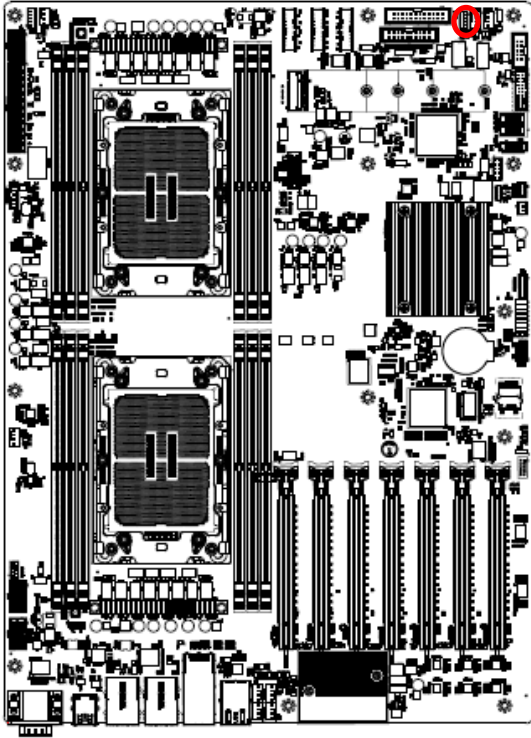
Signal	PIN	PIN	Signal
+3.3VSB	2	1	HDD_LED_P
PWR_LED#	4	3	HDD_LED#
PWRON_BUTTON#	6	5	RESET_BUTTON#
GND	8	7	GND
LAN1_LED_P	10	9	STATUS_LED_P
LAN1_LED#	12	11	STATUS_LED#
SBPWRLED_P	14	13	UID_LED#
GND	16	15	UID_LED_P
LAN2-X_LED_P	18	17	UID_BUTTON#
LAN2-X_LED#	20	19	GND

### 2.3.28 Front Panel connector 2 (JFP2)



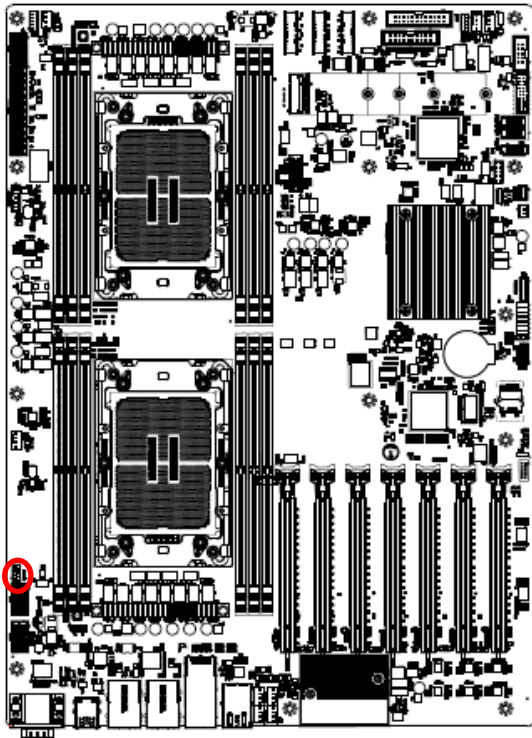
Signal	PIN	PIN	Signal
+3.3VSB	2	1	+3.3VSB
UID_LED_P	4		
UID_LED#	6	5	PWR_LED#
STATUS_LED#	8	7	HDD_LED_P
STATUS_LED_P	10	9	HDD_LED#
LAN1_LED_P	12	11	PWRON_BUTTON#
LAN1_LED#	14	13	GND
SMBus_SDA	16	15	RESET_BUTTON#
SMBus_SCL	18	17	GND
INTRUSION#	20	19	UID_BUTTON#
LAN2-X_LED_P	22	21	NC
LAN2-X_LED#	24	23	NMI_BUTTON#

2.3.29 Inlet Thermal Sensor (JINLET\_SER1)



Signal	PIN
+3.3VSB	1
SMB_INLET_TEMPSENSOR_SDA	2
SMB_INLET_TEMPSENSOR_SCL	3
GND	4

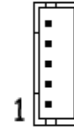
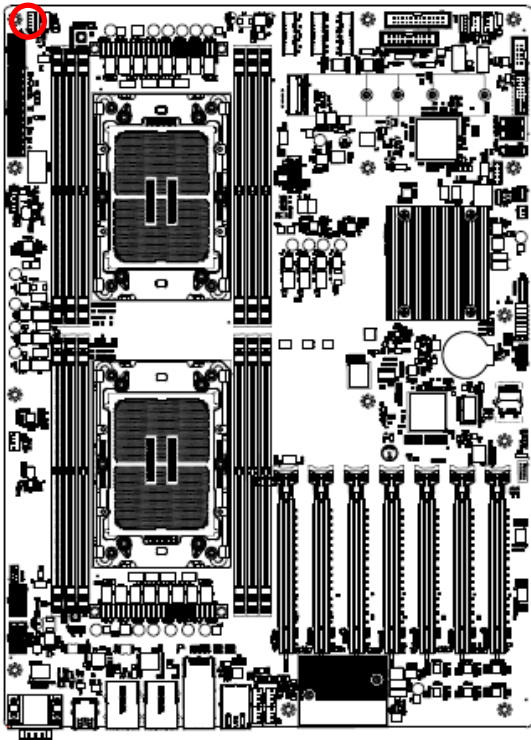
2.3.30 Outlet Thermal Sensor (JOUTLET\_SER1)



Signal	PIN
GND	4
SMB_OUTLET_TEMPSENSOR_SCL	3
SMB_OUTLET_TEMPSENSOR_SDA	2
+3.3VSB	1

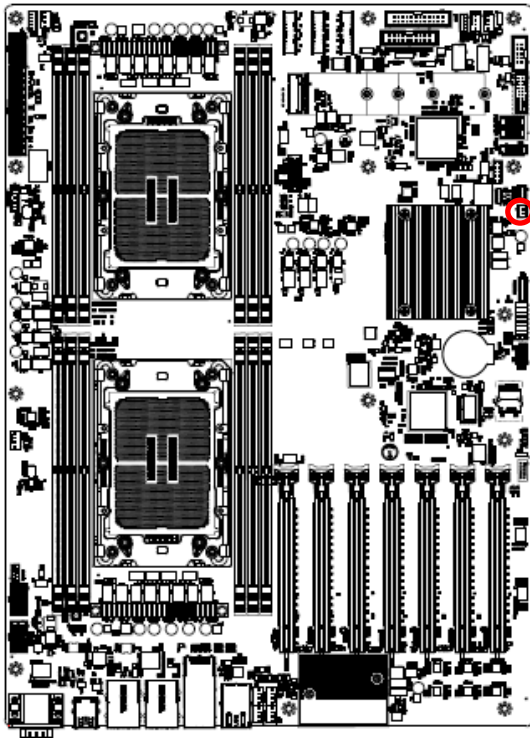


### 2.3.31 HDD Backplane thermal Sensor (JHDD\_SER1)



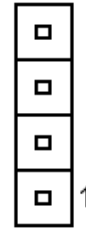
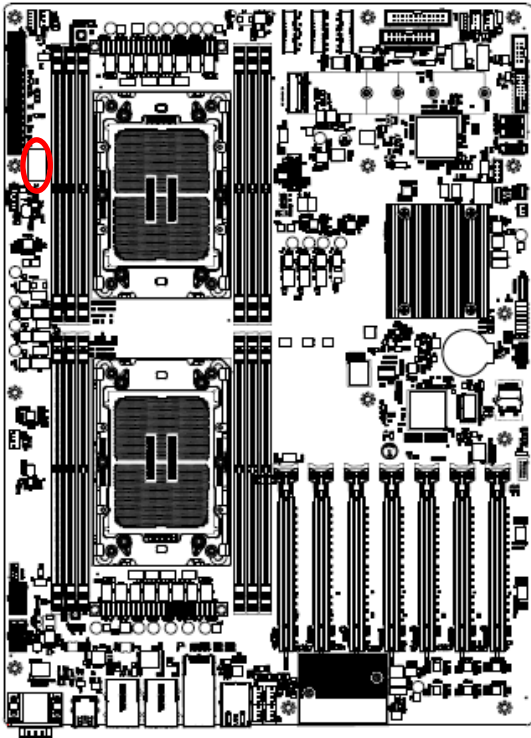
Signal	PIN
SSD_LED_N	5
GND	4
SMB_HDBP_TEMPSENSOR_SCL	3
SMB_HDBP_TEMPSENSOR_SDA	2
+3.3VSB	1

### 2.3.32 CASE OPEN connector (JCASE\_OPEN1)



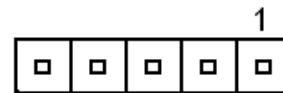
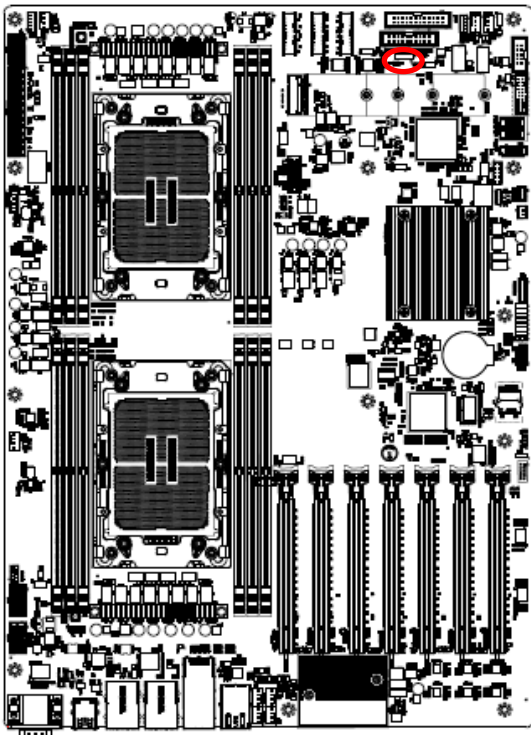
Signal	PIN
CHASSIS_INTRUSION	1
GND	2

2.3.33 VROC Header (JRAID\_KEY1)



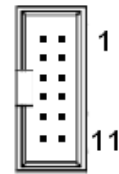
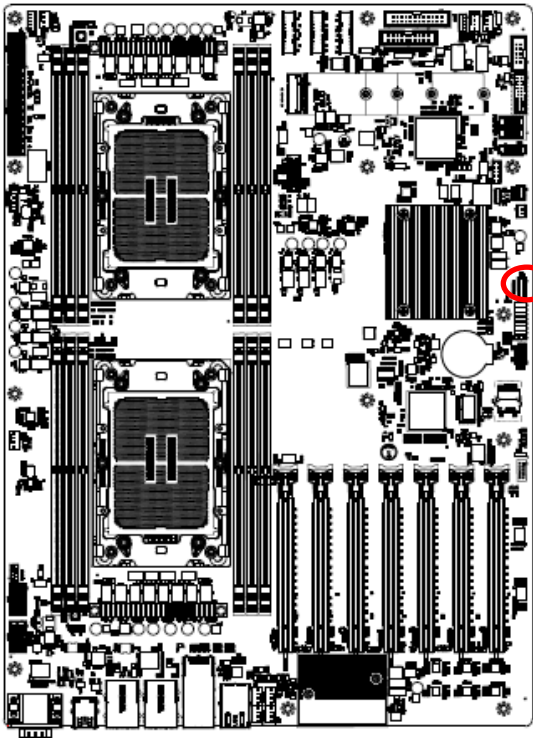
Signal	PIN
PCH_SATA_RAIDKEY	4
GND	3
PU_LEY_CONN	2
GND	1

2.3.34 CPU PCIE HP SMB connector (JPEHPMB1)



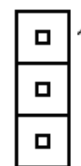
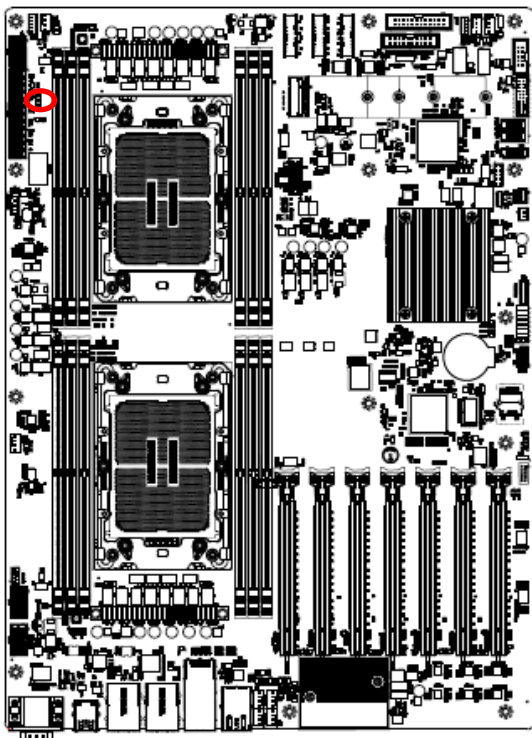
Signal	PIN
SMB_CPUHP_SCL	1
GND	2
SMB_CPUHP_SDA	3
GND	4
SMB_CPUHP_ALERT#	5

2.3.35 AZALIA connector (JAUDIO1)



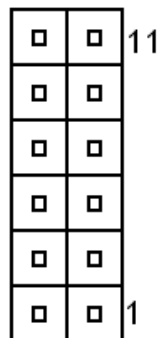
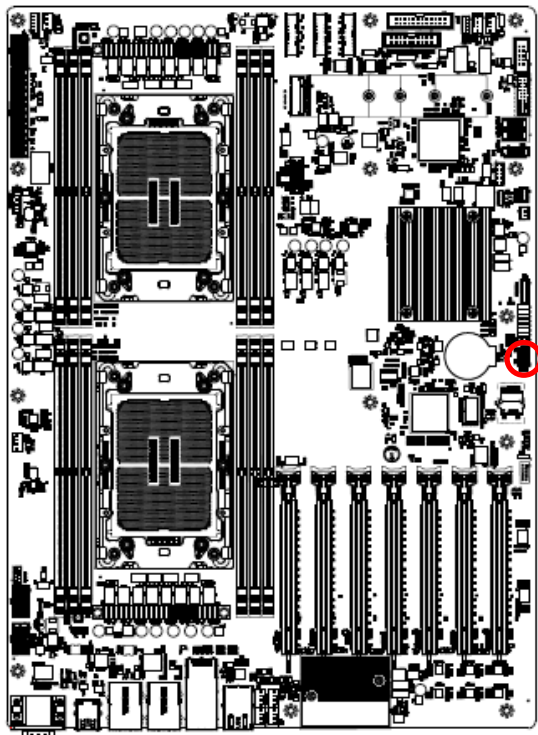
Signal	PIN	PIN	Signal
GND	2	1	+3.3V
AUD_AZA_BCLK	4	3	AUD_AZA_SYNC
AUD_AZA_SDI0	6	5	AUD_AZA_SDO
AUD_AZA_RST#	8	7	AUD_AZA_SDI1
GND	10	9	+5VSB
NC	12	11	GND

2.3.36 SMBUS VR connector (JVR\_PRG1)



Signal	PIN
SMB_VR_SDA	1
GND	2
SMB_VR_SCL	3

2.3.37 ESPI connector (JESPI1)



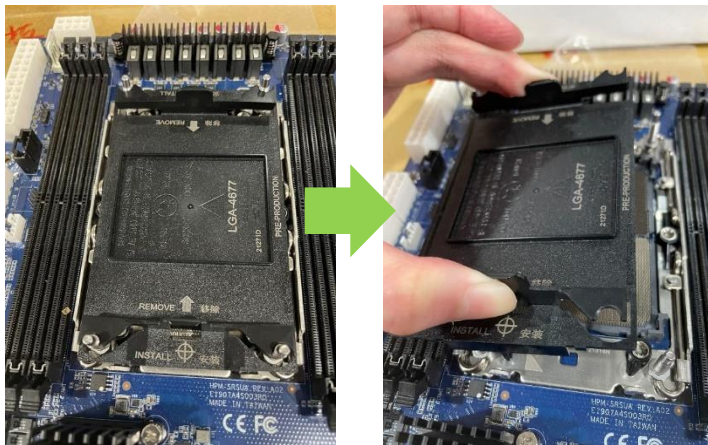
Signal	PIN	PIN	Signal
ESPI_ALERT#	12	11	ESPI_RESET#
GND	10	9	NC
ESPI_CLK	8	7	ESPI_D3
ESPI_CS#	6	5	ESPI_D2
PLTRST#	4	3	ESPI_D1
+3.3VSB	2	1	ESPI_D0

## 2.4 Processor Installation SOP

### Overview of the Processor Assembly installation procedure

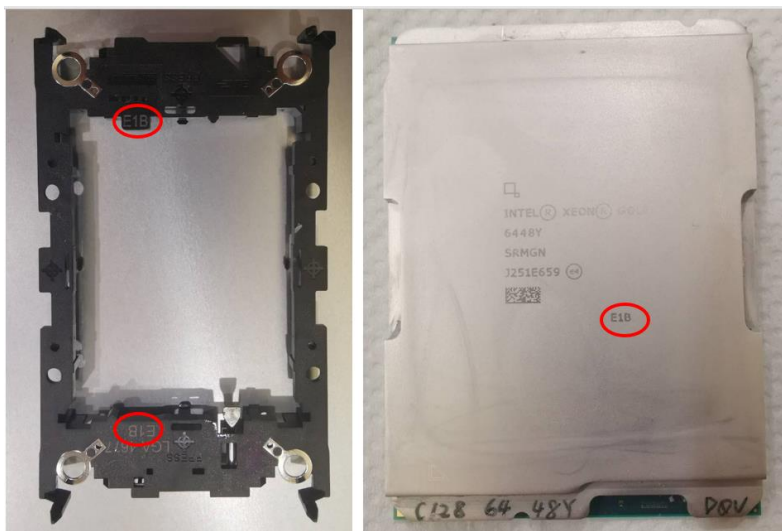
**Note:** Suggest installing the memory first, then installing the CPU cooler module to lower the memory installation difficulty.

1. The CPU socket is protected by a plastic protective cover.
  - a. Hold finger grips on socket cover and squeeze in on the grip tabs.
  - b. Then pull the cover up and off vertically to remove.



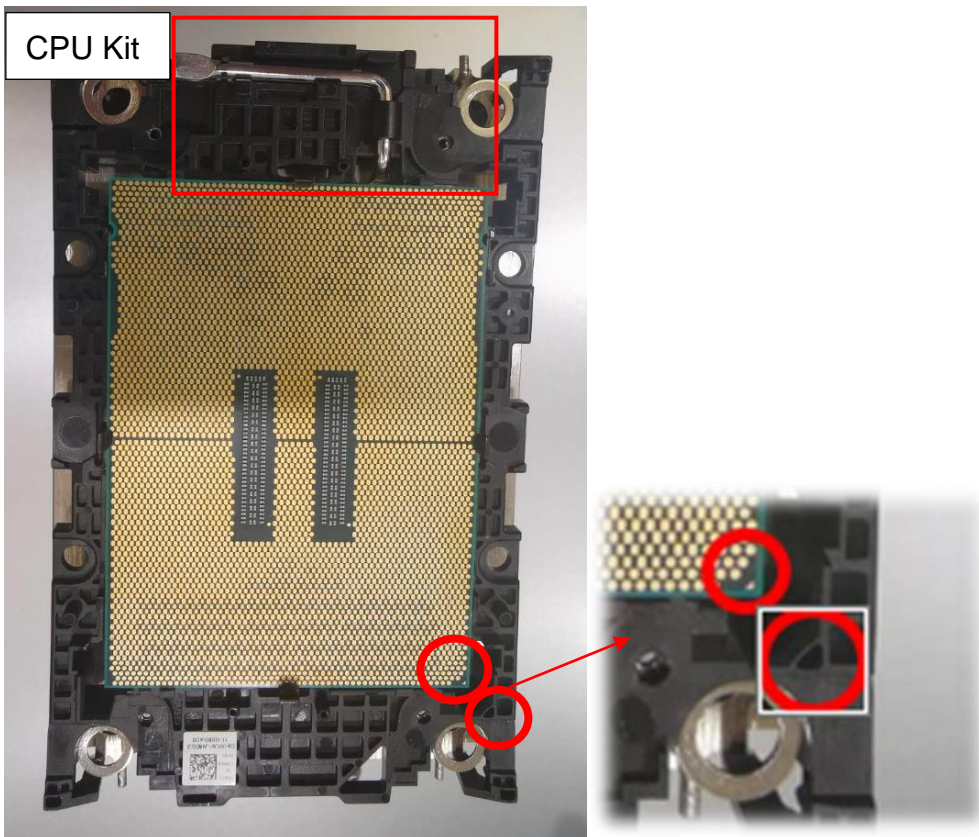
2. The processor assembly contains the Intel Xeon SP with carrier and CPU cooler.
  - 1x Intel 4<sup>th</sup> Xeon SP (MCC SKU)
  - 1x E1B CPU Carrier (In the HPM-SRSDE package)
  - 1x Cooler module (Avalue P/N:BCC-FAN-467-01R)

Please ensure the carrier model on the CPU is consistent with the carrier silkscreen.

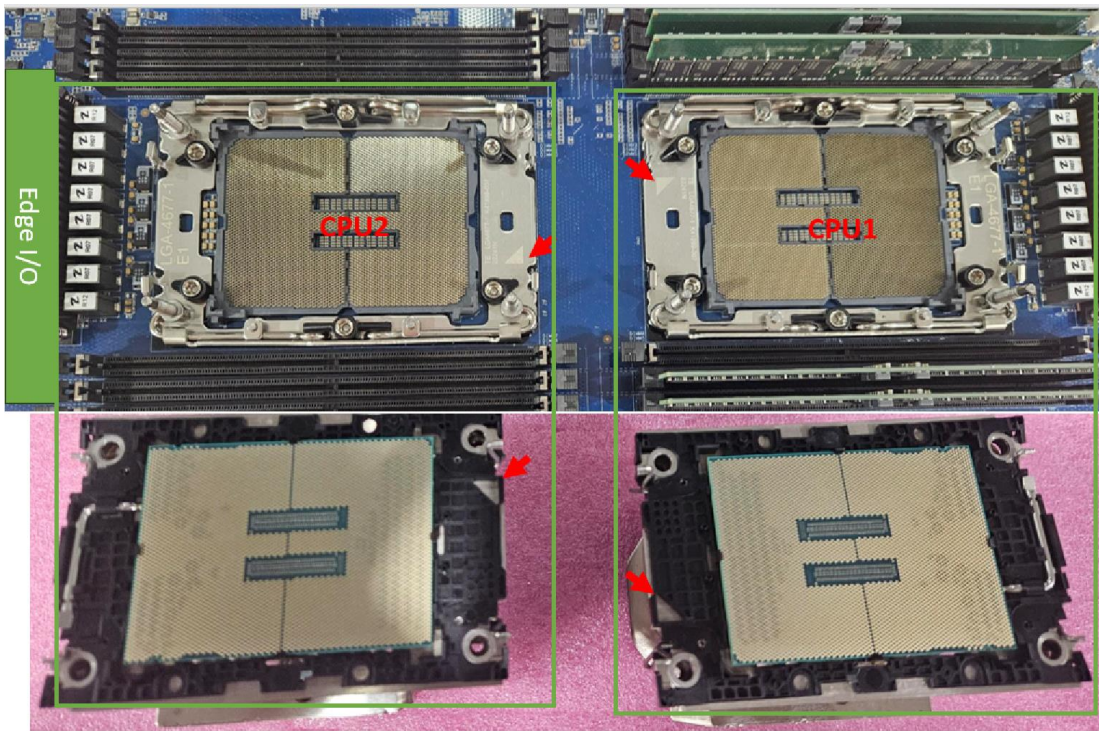
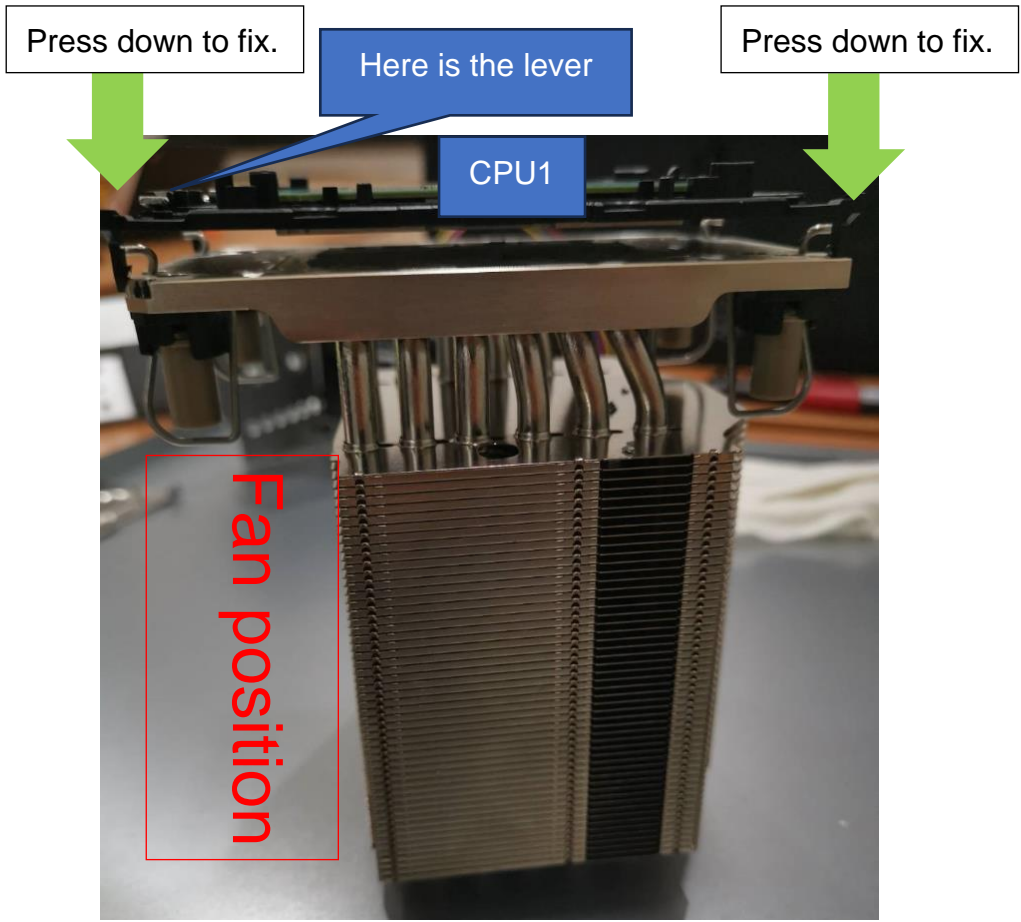


## HPM-SRSDE User's Manual

3. Install the CPU on the carrier and align the triangle marks (Pin 1).  
Look at the below red frame, please make sure the lever is pressed down.



4. Install the CPU kit assembly on the cooler module, please press down the CPU kit to fixate it.  
CPU1: Make sure the lever on the carrier is on the same side as the fan.  
CPU2: Make sure the lever on the carrier is on a different side than the fan.  
(Only applicable to HPM-SRSDE and Avalue Cooler BCC-FAN-467-01R.)  
Note: The Thermal grease must be pre-applied on the heatsink before installation.  
Note: Please ensure the direction of the fan before installing the CPU kit on the Cooler module.

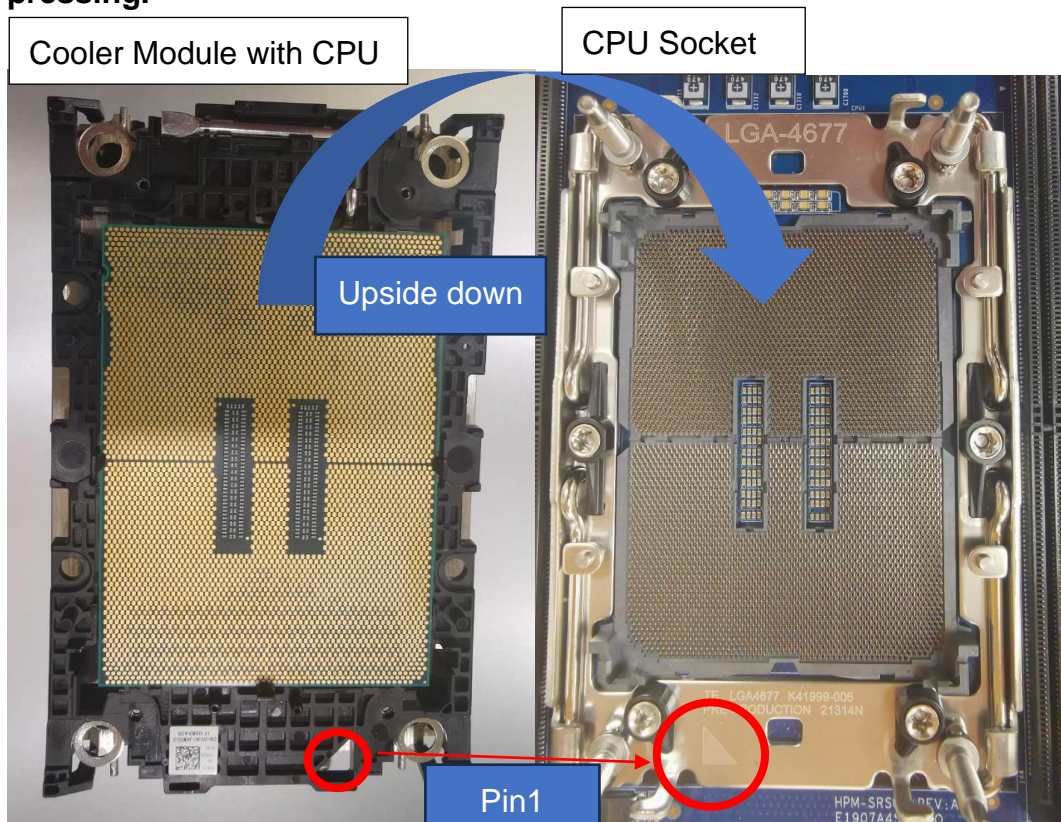


## HPM-SRSDE User's Manual

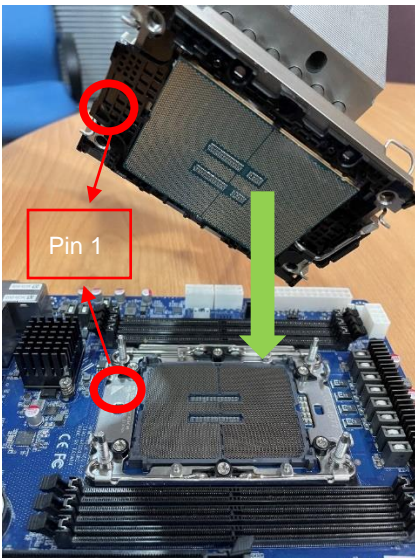
### 5. Cooler module with CPU kit installed on the motherboard.

- a. Please align the triangle mark between the Cooler module and CPU socket and install it. (Figure A)
- b. Hold the Cooler module with the CPU and align the holes with the CPU socket. Press the Cooler module down to the CPU socket until it snaps into place.
- c. Press down the fixing tenons on the four sides to fixate. (Figure B)
- d. With a T30 screwdriver, gradually tighten the four screws to ensure even pressure. (Figure C)

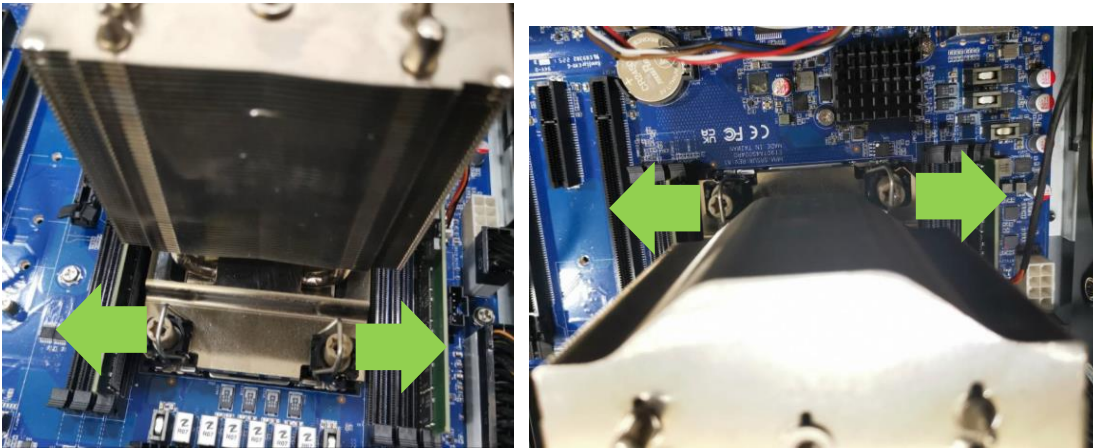
★ The cooler module with CPU pin1 must be aligned with the CPU socket pin1 mark, and the direction cannot be changed at will, or it may cause the CPU to damage after pressing.



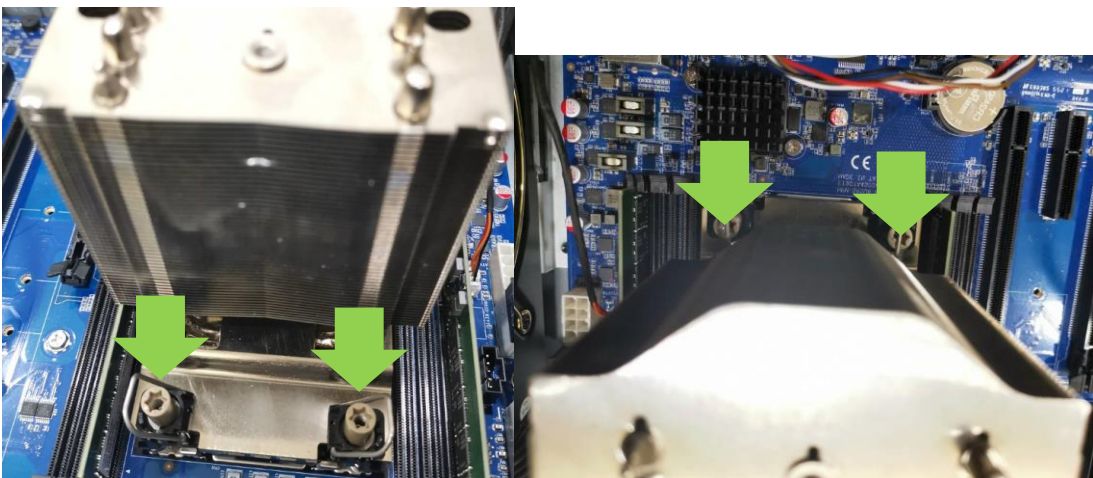




▲ Figure A

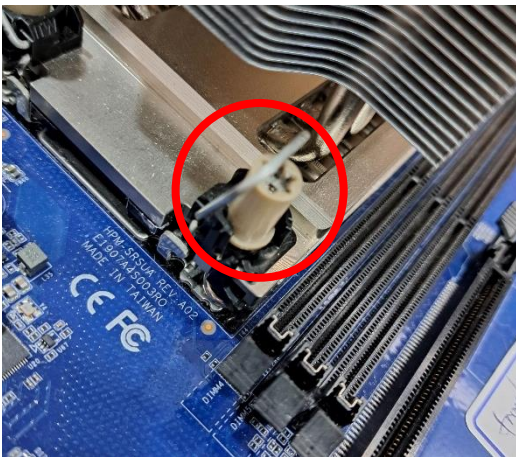


▲ Figure B

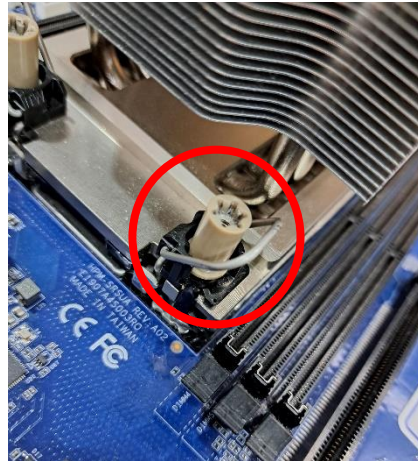


▲ Figure C

▼ Before locking the tenons

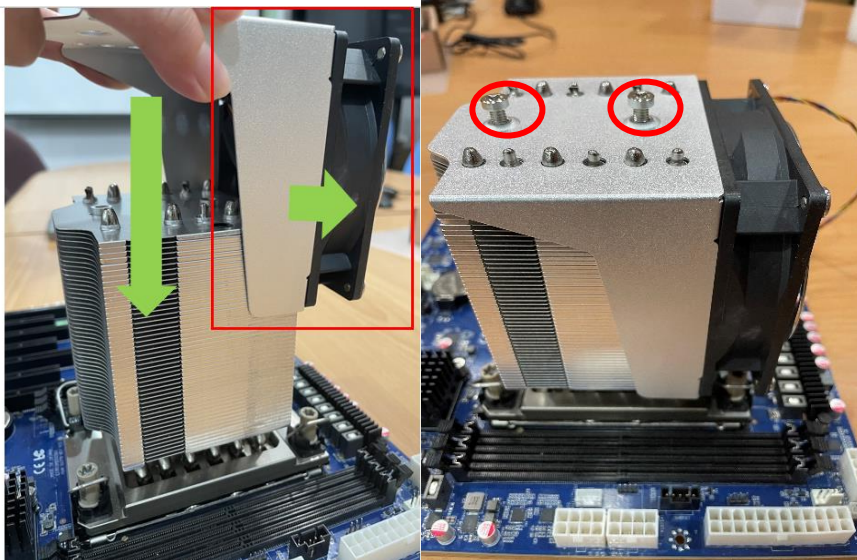


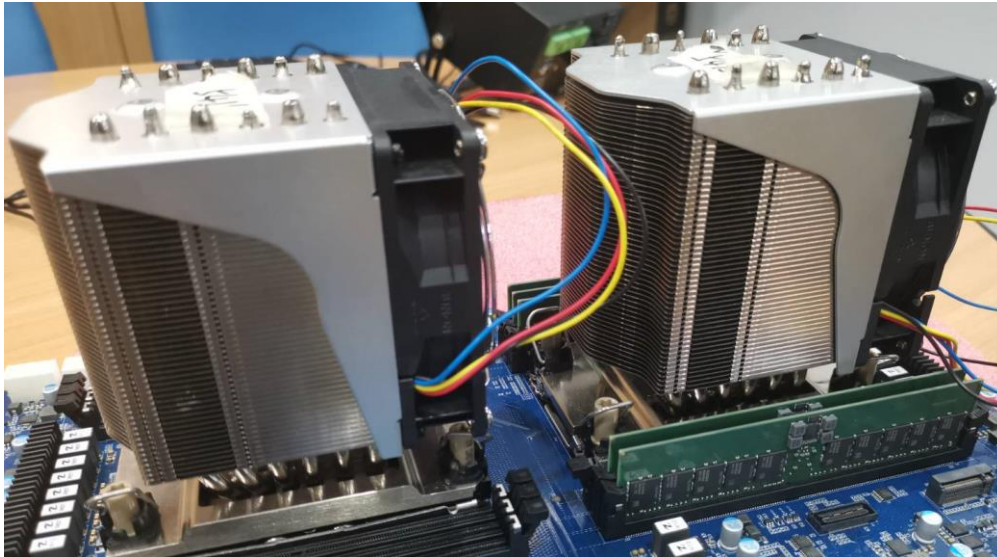
▼ After locking the tenons



6. Install the cooling fan and holder on the cooler module and tighten two locking screws (T30) on the top of the fan holder.

Note: The 4U cooler's fan for Xeon SP dual sockets is facing the opposite side of Edge I/O.





7. Connect the cooling fan connector to the fan header labeled for the CPU on the motherboard.

# 3. BIOS Setup

---

### 3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

### 3.2 Starting Setup

AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing <ESC> or <Del> immediately after switching the system on, or

By pressing the <ESC> or <Del> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

**Press <ESC> or <Del> to enter SETUP**

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys.

### 3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑	Move to previous item
↓	Move to next item
←	Move to the item in the left hand
→	Move to the item in the right hand
Esc key	Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Previous Values
F3 key	Optimized defaults
F4 key	Save & Exit Setup

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



**Note:** Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “➤” pointer marks all sub menus.

### 3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the <Enter> key again.

### 3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

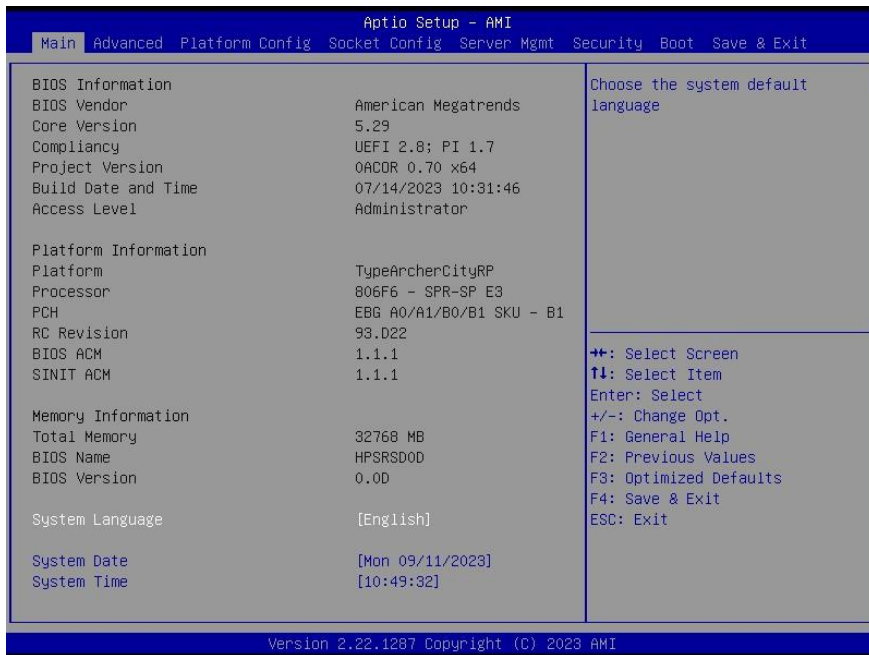
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

## 3.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

### 3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



#### 3.6.1.1 System Language

This option allows choosing the system default language.

#### 3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the Month, day and year.

#### 3.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.



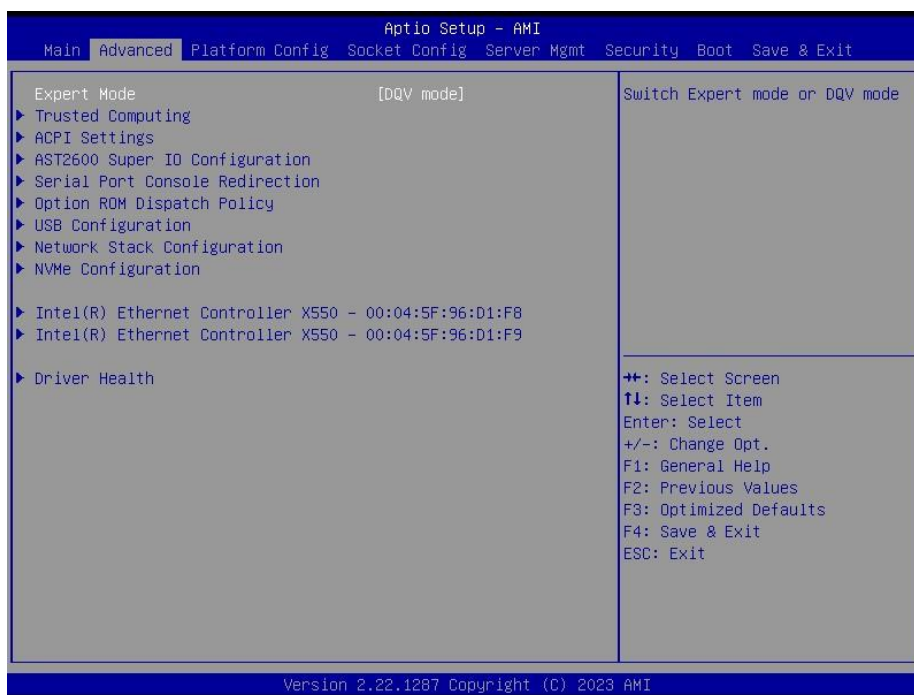
**Note:** The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.

Visit the Avalue website ([www.avalue.com.tw](http://www.avalue.com.tw)) to download the latest product and BIOS information.



### 3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



#### 3.6.2.1 Trusted Computing

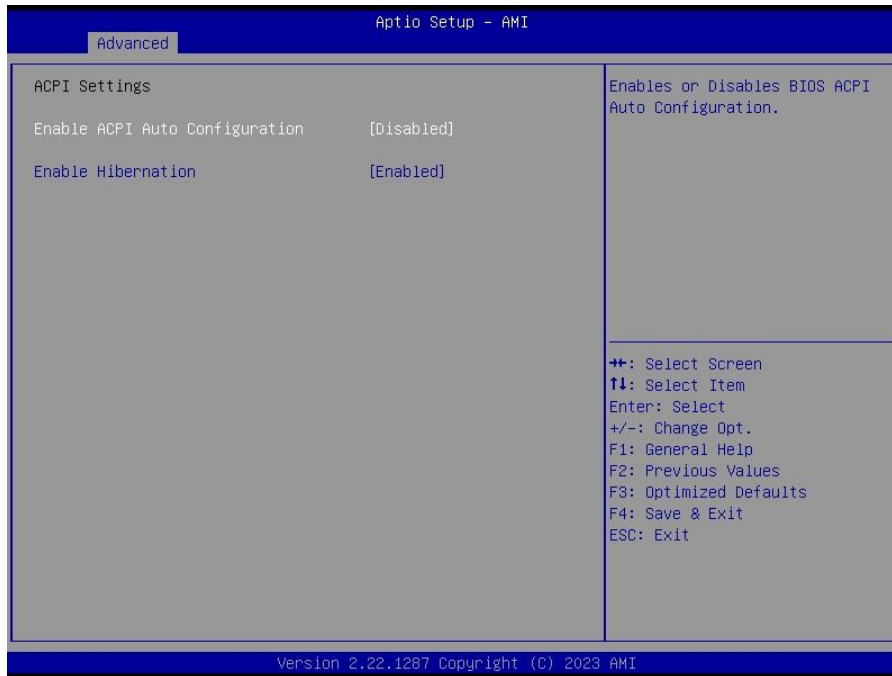


Item	Options	Description
Security Device Support	Disable, Enable[Default]	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

## HPM-SRSDE User's Manual

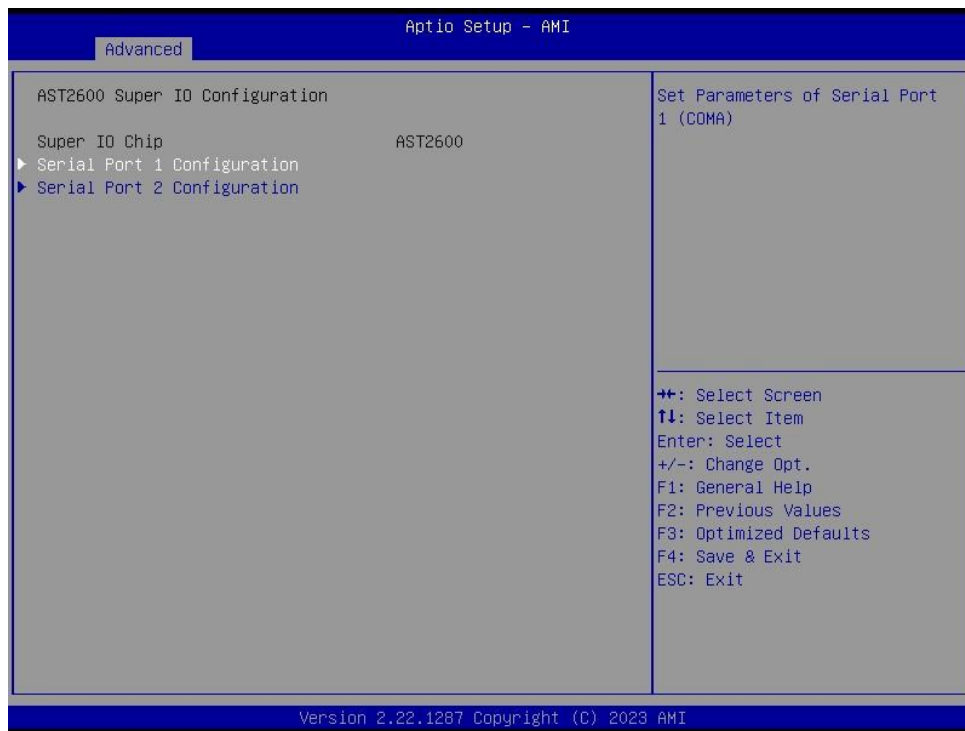
<b>SHA256 PCR Bank</b>	Disabled, Enabled[ <b>Default</b> ]	Enables or Disables SHA256 PCR Bank.
<b>SHA384 PCR Bank</b>	Disabled[ <b>Default</b> ], Enabled	Enables or Disables SHA384 PCR Bank.
<b>Pending operation</b>	None[ <b>Default</b> ] TPM Clear	Schedule an Operation for the Security Device. NOTE: Your Computer will reboot during restart in order to change State of Security Device.
<b>Physical Presence Spec Version</b>	1.2 1.3[ <b>Default</b> ]	Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3 Note some HCK tests might not support 1.3.
<b>Device Select</b>	TPM 2.0 Auto[ <b>Default</b> ]	TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with default set to TPM 2.0 devices if not found, TPM 1.2 devices will be enumerated.

### 3.6.2.2 ACPI Settings



Item	Options	Description
<b>Enable ACPI Auto Configuration</b>	Disabled[ <b>Default</b> ] Enabled	Enables or Disables BIOS ACPI Auto Configuration.
<b>Enable Hibernation</b>	Disabled Enabled[ <b>Default</b> ]	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some operating systems.

### 3.6.2.3 AST2600 Super IO Configuration



Item	Description
<b>Serial Port 1 Configuration</b>	Set Parameters of Serial Port 1 (COMA).
<b>Serial Port 2 Configuration</b>	Set Parameters of Serial Port 2 (COMB).

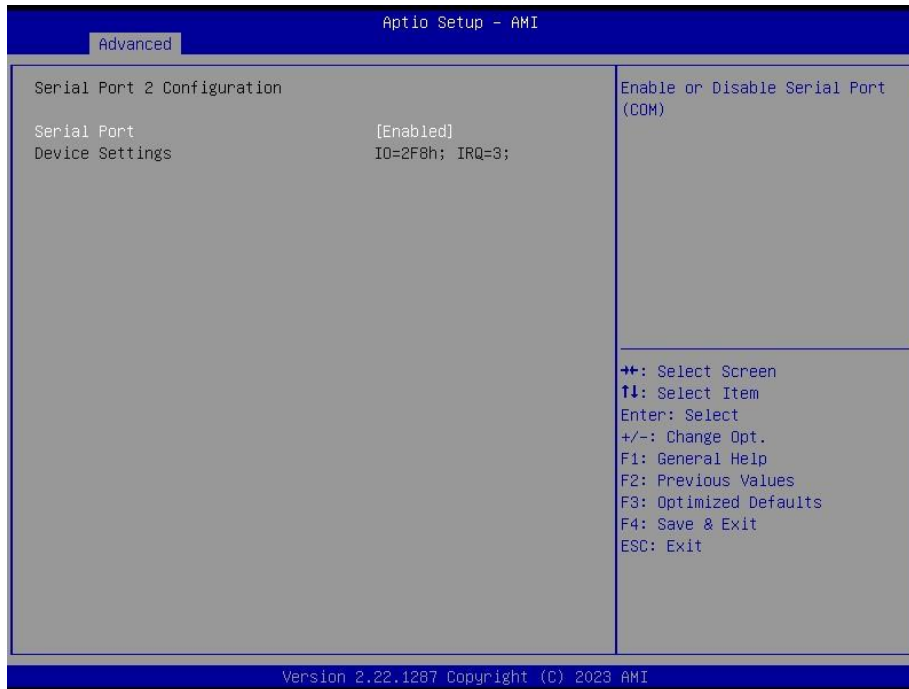
#### 3.6.2.3.1 Serial Port 1 Configuration



## HPM-SRSDE User's Manual

Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.3.2 Serial Port 2 Configuration



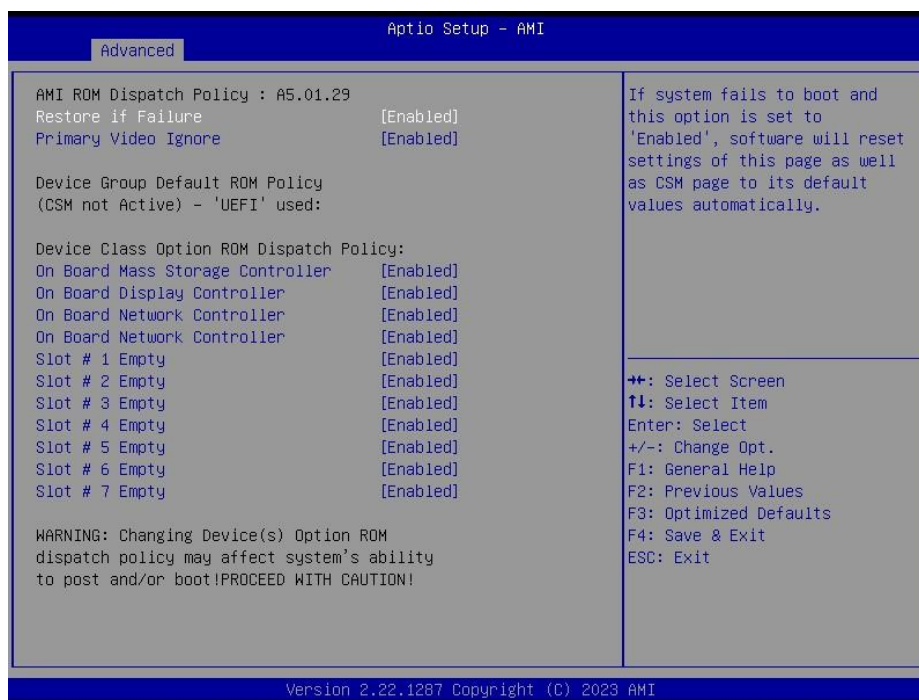
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.4 Serial Port Console Redirection



Item	Options	Description
Console Redirection	Disabled[Default], Enabled	Console Redirection Enable or Disable.
Console Redirection EMS	Disabled[Default], Enabled	Console Redirection Enable or Disable.

### 3.6.2.5 Option ROM Dispatch Policy



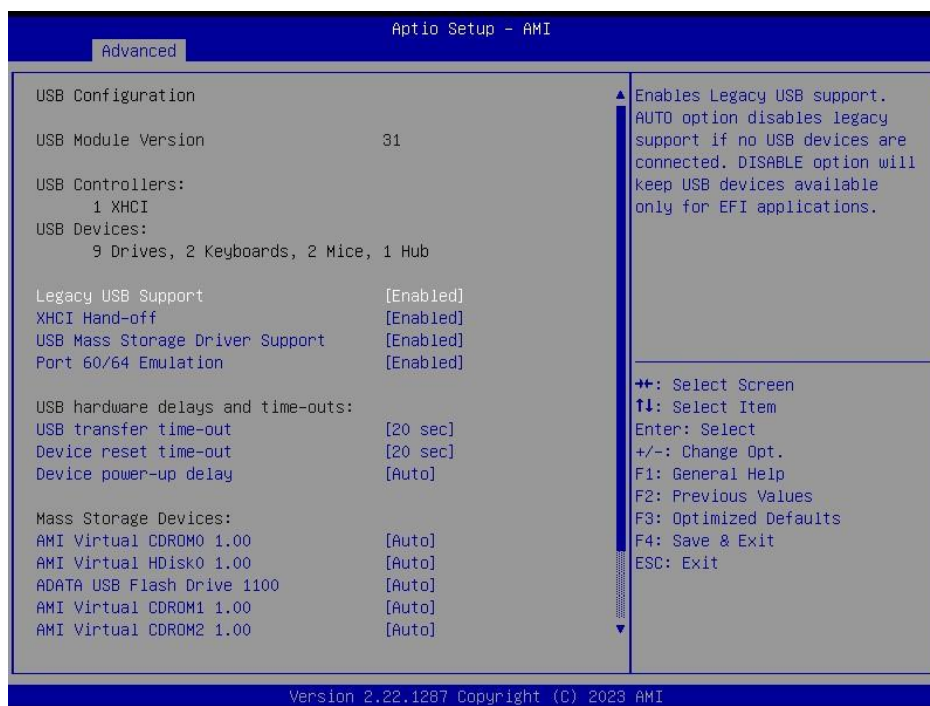
Item	Options	Description
Restore if Failure	Disabled Enabled[Default],	If system fails to boot and this option is set to 'Enabled', software will reset settings of this page as well as CSM page to its default values automatically.
Primary Video Ignore	Disabled Enabled[Default],	If software will detect that due to the Policy settings. Option ROM of Primary Video Device will not dispatch, it will ignore this device policy settings, and restore it to 'Enable' automatically.
Onboard Mass Storage Controller	Enabled[Default], Disabled	Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx8086; DIDxA1D2 @ s0 Bx0  Dx11  Fx5
Onboard Display Controller	Enabled[Default], Disabled	Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx1A03; DIDx2000 @ s0 BxA  Dx0  Fx0

## HPM-SRSDE User's Manual

<b>Onboard Network Controller</b>	Enabled[ <b>Default</b> ], Disabled	Onboard Device has: UEFI [X] Legacy [X] Embedded ROM(s). VIDx8086; DIDx1533 @ s0 Bx6  Dx0  Fx0
<b>Slot#1 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.
<b>Slot#2 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.
<b>Slot#3 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.
<b>Slot#4 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.
<b>Slot#5 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.
<b>Slot#6 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.
<b>Slot#7 Empty</b>	Enabled[ <b>Default</b> ], Disabled	Enable or Disable Option ROM execution for selected Slot.

### 3.6.2.6 USB Configuration

The USB Configuration menu helps read USB information and configures USB settings.

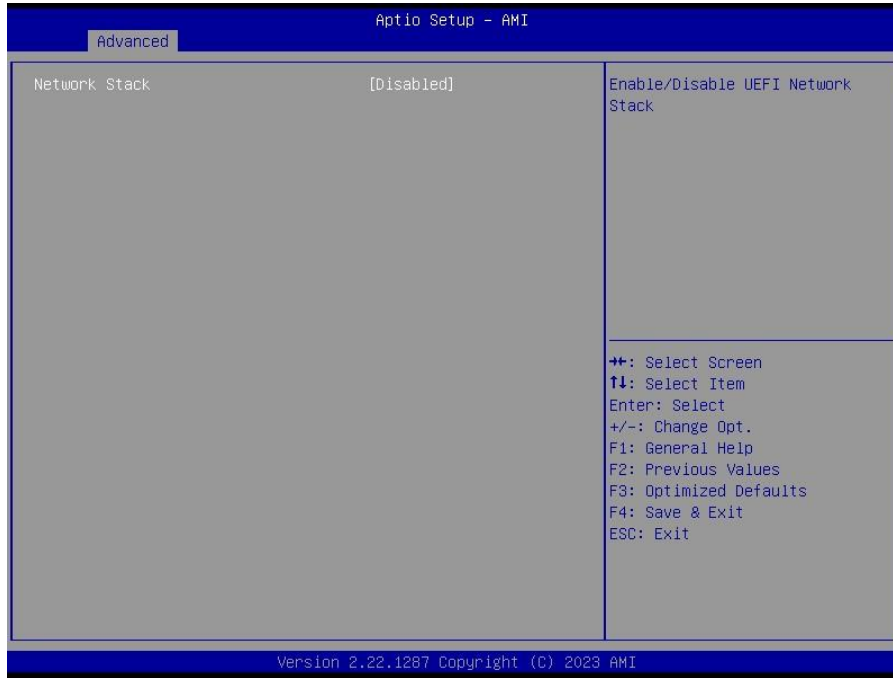


Item	Options	Description
<b>Legacy USB Support</b>	Enabled[Default], Disabled Auto	Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications.
<b>XHCI Hand-off</b>	Enabled[Default], Disabled	This is a workaround for Oses without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
<b>USB Mass Storage Driver Support</b>	Disabled Enabled[Default],	Enable/Disable USB Mass Storage Driver Support.
<b>Port 60/64 Emulation</b>	Disabled Enabled[Default],	Enables I/O port 60h/64h emulation support. This should be enabled for the complete USB keyboard legacy support for non-USB aware Oses.
<b>USB transfer time-out</b>	1 sec 5 sec 10 sec 20 sec[Default]	The time-out value for Control, Bulk, and Interrupt transfers.
<b>Device reset time-out</b>	10 sec 20 sec[Default] 30 sec 40 sec	USB mass storage device Start Unit command time-out.
<b>Device power-up delay</b>	Auto[Default] Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor.

## HPM-SRSDE User's Manual

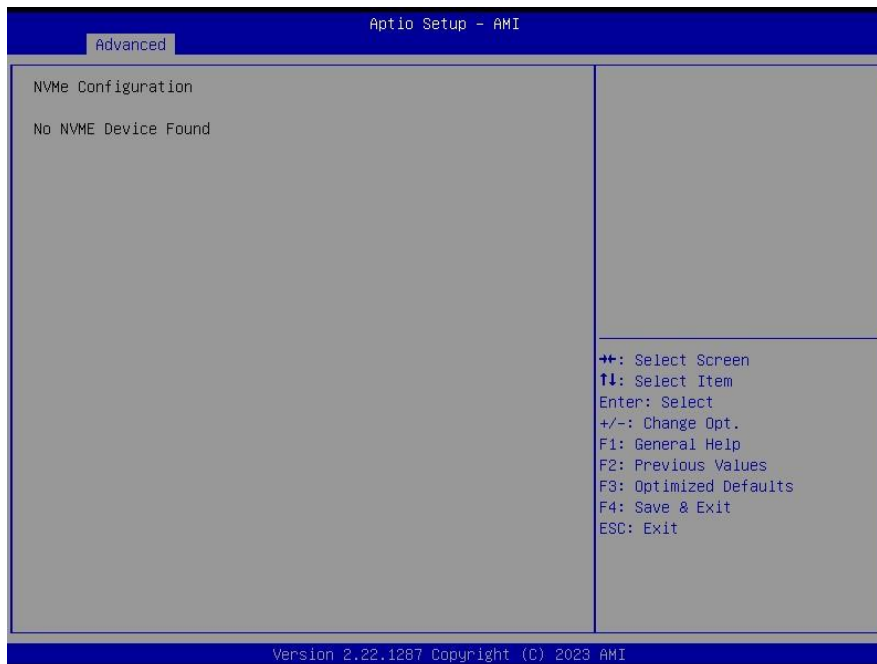
<p><b>Mass Storage Devices</b></p>	<p>Auto[<b>Default</b>]          Floppy          Forced FDD          Hard Disk          CD-ROM</p>	<p>Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type.</p>
------------------------------------	--	--

### 3.6.2.7 Network Stack Configuration



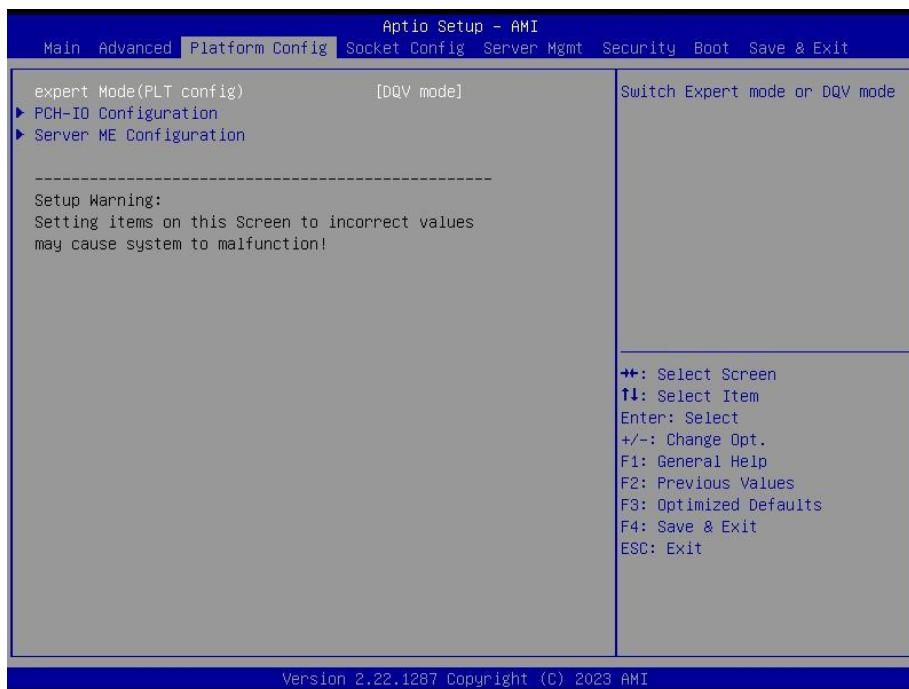
Item	Options	Description
<p><b>Network Stack</b></p>	<p>Enabled            Disabled[<b>Default</b>]</p>	<p>Enable/Disable UEFI Network Stack.</p>

### 3.6.2.8 NVMe Configuration





### 3.6.3 Platform Config



Item	Options	Description
Expert Mode(PLT config)	DQV mode[Default] Expert mode	Switch Expert mode or DQV mode.

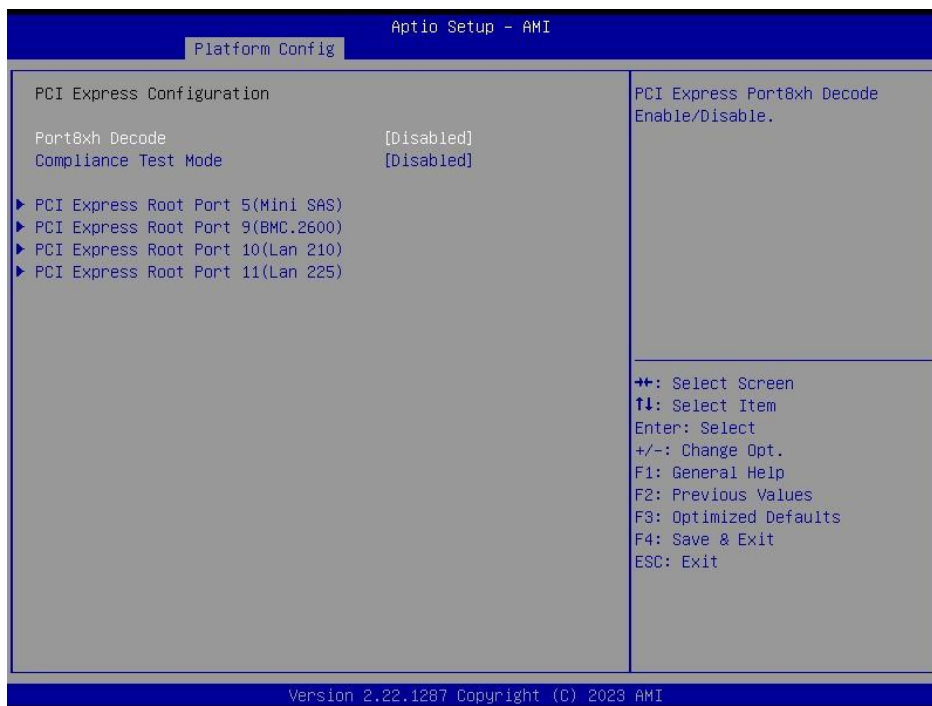
#### 3.6.3.1 PCH-IO Configuration



## HPM-SRSDE User's Manual

Item	Option	Description
Serial IRQ Mode	Quiet[Default] Continuous	Configure Serial IRQ Mode.
State After G3	S0 State S5 State[Default]	Specify what state to go to when power is re-applied after a power failure (G3 state).
Port 80h Redirection	LPC Bus[Default] PCIE Bus	Control where the Port 80h cycles are sent.
Lock PCH Side band Access	Disabled Enabled[Default]	Lock PCH Sideband access, include SideBand interface lock and SideBand PortID mask for certain end point (e.g. PSFx). The option is invalid if POSTBOOT SAI is set.
Flash Protection Range Registers(FRRR)	Disabled[Default] Enabled	Enable Flash Protection Range Registers.
SPD Write Disable	Disabled Enabled[Default]	Enable/Disable setting SPD Write Disable bit. For security recommendations, SPD write disable bit must be set.

### 3.6.3.1.1 PCI Express Configuration



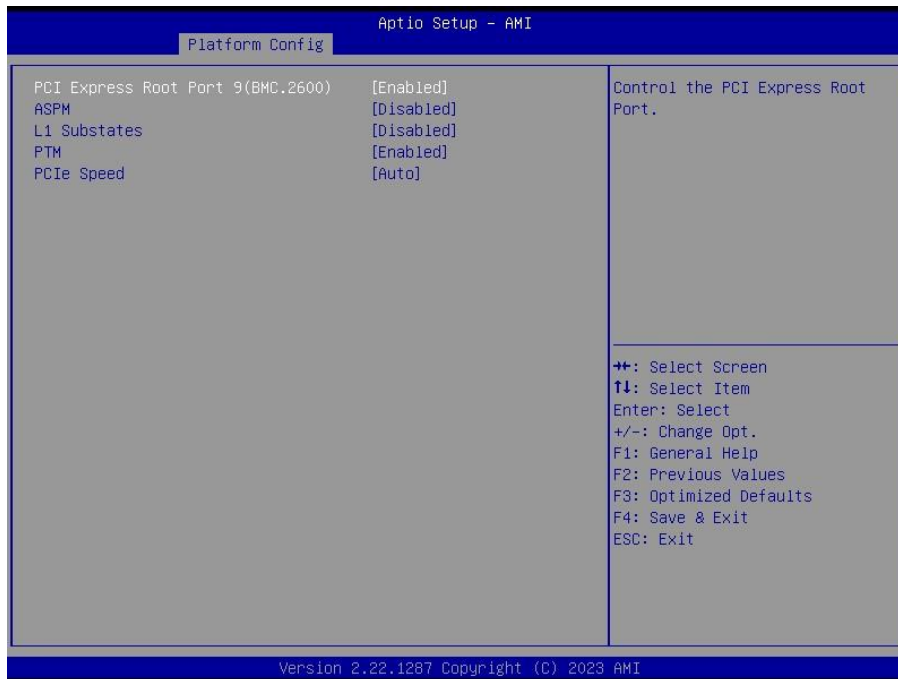
Item	Option	Description
Port8xh Decode	Disabled[Default] Enabled	PCI Express Port8xh Decode Enable/Disable.
Compliance Test Mode	Disabled[Default] Enabled	Enable when using Compliance Load Board.

### 3.6.3.1.1.1 PCI Express Root Port 5(Mini SAS)



Item	Option	Description
<b>PCI Express Root Port 5(Mini SAS)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled[Default], L1	PCI Express Active State Power Management settings.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PTM</b>	Enabled[Default], Disabled	Enable/Disable Precision Time Measurement.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe Speed.

3.6.3.1.1.2 PCI Express Root Port 9(BMC.2600)



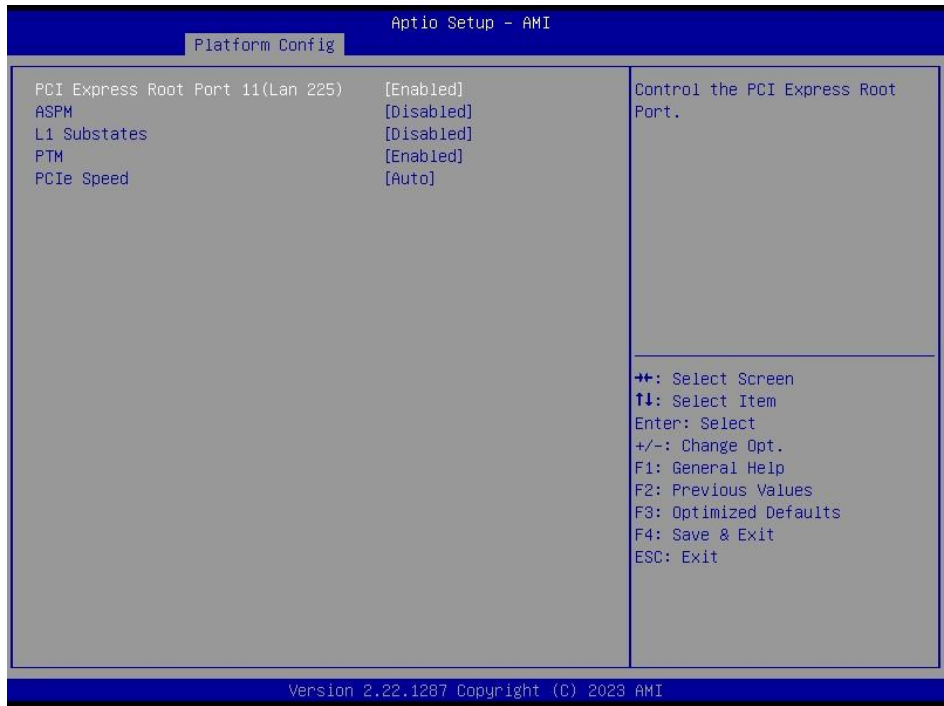
Item	Option	Description
<b>PCI Express Root Port 9(BMC.2600)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled[Default], L1	PCI Express Active State Power Management settings.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PTM</b>	Enabled[Default], Disabled	Enable/Disable Precision Time Measurement.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe Speed.

### 3.6.3.1.1.3 PCI Express Root Port 10(Lan 210)



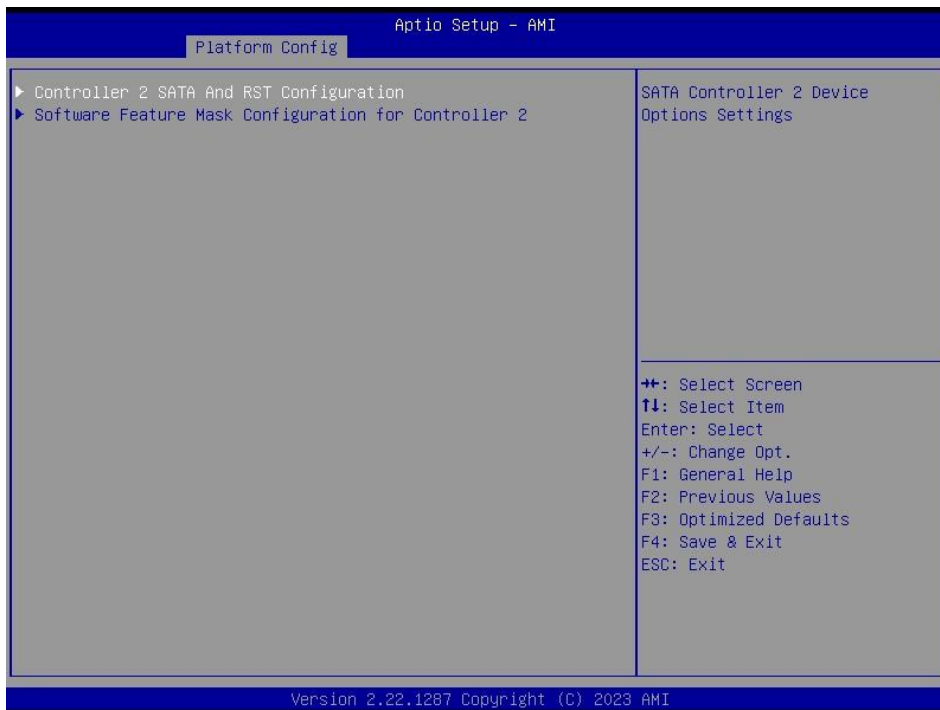
Item	Option	Description
<b>PCI Express Root Port 10(Lan 210)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled[Default], L1	PCI Express Active State Power Management settings.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PTM</b>	Enabled[Default], Disabled	Enable/Disable Precision Time Measurement.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe Speed.

3.6.3.1.1.4 PCI Express Root Port 11(LAN 225)

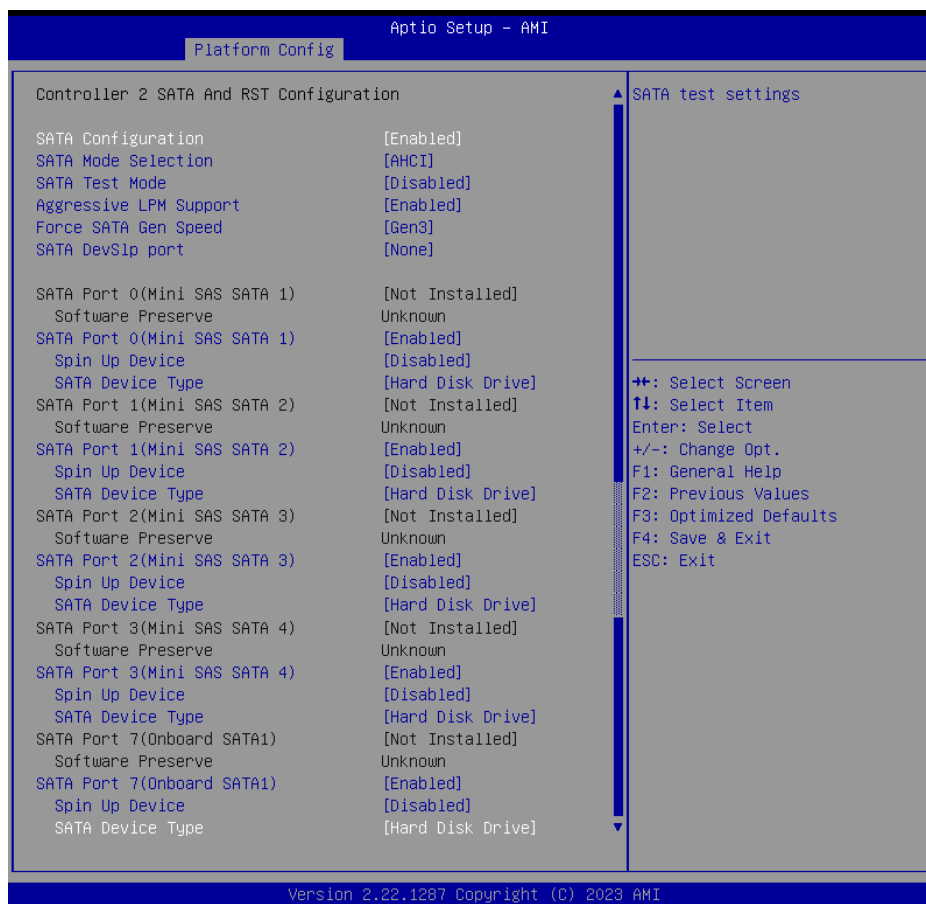


Item	Option	Description
<b>PCI Express Root Port 11(LAN 225)</b>	Enabled[Default], Disabled	Control the PCI Express Root Port.
<b>ASPM</b>	Disabled[Default], L1	PCI Express Active State Power Management settings.
<b>L1 Substates</b>	Disabled[Default] L1.1 L1.2 L1.1 & L1.2	PCI Express L1 Substates settings.
<b>PTM</b>	Enabled[Default], Disabled	Enable/Disable Precision Time Measurement.
<b>PCIe Speed</b>	Auto[Default] Gen1 Gen2 Gen3	Configure PCIe Speed.

### 3.6.3.1.2 SATA And RST Configuration



#### 3.6.3.1.2.1 Controller 2 SATA And RST Configuration



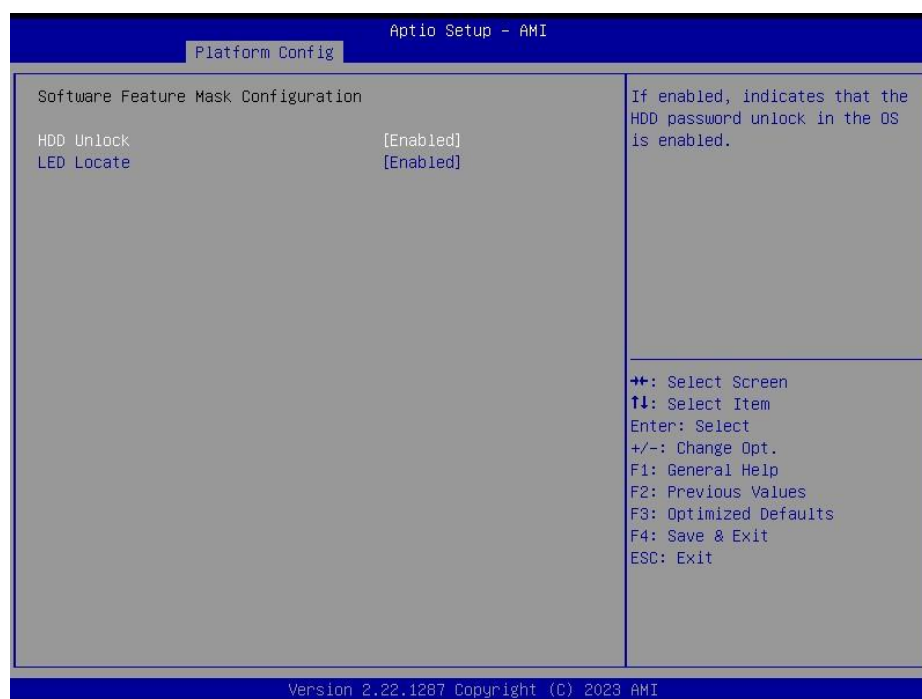
## HPM-SRSDE User's Manual

Item	Options	Description
<b>SATA Configuration</b>	Enabled[ <b>Default</b> ] Disabled,	SATA test settings.
<b>SATA Mode Selection</b>	AHCI[ <b>Default</b> ], RAID	Determines how SATA controller(s) operate.
<b>SATA Test Mode</b>	Enabled Disabled[ <b>Default</b> ]	Test Mode Enable/Disable (Loop Back).
<b>Aggressive LPM Support</b>	Enabled[ <b>Default</b> ] Disabled	Enable PCH to aggressively enter link power state.
<b>Force SATA Gen Speed</b>	Gen1 Gen2 Gen3[ <b>Default</b> ]	Changes SATA Gen Speed for port.
<b>SATA DevSlp port</b>	None[ <b>Default</b> ] Port0 Port1 Port2 Port3 Port4 Port5 Port6 Port7	Enable SATA DevSlp feature for port. It is possible to enable DevSlp for only one port or none.
<b>SATA Port 0(Mini SAS SATA 1)</b>	Disabled Enabled[ <b>Default</b> ]	Enable or Disable SATA Port.
<b>Spin Up Device</b>	Disabled[ <b>Default</b> ] Enabled	If enabled for any of ports Staggerred Spin Up will be performed and only the drives which have this option enabled will spin up at boot. Otherwise all drives spin up at boot.
<b>SATA Device Type</b>	Hard Disk Drive[ <b>Default</b> ] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.
<b>SATA Port 1(Mini SAS SATA 2)</b>	Disabled Enabled[ <b>Default</b> ]	Enable or Disable SATA Port.
<b>Spin Up Device</b>	Disabled[ <b>Default</b> ] Enabled	If enabled for any of ports Staggerred Spin Up will be performed and only the drives which have this option enabled will spin up at boot. Otherwise all drives spin up at boot.
<b>SATA Device Type</b>	Hard Disk Drive[ <b>Default</b> ] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.
<b>SATA Port 2(Mini SAS SATA 3)</b>	Disabled Enabled[ <b>Default</b> ]	Enable or Disable SATA Port.
<b>Spin Up Device</b>	Disabled[ <b>Default</b> ] Enabled	If enabled for any of ports Staggerred Spin Up will be performed and only the drives which have this option enabled will



		spin up at boot. Otherwise all drives spin up at boot.
<b>SATA Device Type</b>	Hard Disk Drive[Default] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.
<b>SATA Port 3(Mini SAS SATA 4)</b>	Disabled Enabled[Default]	Enable or Disable SATA Port.
<b>Spin Up Device</b>	Disabled[Default] Enabled	If enabled for any of ports Staggered Spin Up will be performed and only the drives which have this option enabled will spin up at boot. Otherwise all drives spin up at boot.
<b>SATA Device Type</b>	Hard Disk Drive[Default] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.
<b>SATA Port 7(Onboard SATA 1)</b>	Disabled Enabled[Default]	Enable or Disable SATA Port.
<b>Spin Up Device</b>	Disabled[Default] Enabled	If enabled for any of ports Staggered Spin Up will be performed and only the drives which have this option enabled will spin up at boot. Otherwise all drives spin up at boot.
<b>SATA Device Type</b>	Hard Disk Drive[Default] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.

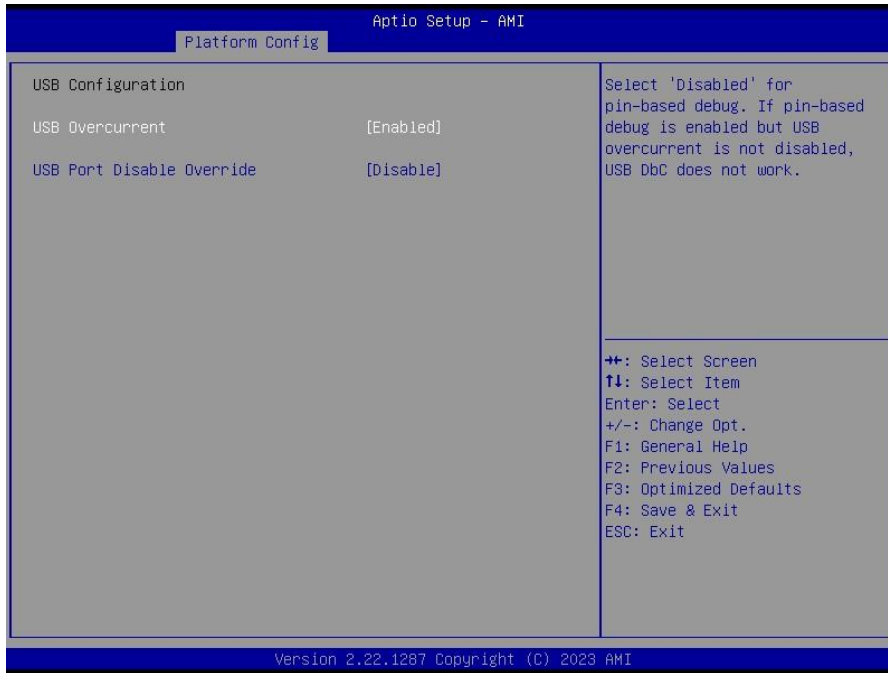
### 3.6.3.1.2.2 Software Feature Mask Configuration for Controller 2



## HPM-SRSDE User's Manual

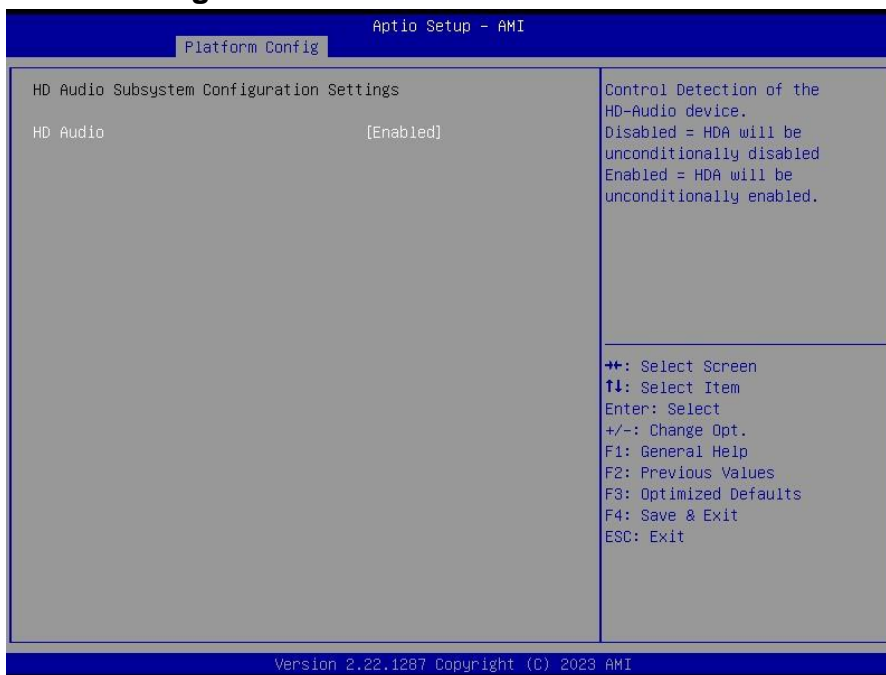
Item	Options	Description
<b>HDD Unlock</b>	Disabled, Enabled[ <b>Default</b> ]	If enabled, indicates that the HDD password unlock in the OS is enabled.
<b>LED Locate</b>	Disabled, Enabled[ <b>Default</b> ]	If enabled, indicates that the LED/SGPIO hardware is attached and ping to locate feature is enabled on the OS.

### 3.6.3.1.3 USB Configuration



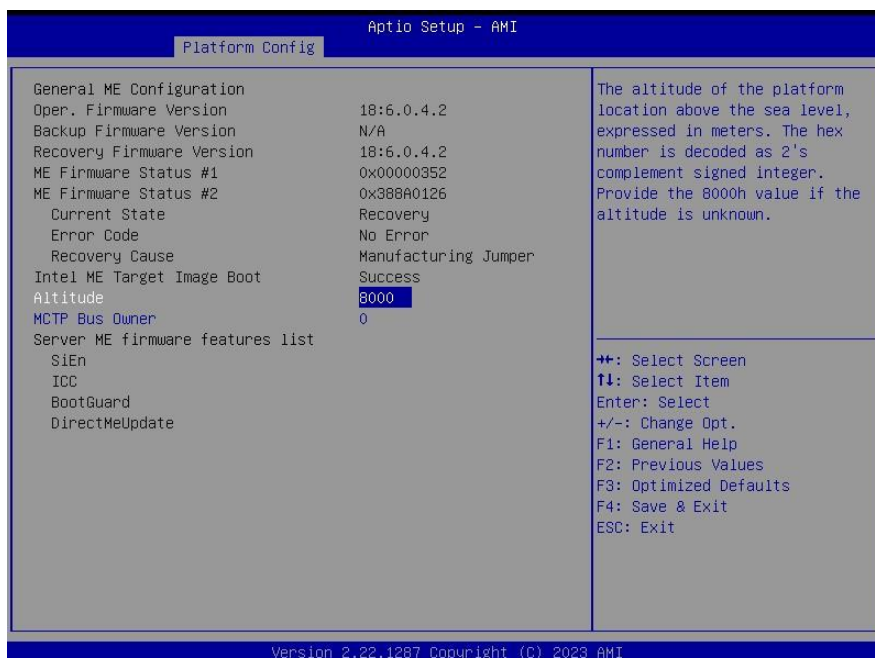
Item	Options	Description
<b>USB Overcurrent</b>	Disabled, Enabled[ <b>Default</b> ]	Select 'Disabled' for pin-based debug. If pin-based debug is enabled but USB overcurrent is not disabled, USB DbC does not work.
<b>USB Port Disable Override</b>	Disable[ <b>Default</b> ] Select Per-Pin	Selectively Enable/Disable the corresponding USB port from reporting a Device Connection to the controller.

### 3.6.3.1.4 HD Audio Configuration



Item	Options	Description
HD Audio	Disabled, Enabled[Default]	Control Detection of the HD-Audio device. Disabled=HDA will be unconditionally disabled. Enabled=HDA will be unconditionally enabled.

### 3.6.3.2 Server ME Configuration



Item	Option	Description
Altitude	8000	The altitude of the platform location above the sea level, expressed in meters. The hex number is decoded as 2's complement signed integer. Provide the 8000h value if the

## HPM-SRSDE User's Manual

		altitude is unknown.
<b>MCTP Bus Owner</b>	0	MCTP bus owner location on PCIe: [15:8] bus, [7:3] device, [2:0] function. If all zeros sending bus owner is disabled.

### 3.6.4 Socket Config



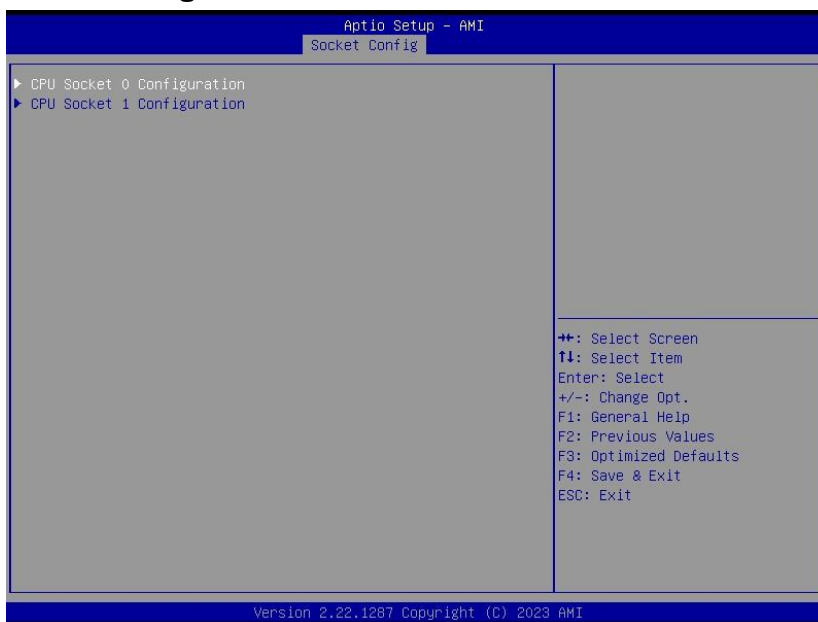
Item	Options	Description
<b>Expert Mode (Socket config)</b>	DQV mode[Default] Expert mode	Switch Expert mode or DQV mode.

#### 3.6.4.1 Processor Configuration

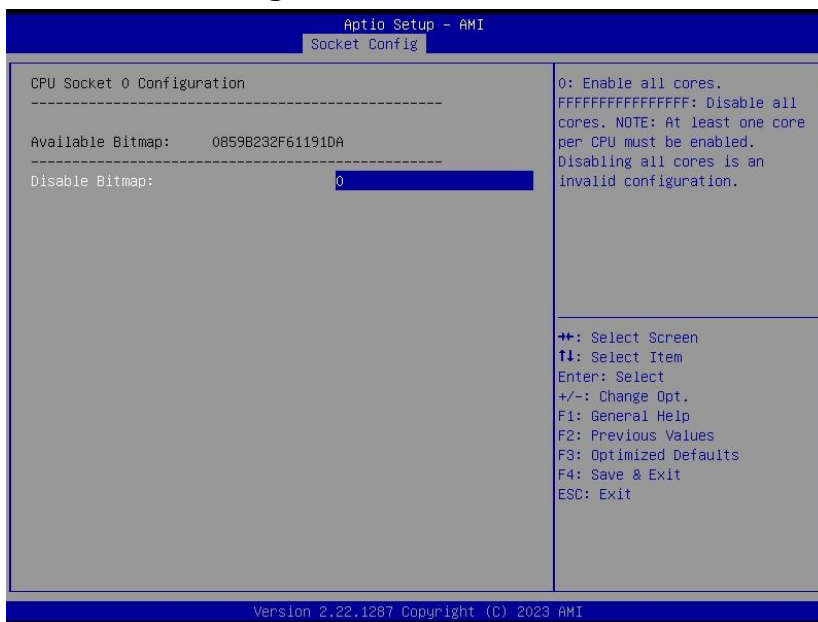


Item	Option	Description
Extended APIC	Disable Enable[Default]	Enable/disable extended APIC support. Note: When enabled, VT-d_Interrupt Remapping will be automatically enabled.

### 3.6.4.1.1 Per-Socket Configuration



#### 3.6.4.1.1.1 CPU Socket 0 Configuration



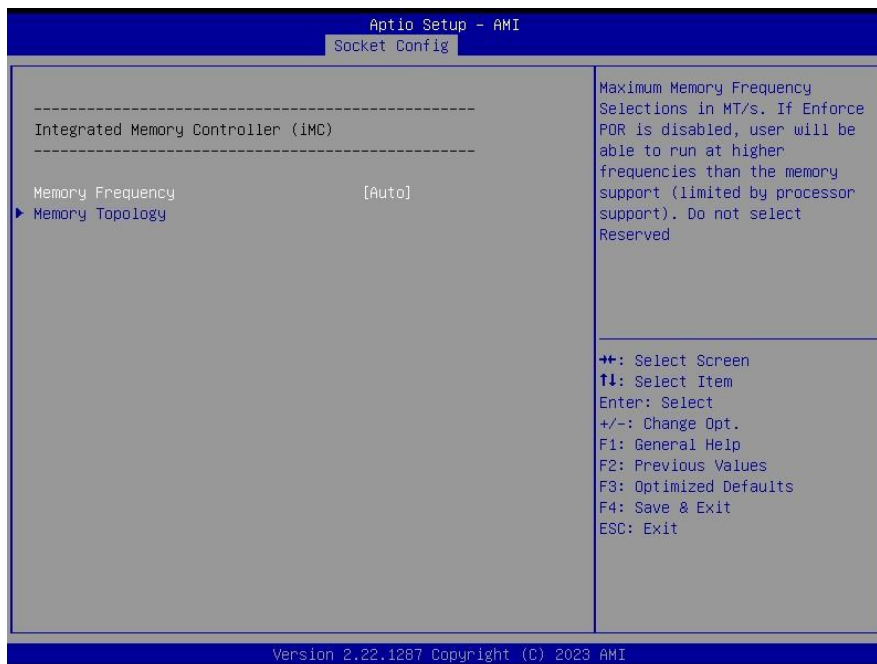
Item	Option	Description
Disable Bitmap:	0	0: Enable all cores. FFFFFFFFFFFFFFFF: Disable all cores. NOTE: AT least one core per CPU must be enabled. Disabling all cores is an invalid configuration.

3.6.4.1.1.2 CPU Socket 1 Configuration



Item	Option	Description
Disable Bitmap:	0	0: Enable all cores. FFFFFFFFFFFFFFFF: Disable all cores. NOTE: AT least one core per CPU must be enabled. Disabling all cores is an invalid configuration.

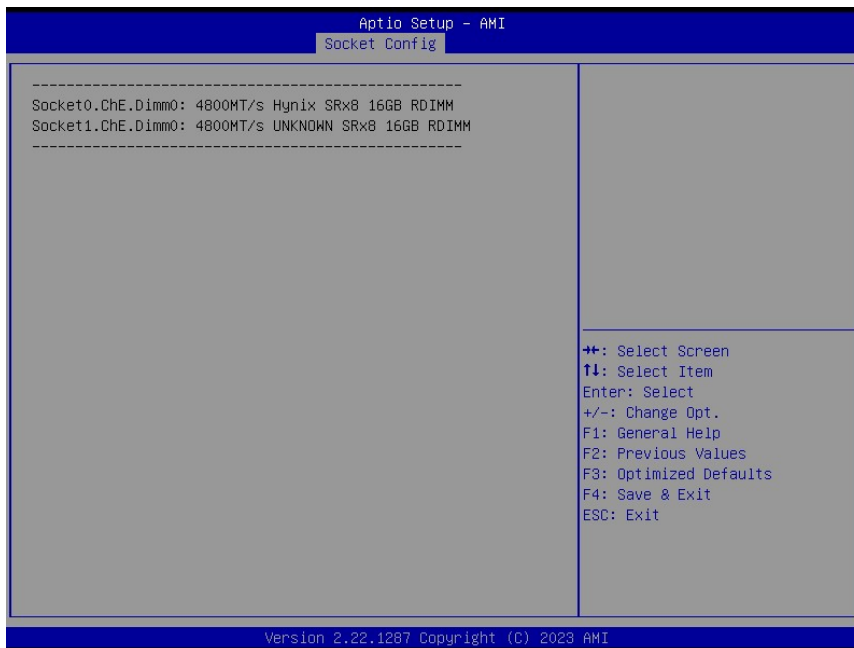
3.6.4.2 Memory Configuration



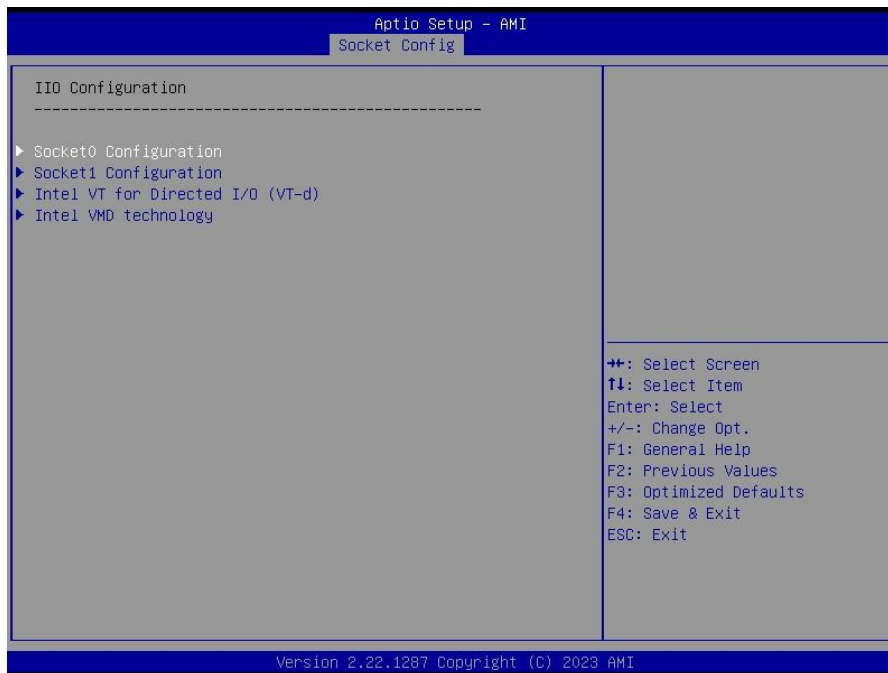
Item	Option	Description
Memory Frequency	Auto[Default] 3200	Maximum Memory Frequency Selections in MT/s. If Enforce POR is disabled, user will

	3600	be able to run at higher frequencies than the memory support (limited by processor support). Do not select Reserved.
	4000	
	4400	
	4800	
	5200	
	5600	

### 3.6.4.2.1 Memory Topology

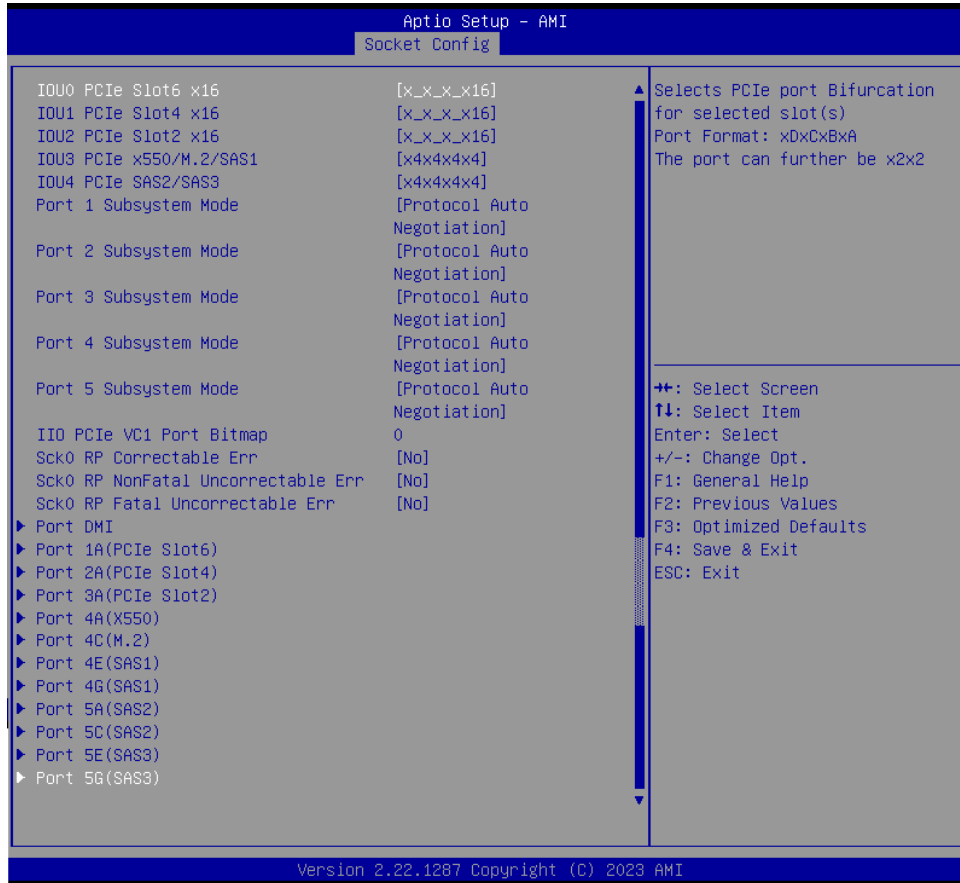


### 3.6.4.3 IIO Configuration



# HPM-SRSDE User's Manual

## 3.6.4.3.1 Socket0 Configuration



Item	Options	Description
<p><b>IOU0 PCIe Slot6 x16</b></p>	<p>Auto                      x4x4x4x4                      x4x4x_x8                      x_x8x4x4                      x_x8x_x8                      x_x_x_x16[Default]                      x2x2x4x_x8                      x4x2x2x_x8                      x_x8x2x2x4                      x2x2x4x4x4                      x4x2x2x4x4                      x4x4x2x2x4                      x2x2x2x2x_x8                      x2x2x2x2x4x4                      x2x2x4x2x2x4                      x4x2x2x2x2x4                      x2x2x2x2x2x2x4                      x_x8x4x2x2                      x4x4x4x2x2                      x_x8x2x2x2x2                      x2x2x4x4x2x2                      x4x2x2x4x2x2                      x4x4x2x2x2x2                      x2x2x2x2x4x2x2                      x2x2x4x2x2x2x2                      x4x2x2x2x2x2x2                      x2x2x2x2x2x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>



<p><b>IOU1 PCIe Slot4 x16</b></p>	<p>Auto  x4x4x4x4  x4x4x_x8  x_x8x4x4  x_x8x_x8  <b>x_x_x_x16[Default]</b>  x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>
<p><b>IOU2 PCIe Slot2 x16</b></p>	<p>Auto  x4x4x4x4  x4x4x_x8  x_x8x4x4  x_x8x_x8  <b>x_x_x_x16[Default]</b>  x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>
<p><b>IOU3 PCIe x550/M.2/SAS1</b></p>	<p>Auto  <b>x4x4x4x4[Default]</b>  x4x4x_x8  x_x8x4x4  x_x8x_x8  x_x_x_x16</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>

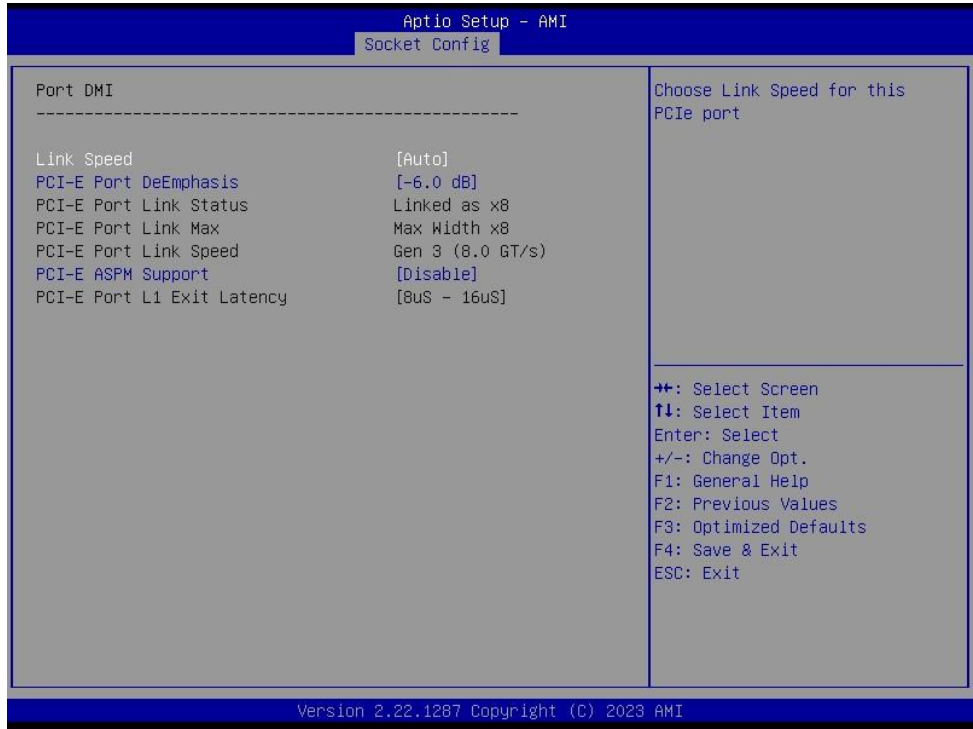
## HPM-SRSDE User's Manual

	<p>x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	
<b>IOU4 PCIe SAS2/SAS3</b>	<p>Auto  x4x4x4x4[Default]  x4x4x_x8  x_x8x4x4  x_x8x_x8  x_x_x_x16  x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>
<b>Port 1 Subsystem Mode</b>	<p>Gen5  Protocol Auto Negotiation[Default]</p>	<p>Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.</p>
<b>Port 2 Subsystem Mode</b>	<p>Gen5  Protocol Auto Negotiation[Default]</p>	<p>Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller</p>

		only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.
<b>Port 3 Subsystem Mode</b>	Gen5 Protocol Auto Negotiation[Default]	Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.
<b>Port 4 Subsystem Mode</b>	Gen5 Protocol Auto Negotiation[Default]	Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.
<b>Port 5 Subsystem Mode</b>	Gen5 Protocol Auto Negotiation[Default]	Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.
<b>IIO PCIe VC1 Port Bitmap</b>	0	Enable/Disable PCIe Port VC1 support. Port 0 is allocated to DMI or DMI as PCIe. Port 0 bit will have no effect in DMI mode. 0-VC1 support disabled. 1-VC1 support enabled. Example: bit 0= IIO PCIe Port 0...bit n = IIO PCIe Portn.
<b>Sck0 RP Correctable Err</b>	No[Default] Yes	Applies to root ports only. Enabled interrupt on correctable errors.
<b>Sck0 RP NonFatal Uncorrectable Err</b>	No[Default] Yes	Applies to root ports only. Enabled interrupt on a non-fatal error.
<b>Sck0 RP Fatal Uncorrectable Err</b>	No[Default] Yes	Applies to root ports only. Enabled MSI/INTx interrupt on fatal errors.

# HPM-SRSDE User's Manual

## 3.6.4.3.1.1 Port DMI



Item	Option	Description
<b>Link Speed</b>	Auto[Default] Gen 1 (2.5 GT/s) Gen 2 (5 GT/s) Gen 3 (8 GT/s) Gen 4 (16 GT/s) Gen 5 (32 GT/s)	Choose Link Speed for this PCIe port.
<b>PCI-E Port DeEmphasis</b>	-6.0 dB[Default] -3.5 dB	De-Emphasis control (LNKCON2[6]) for this PCIe port.
<b>PCI-E ASPM Support</b>	Disabled[Default] Auto	This option can disable ASPM support in a PCIe root port. 'Auto' keeps hardware default.

**3.6.4.3.1.2 Port 1A(PCIe Slot6), Port 2A(PCIe Slot4), Port 3A(PCIe Slot2), Port 4A(X550), Port 4C(M.2), Port 4E(SAS1), Port 4G(SAS1), Port 5A(SAS2), Port 5C(SAS2), Port 5E(SAS3), Port 5G(SAS3)**



Item	Option	Description
<b>PCI-E Port</b>	Auto[Default] No Yes	In auto mode the BIOS will remove the EXP port if there is no device or errors on that device and the device is not HP capable. Enable/Disable is used to enable/disable the port and expose/hide its CFG space.
<b>PCI-E Port Link Disable</b>	No[Default] Yes	This option disables the link so that the no training occurs but the CFG space is still active.
<b>Link Speed</b>	Auto[Default] Gen 1 (2.5 GT/s) Gen 2 (5 GT/s) Gen 3 (8 GT/s) Gen 4 (16 GT/s) Gen 5 (32 GT/s)	Choose Link Speed for this PCIe port.
<b>Override Max Link Width</b>	Auto[Default] x1 x2 x4 x8 x16	Override the max link width that was set by bifurcation.
<b>PCI-E Port DeEmphasis</b>	-6.0 dB[Default] -3.5 dB	De-Emphasis control (LNKCON2[6]) for this PCIe port.
<b>PCI-E Port MPSS</b>	128B 256B 512B Auto[Default]	Configure Max Payload Size Supported in PCIe Device Capabilities register. 'Auto' keeps hardware default.

## HPM-SRSDE User's Manual

<b>PCI-E ASPM Support</b>	Disabled[ <b>Default</b> ] Auto	This option can disable ASPM support in a PCIe root port. 'Auto' keeps hardware default.
<b>PCI-E Detect Wait Time</b>	Disable 500ms Auto[ <b>Default</b> ]	Set PCIe port TxRx detect polling.
<b>Compliance Mode</b>	No[ <b>Default</b> ] Yes	Enable/Disable Compiancer Mode for this PCIe port.
<b>MCTP</b>	No Yes[ <b>Default</b> ]	Enable/Disable MCTP.
<b>Equalization Bypass To Highest Rate</b>	Disable Enable[ <b>Default</b> ]	Equalization Bypass To Highest Rate Support Enable/Disable.
<b>CXL Drift Buffer</b>	Disable[ <b>Default</b> ] Enable	Enable/Disable CXL Drift Buffer if there is a common reference clock.

### 3.6.4.3.2 Socket1 Configuration



Item	Options	Description
<b>IOU0 PCIe Slot7 x16</b>	Auto x4x4x4x4 x4x4x_x8 x_x8x4x4 x_x8x_x8 x_x_x_x16[ <b>Default</b> ] x2x2x4x_x8 x4x2x2x_x8 x_x8x2x2x4 x2x2x4x4x4 x4x2x2x4x4 x4x4x2x2x4 x2x2x2x2x_x8 x2x2x2x2x4x4 x2x2x4x2x2x4	Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.

	<p>x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	
<p><b>IOU1 PCIe Slot5 x16</b></p>	<p>Auto  x4x4x4x4  x4x4x_x8  x_x8x4x4  x_x8x_x8  <b>x_x_x_x16[Default]</b>  x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>
<p><b>IOU2 PCIe Slot3 x16</b></p>	<p>Auto  x4x4x4x4  x4x4x_x8  x_x8x4x4  x_x8x_x8  <b>x_x_x_x16[Default]</b>  x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>

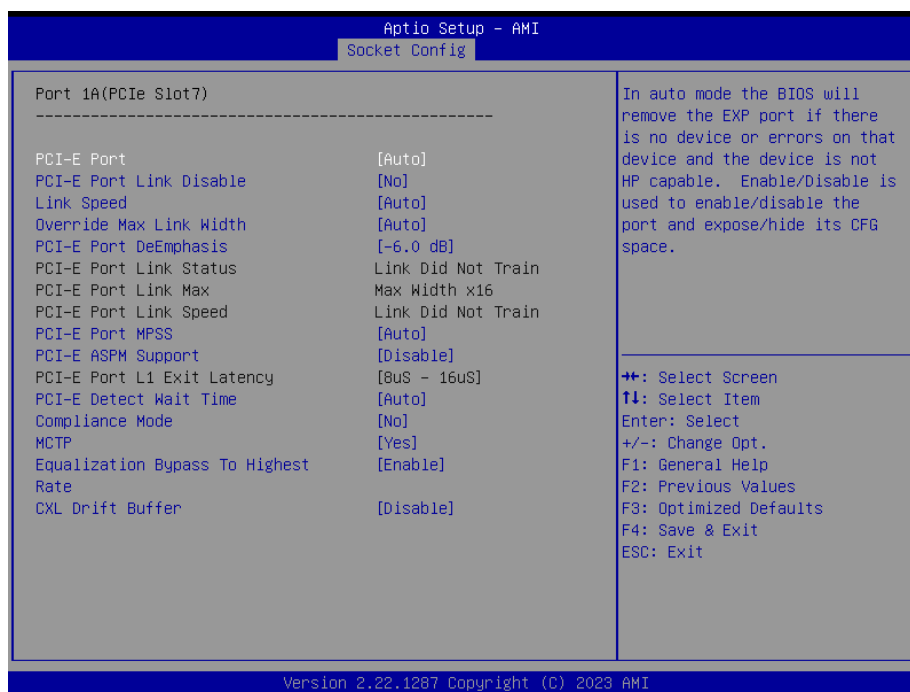
# HPM-SRSDE User's Manual

	<p>x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	
<p><b>IOU4 PCIe Slot1 x16</b></p>	<p>Auto  x4x4x4x4  x4x4x_x8  x_x8x4x4  x_x8x_x8  <b>x_x_x_x16[Default]</b>  x2x2x4x_x8  x4x2x2x_x8  x_x8x2x2x4  x2x2x4x4x4  x4x2x2x4x4  x4x4x2x2x4  x2x2x2x2x_x8  x2x2x2x2x4x4  x2x2x4x2x2x4  x4x2x2x2x2x4  x2x2x2x2x2x2x4  x_x8x4x2x2  x4x4x4x2x2  x_x8x2x2x2x2  x2x2x4x4x2x2  x4x2x2x4x2x2  x4x4x2x2x2x2  x2x2x2x2x4x2x2  x2x2x4x2x2x2x2  x4x2x2x2x2x2x2  x2x2x2x2x2x2x2x2</p>	<p>Selects PCIe port Bifurcation for selected slot(s) Port Format: xDxCxBxA The port can further be x2x2.</p>
<p><b>Port 1 Subsystem Mode</b></p>	<p>Gen5  Protocol Auto Negotiation[Default]</p>	<p>Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.</p>
<p><b>Port 2 Subsystem Mode</b></p>	<p>Gen5  Protocol Auto Negotiation[Default]</p>	<p>Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.</p>
<p><b>Port 3 Subsystem Mode</b></p>	<p>Gen5  Protocol Auto Negotiation[Default]</p>	<p>Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.</p>



<b>Port 5 Subsystem Mode</b>	Gen5 Protocol Auto Negotiation[Default]	Select PCIe Subsystem Mode for selected slot(s) Gen4: Gen4 controller only Gen5: Gen5 with or without mix mode Auto: Auto select Force CXL: There is no training discovery, the attached device must also supports this mode.
<b>IIO PCIe VC1 Port Bitmap</b>	0	Enable/Disable PCIe Port VC1 support. Port 0 is allocated to DMI or DMI as PCIe. Port 0 bit will have no effect in DMI mode. 0-VC1 support disabled. 1-VC1 support enabled. Example: bit 0= IIO PCIe Port 0...bit n = IIO PCIe Portn.
<b>Sck1 RP Correctable Err</b>	No[Default] Yes	Applies to root ports only. Enabled interrupt on correctable errors.
<b>Sck1 RP NonFatal Uncorrectable Err</b>	No[Default] Yes	Applies to root ports only. Enabled interrupt on a non-fatal error.
<b>Sck1 RP Fatal Uncorrectable Err</b>	No[Default] Yes	Applies to root ports only. Enabled MSI/INTx interrupt on fatal errors.

### 3.6.4.3.2.1 Port 1A(Pcie Slot7), Port 2A(Pcie Slot5), Port 3A(Pcie Slot3), Port 5A(Pcie Slot1)

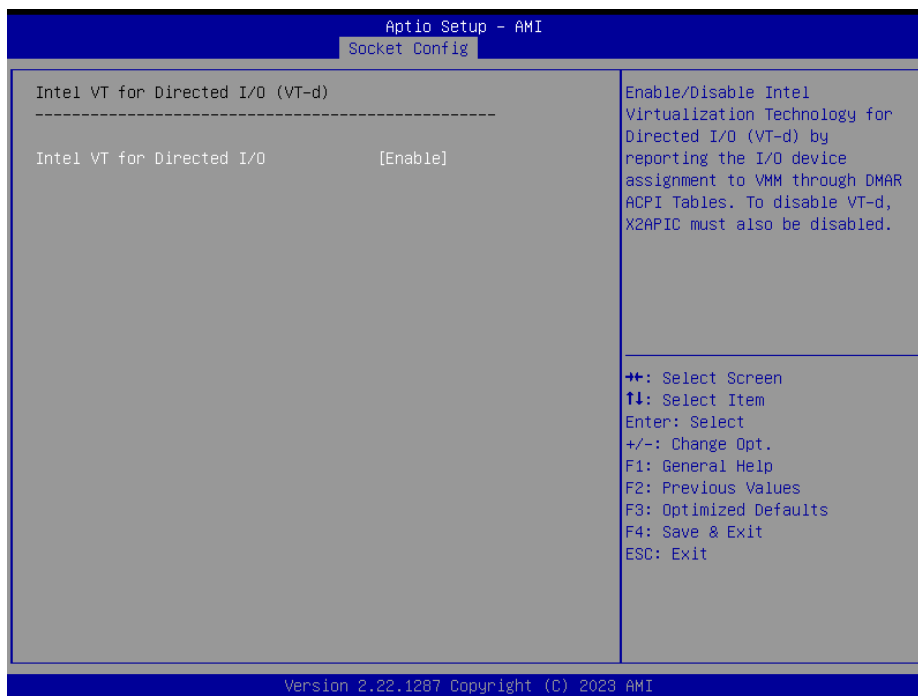


Item	Option	Description
<b>PCI-E Port</b>	Auto[Default] No Yes	In auto mode the BIOS will remove the EXP port if there is no device or errors on that device and the device is not HP capable. Enable/Disable is used to

## HPM-SRSDE User's Manual

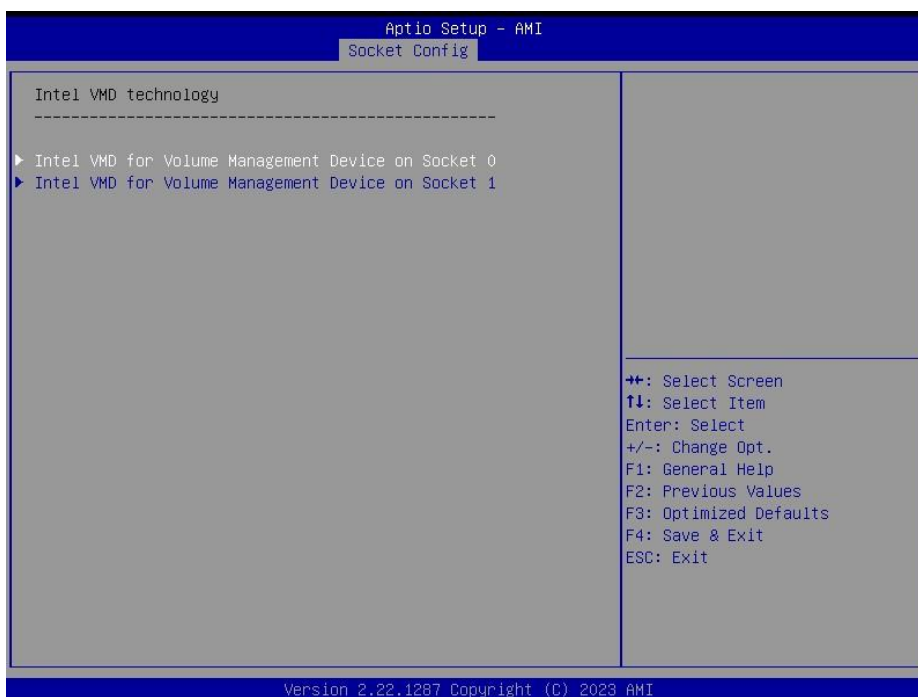
		enable/disable the port and expose/hide its CFG space.
<b>PCI-E Port Link Disable</b>	No[Default] Yes	This option disables the link so that the no training occurs but the CFG space is still active.
<b>Link Speed</b>	Auto[Default] Gen 1 (2.5 GT/s) Gen 2 (5 GT/s) Gen 3 (8 GT/s) Gen 4 (16 GT/s) Gen 5 (32 GT/s)	Choose Link Speed for this PCIe port.
<b>Override Max Link Width</b>	Auto[Default] x1 x2 x4 x8 x16	Override the max link width that was set by bifurcation.
<b>PCI-E Port DeEmphasis</b>	-6.0 dB[Default] -3.5 dB	De-Emphasis control (LNKCON2[6]) for this PCIe port.
<b>PCI-E Port MPSS</b>	128B 256B 512B Auto[Default]	Configure Max Payload Size Supported in PCIe Device Capabilities register. 'Auto' keeps hardware default.
<b>PCI-E ASPM Support</b>	Disabled[Default] Auto	This option can disable ASPM support in a PCIe root port. 'Auto' keeps hardware default.
<b>PCI-E Detect Wait Time</b>	Disable 500ms Auto[Default]	Set PCIe port TxRx detect polling.
<b>Compliance Mode</b>	No[Default] Yes	Enable/Disable Comliancer Mode for this PCIe port.
<b>MCTP</b>	No Yes[Default]	Enable/Disable MCTP.
<b>Equalization Bypass To Highest Rate</b>	Disable Enable[Default]	Equalization Bypass To Highest Rate Support Enable/Disable.
<b>CXL Drift Buffer</b>	Disable[Default] Enable	Enable/Disable CXL Drift Buffer if there is a common reference clock.

### 3.6.4.3.3 Intel VT for Directed I/O (VT-d)



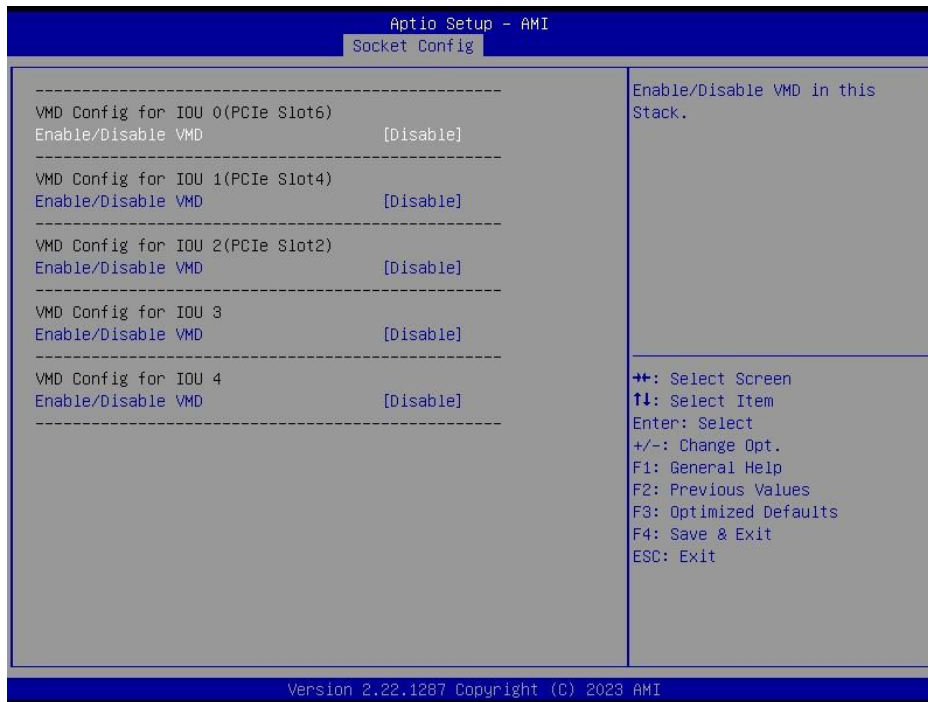
Item	Options	Description
Intel VT for Directed I/O	Enable[Default] Disable	Enable/Disable Intel Virtualization Technology for Directed I/O (VT-d) by reporting the I/O device assignment to VMM through DMAR ACPI Tables. To disable VT-d, X2APIC must also be disabled.

### 3.6.4.3.4 Intel VMD technology



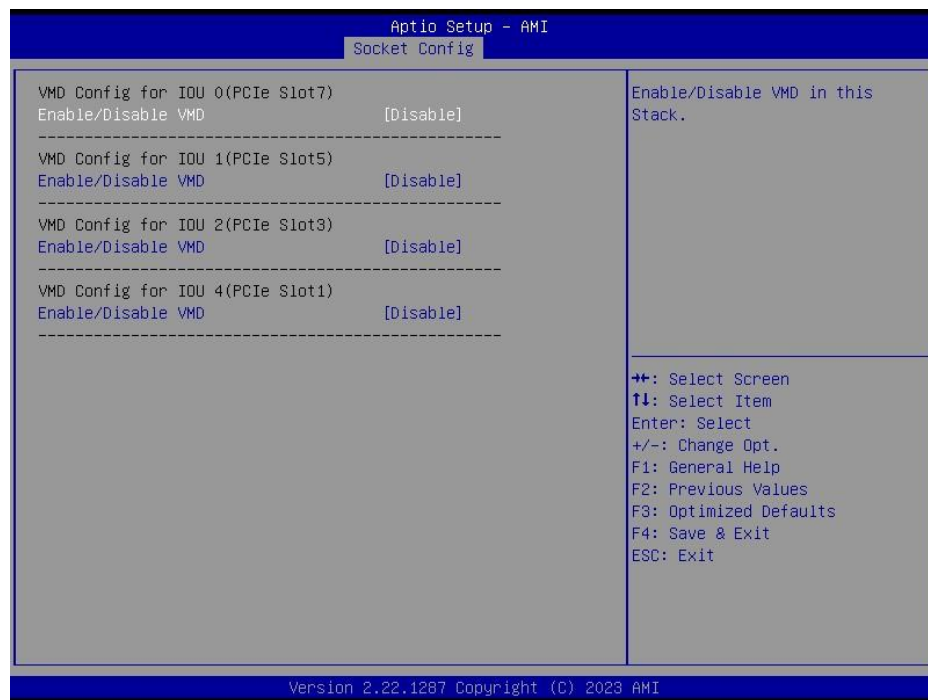
# HPM-SRSDE User's Manual

## 3.6.4.3.4.1 Intel VMD for Volume Management Device on Socket 0



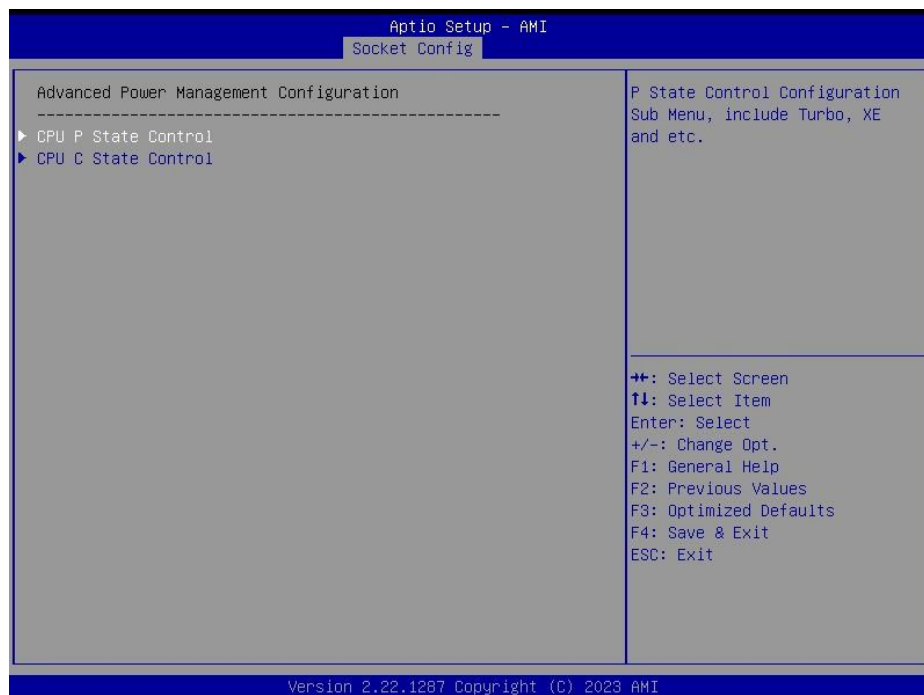
Item	Option	Description
Enable/Disable VMD	Disable[Default] Enable	Enable/Disable VMD in this Stack.

## 3.6.4.3.4.2 Intel VMD for Volume Management Device on Socket 1

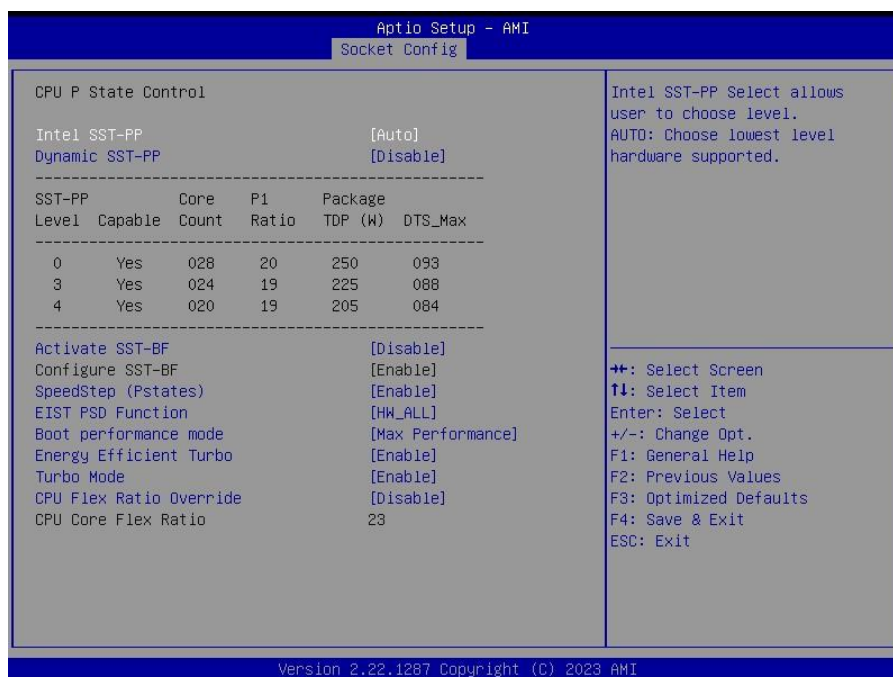


Item	Option	Description
Enable/Disable VMD	Disable[Default] Enable	Enable/Disable VMD in this Stack.

### 3.6.4.4 Advanced Power Management Configuration



#### 3.6.4.4.1 CPU P State Control

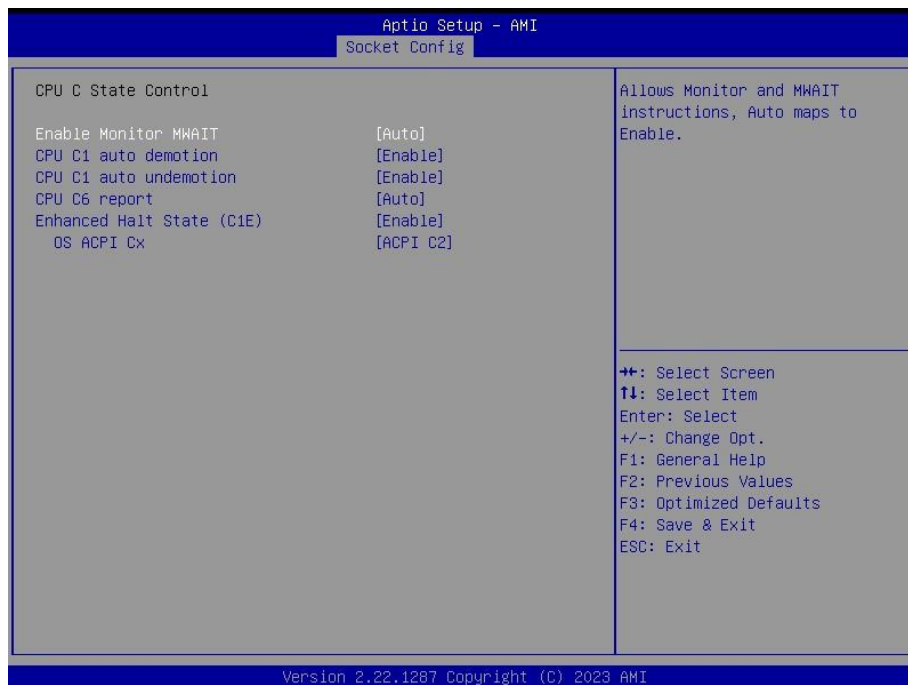


Item	Option	Description
Intel SST-PP	Auto[Default]	Intel SST-PP Select allows user to choose level. AUTO: Choose lowest level hardware supported.
	Level0	
	Level1	
	Level2	

## HPM-SRSDE User's Manual

	Level3 Level4	
<b>Dynamic SST-PP</b>	Disable[ <b>Default</b> ] Enable	Support Dynamic SST-PP selection NOTE: HWP Native Mode is a pre-requisite for enabling Dynamic SST-PP.
<b>Activate SST-BF</b>	Disable[ <b>Default</b> ] Enable	This Option allows SST-BF to be enabled. NOTE: HWP Native Mode is a pre-requisite for enabling SST-BF; HWP Native Mode with No Legacy is a pre-requisite for configuring SST-BF.
<b>SpeedStep (Pstates)</b>	Disable Enable[ <b>Default</b> ]	Enable/Disable EIST (P-States).
<b>EIST PSD Function</b>	HW_ALL[ <b>Default</b> ] SW_ALL	Choose HW_ALL/SW_ALL in _PSD return.
<b>Boot performance mode</b>	Max Performance[ <b>Default</b> ] Max Efficient Set by Intel Node Manager	Select the performance state that the BIOS will set before OS hand off.
<b>Energy Efficient Turbo</b>	Enable[ <b>Default</b> ] Disable	Energy Efficient Turbo Disable, MSR 0x1FC[19].
<b>Turbo Mode</b>	Disable Enable[ <b>Default</b> ]	Enable/Disable processor Turbo Mode (requires EMTTM enabled too).
<b>CPU Flex Ratio Override</b>	Disable[ <b>Default</b> ] Enable	Enable/Disable CPU Flex Ratio Programming.

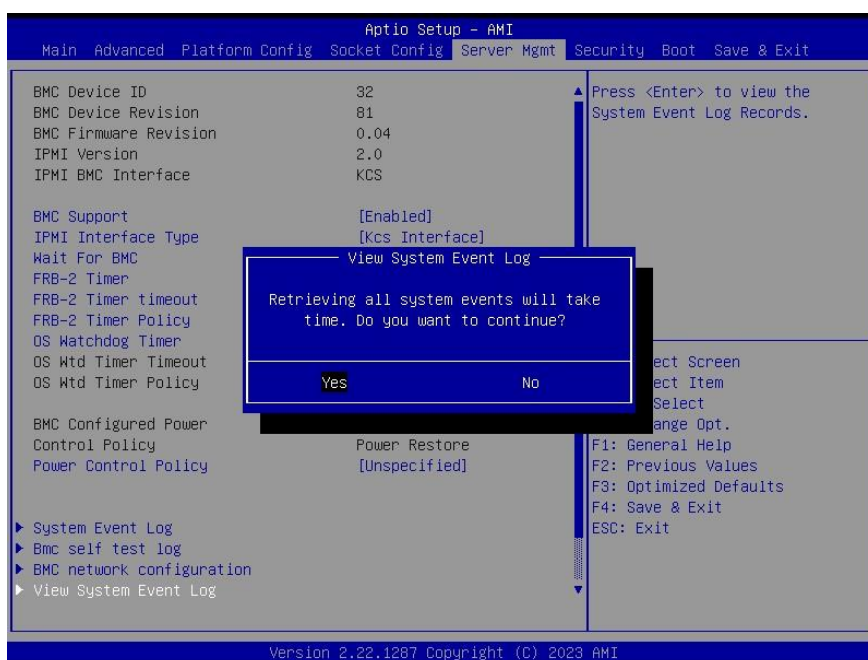
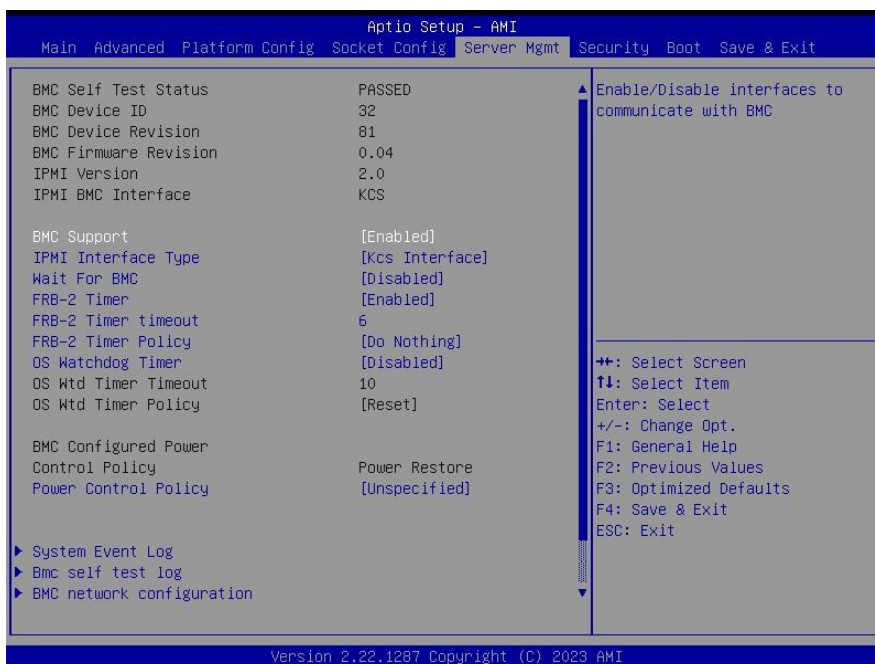
### 3.6.4.4.2 CPU C State Control



Item	Option	Description
<b>Enable Monitor MWAIT</b>	Disable Enable Auto[ <b>Default</b> ]	Allows Monitor and MWAIT instructions, Auto maps to Enable.
<b>CPU C1 auto demotion</b>	Disable	Allows CPU to automatically demote to C1.

	Enable[Default]	Takes effect after reboot.
<b>CPU C1 auto undemotion</b>	Disable Enable[Default]	Allows CPU to automatically undemote from C1. Takes effect after reboot.
<b>CPU C6 report</b>	Disable Enable Auto[Default]	Enable/Disable CPU C6(ACPI C3) report to OS, Auto maps to enable.
<b>Enhanced Halt State (C1E)</b>	Disable Enable[Default]	Core C1E auto promotion Control. Takes effect after reboot. Will be enforced to enable when Optimized Power Mode is enabled.
<b>OS ACPI Cx</b>	ACPI C2[Default] ACPI C3	Report CC3/CC6 to OS ACPI C2 or ACPI C3.

### 3.6.5 Server Mgmt

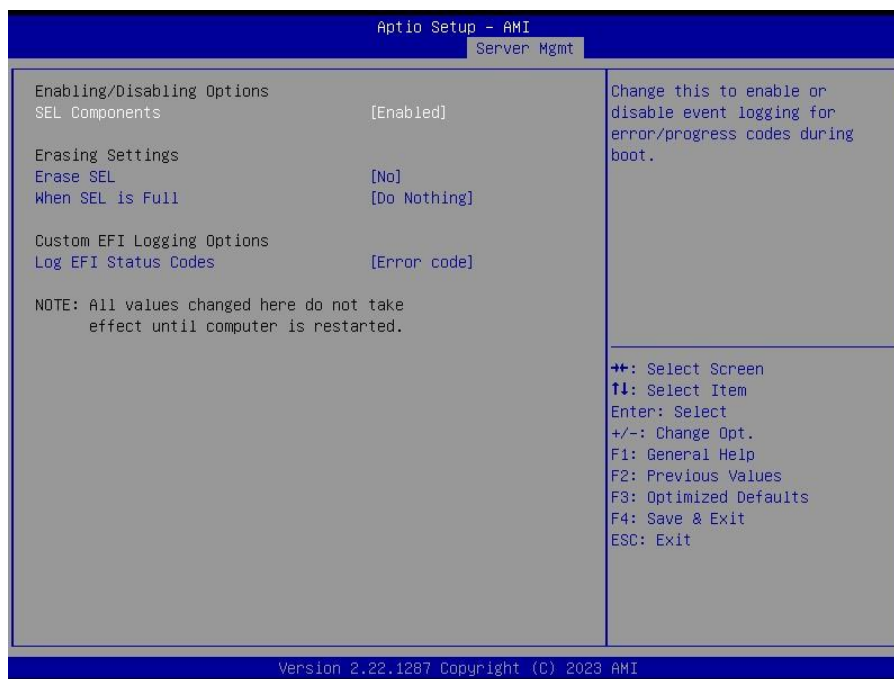


## HPM-SRSDE User's Manual

Item	Options	Description
<b>BMC Support</b>	Enabled[ <b>Default</b> ] Disabled	Enable/Disable interfaces to communicate with BMC.
<b>IPMI Interface Type</b>	Kcs Interface[ <b>Default</b> ] Ssif Interface Ipmb Interface Usb Interface Oem1 Interface Oem2 Interface	Type of Interface to communicate BMC from HOST.
<b>Wait For BMC</b>	Enabled Disabled[ <b>Default</b> ]	Wait For BMC response for specified time out. BMC starts at the same time when BIOS starts during AC power ON. It takes around 30 seconds to initialize Host to BMC interfaces.
<b>FRB-2 Timer</b>	Enabled[ <b>Default</b> ] Disabled	Enable or Disable FRB-2 time (POST timer).
<b>FRB-2 Timer timeout</b>	6	Enter value Between 3 to 6 min for FRB-2 Timer Expiration value.
<b>FRB-2 Timer Policy</b>	Do Nothing[ <b>Default</b> ] Reset Power Down Power Cycle	Configure how the system should respond if the FRB-2 Timer expires. Not available if FRB-2 Timer is disabled.
<b>OS Watchdog Timer</b>	Enabled Disabled[ <b>Default</b> ]	If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine that the OS successfully loaded or follows the OS Boot Watchdog Timer policy.
<b>Power Control Policy</b>	Do Not PowerUp Last Power State Power Restore Unspecified[ <b>Default</b> ]	Configure how the system should respond if AC Power is lost, Reset not required as selected Power policy will be set in BMC when policy is saved.



### 3.6.5.1 System Event Log



Item	Option	Description
<b>SEL Components</b>	Enabled[Default] Disabled	Change this to enable or disable event logging for error/progress codes during boot.
<b>Erase SEL</b>	No[Default] Yes, On next reset Yes, On every reset	Choose options for erasing SEL.
<b>When SEL is Full</b>	Do Nothing[Default] Erase Immediately Delete Oldest Record	Choose options for reactions to a full SEL.
<b>Log EFI Status Codes</b>	Disabled Both Error code[Default] Progress code	Disable the logging of EFI Status Codes or log only error code or only progress code or both.

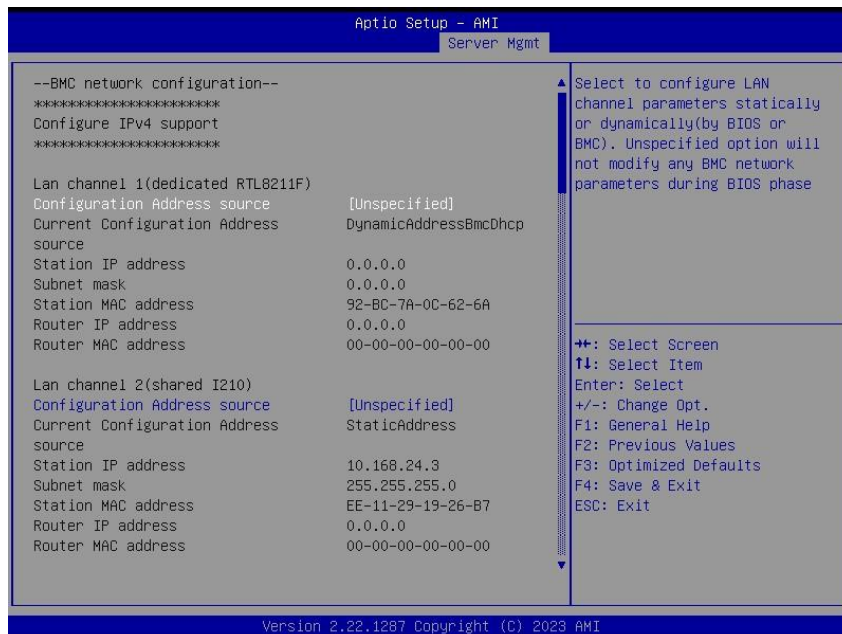
# HPM-SRSDE User's Manual

## 3.6.5.2 BMC self test log



Item	Option	Description
Erase Log	Yes, On every reset[Default]	Erase Log Options.
	No	
When log is full	Clear Log[Default]	Select the action to be taken when log is full.
	Do not log any more	

## 3.6.5.3 BMC network configuration



Item	Option	Description
Configuration Address source	Unspecified[Default]	Select configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.
	Static	
	DynamicBmcDhcp DynamicBmcNonDhcp	

### 3.6.5.4 BMC User Settings



#### 3.6.5.4.1 BMC Add User Details



Item	Description
User Name	Enter BMC User Name.

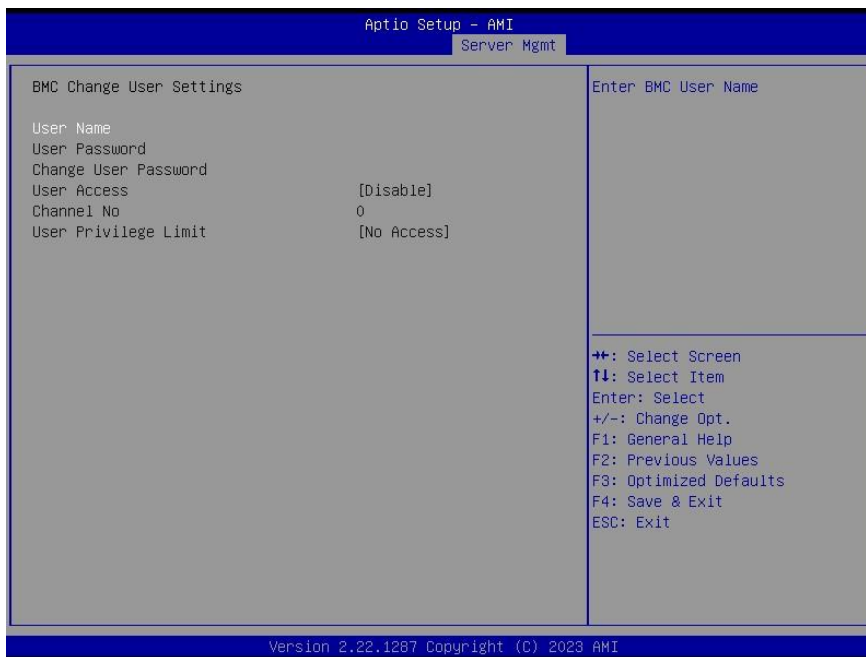
# HPM-SRSDE User's Manual

## 3.6.5.4.2 BMC Delete User Details



Item	Description
User Name	Enter BMC User Name.

## 3.6.5.4.3 BMC Change User Settings



Item	Description
User Name	Enter BMC User Name.

### 3.6.6 Security



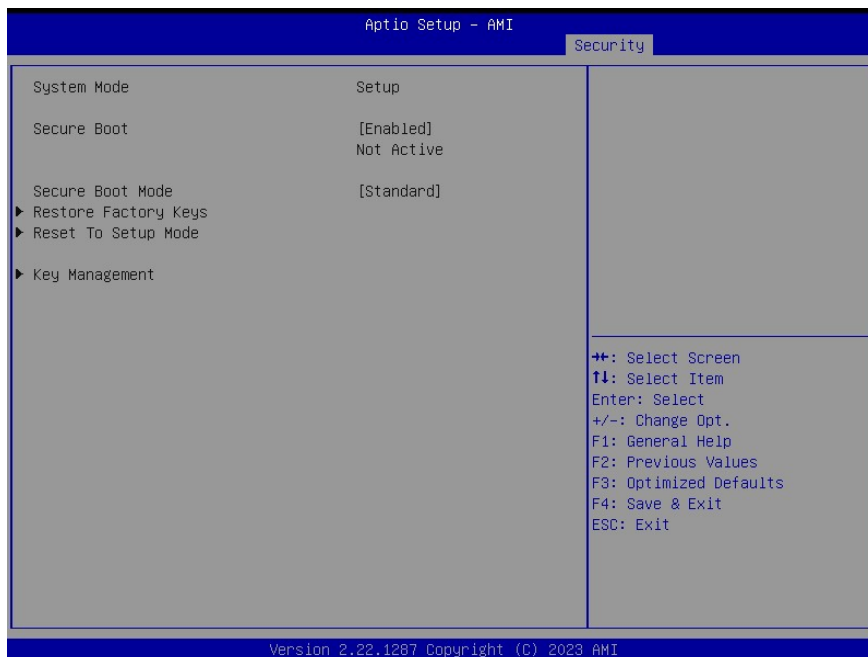
- **Administrator Password**

Set setup Administrator Password

- **User Password**

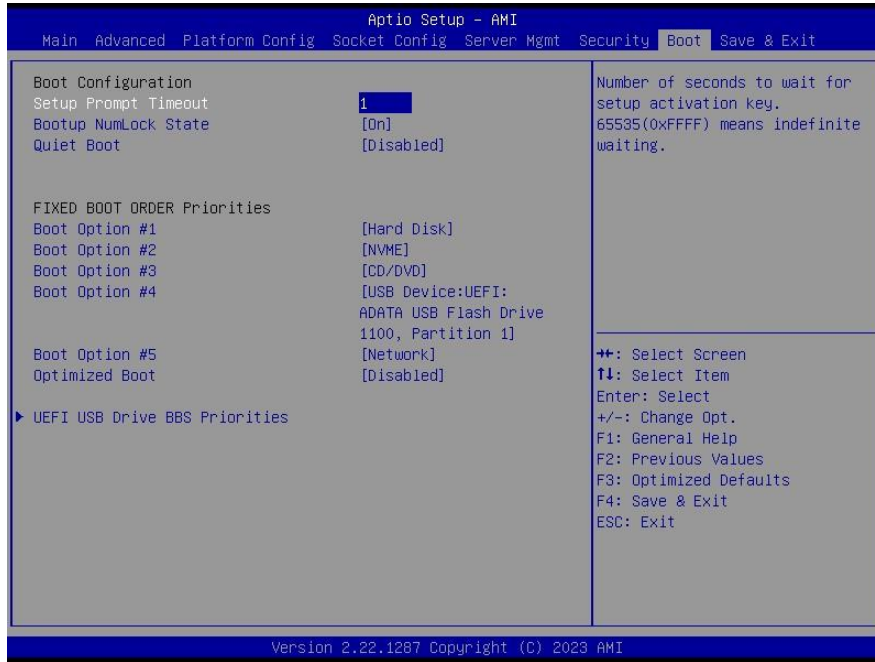
Set User Password

#### 3.6.6.1 Secure Boot



# HPM-SRSDE User's Manual

## 3.6.7 Boot

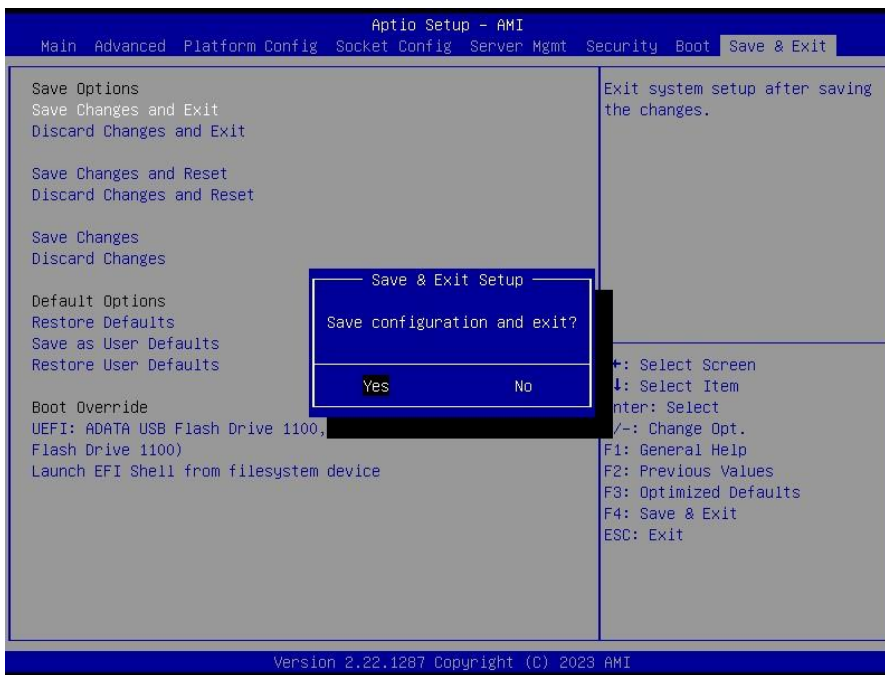
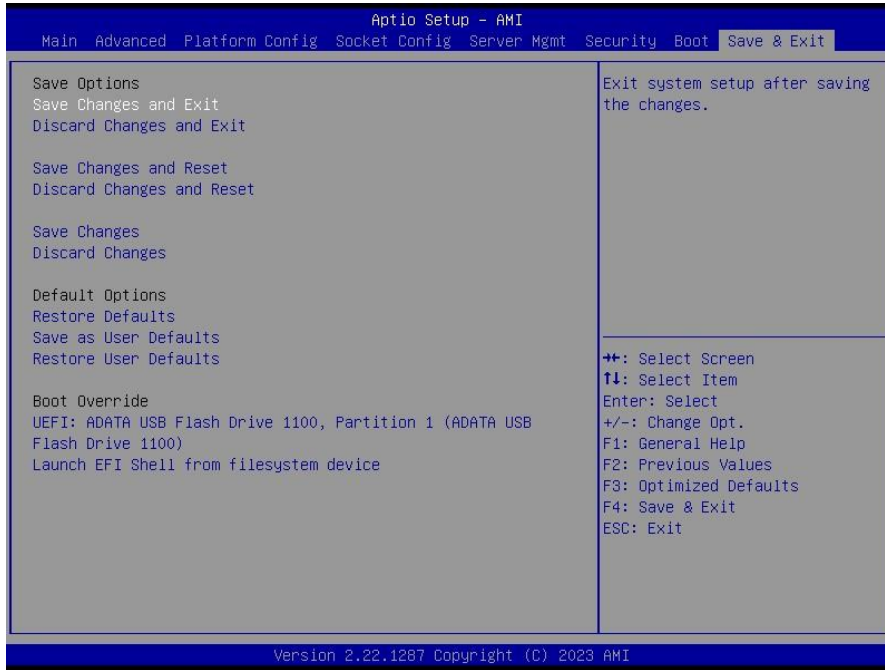


Item	Option	Description
Setup Prompt Timeout	1~ 65535	Set the default timeout before system boot. A value of 65535 will disable the timeout completely.
Bootup NumLock State	On[Default] Off	Select the keyboard NumLock state
Quiet Boot	Disabled[Default] Enabled	Enables or disables Quiet Boot option
Boot Option #1	Hard Disk[Default] NVME CD/DVD	Set the system boot order.

	USB Device Network Disabled	
<b>Boot Option #2</b>	Hard Disk NVME[Default] CD/DVD USB Device Network Disabled	Set the system boot order.
<b>Boot Option #3</b>	Hard Disk NVME CD/DVD[Default] USB Device Network Disabled	Set the system boot order.
<b>Boot Option #4</b>	Hard Disk NVME CD/DVD USB Device[Default] Network Disabled	Set the system boot order.
<b>Boot Option #5</b>	Hard Disk NVME CD/DVD USB Device Network[Default] Disabled	Set the system boot order.
<b>Optimized Boot</b>	Disabled[Default] Enabled	Enables or disables Optimized Boot. Enabling Optimized Boot will disable Csm support and disable connecting Network devices to decrease boot time. While disabling Optimized Boot, make sure to restore Csm Support option to previous value before enabling Optimized Boot.

# HPM-SRSDE User's Manual

## 3.6.8 Save and exit



### 3.6.8.1 Save Changes and Exit

Use the save changes and reset option to save the changes made to the BIOS options and to exit the BIOS configuration setup program.

### 3.6.8.2 Discard Changes and Exit

Use the Discard changes and Exit option to exit the system without saving the changes made to the BIOS configuration setup program.



**3.6.8.3 Save Changes and Reset**

Reset the system after saving the changes.

**3.6.8.4 Discard Changes and Reset**

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.

**3.6.8.5 Save Changes**

Changes made to BIOS settings during this session are committed to NVRAM. The setup program remains active, allowing further changes.

**3.6.8.6 Discard Changes**

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The BIOS setup continues to be active.

**3.6.8.7 Restore Defaults**

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

**3.6.8.8 Save as User Defaults**

This option saves a copy of the current BIOS settings as the User Defaults. This option is useful for preserving custom BIOS setup configurations.

**3.6.8.9 Restore User Defaults**

This option restores all BIOS settings to the user defaults. This option is useful for restoring previously preserved custom BIOS setup configurations.

# 4. Drivers Installation

---



**Note:** Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

## 4.1 Install Chipset Driver

All drivers can be found on the Avalue Official Website:

<http://www.avalue.com.tw>.



**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



### Step 3. Click Install.



### Step1. Click Next.



### Step 4. Setup completed.



### Step 2. Click Accept.

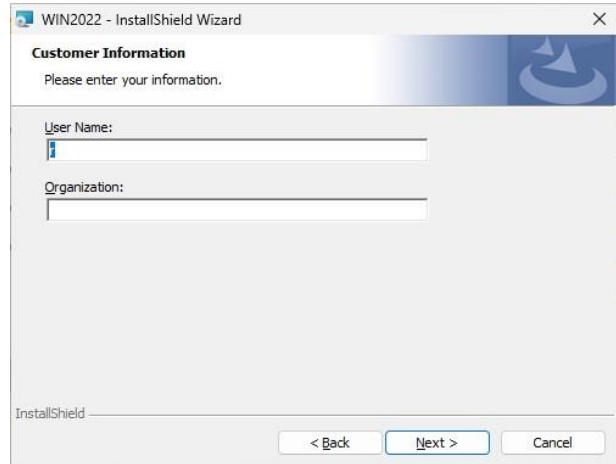
## 4.2 Install VGA Driver

All drivers can be found on the Avalue Official Website:

<http://www.avalue.com.tw>.



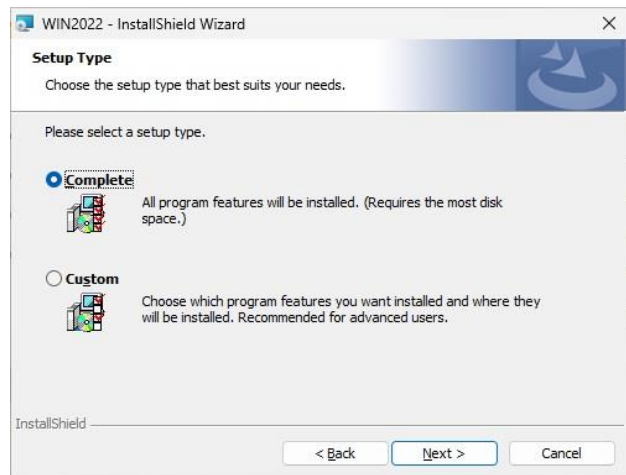
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



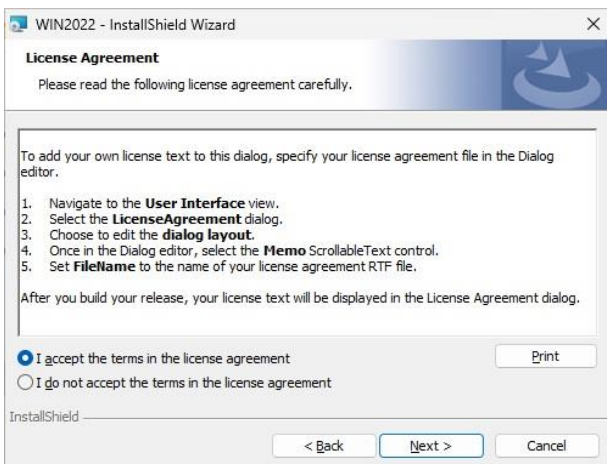
**Step 3. Click Next.**



**Step 1. Click Next** to continue installation.



**Step 4. Click Next.**



**Step 2. Click Next.**



**Step 5. Click Install.**



**Step 6.** Click **Finish** to complete setup.

## 4.3 Install Audio Driver

All drivers can be found on the Avalue Official Website:

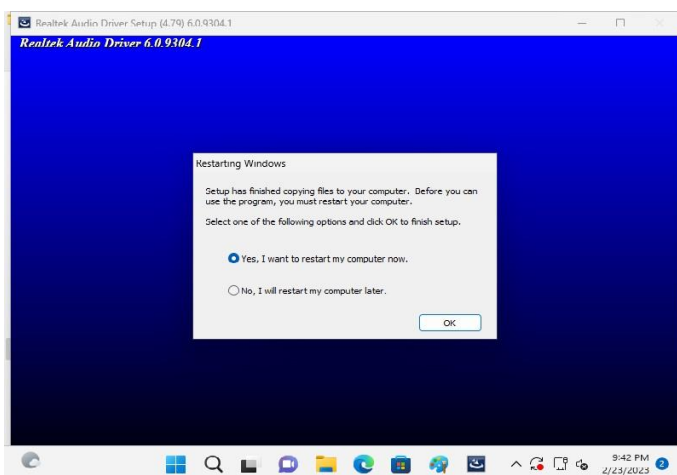
<http://www.avalue.com.tw>.



**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



**Step 1.** Click **Yes** to continue installation.



**Step 2.** Setup completed.

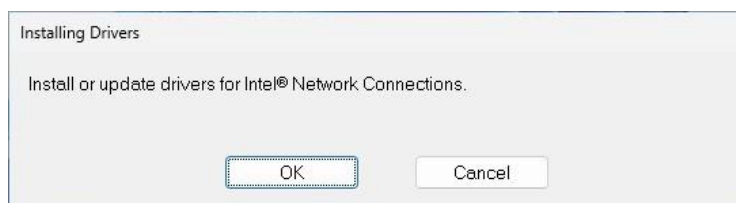
## 4.4 Install Ethernet Driver

All drivers can be found on the Avalue Official Website:

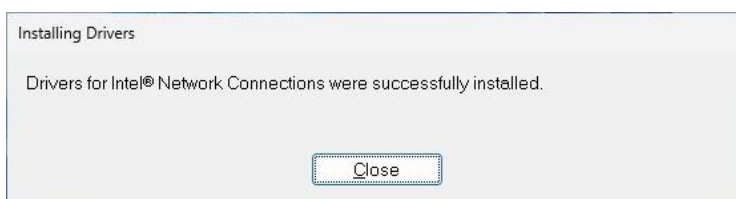
<http://www.avalue.com.tw>.



**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



**Step 1.** Click **OK** to continue installation.



**Step 2.** Setup completed.

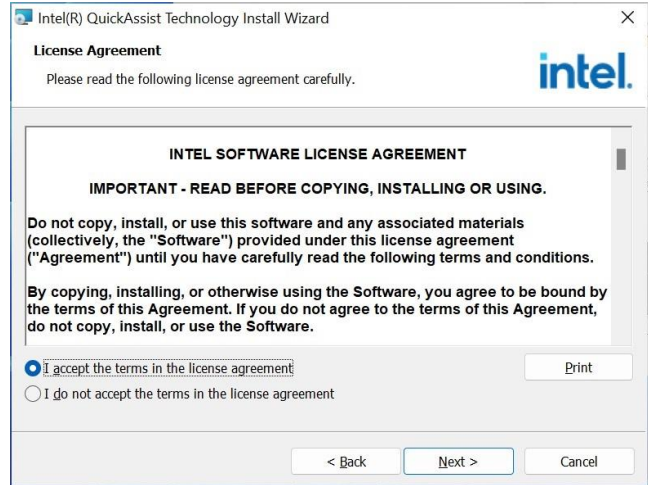
## 4.5 Install QuickAssist Technology Driver

All drivers can be found on the Avalue Official Website:

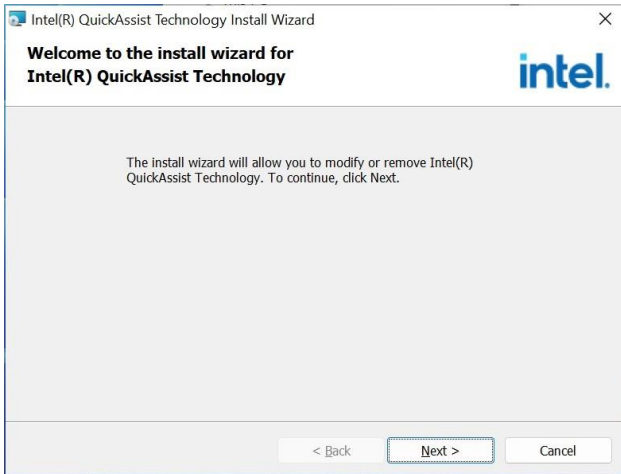
<http://www.avalu.com.tw>.



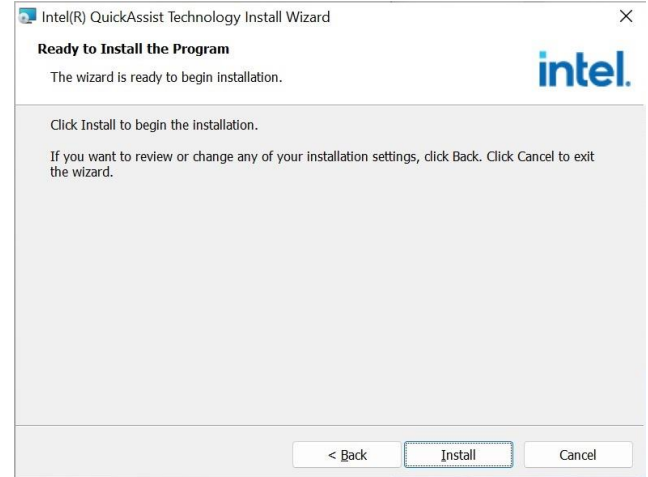
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



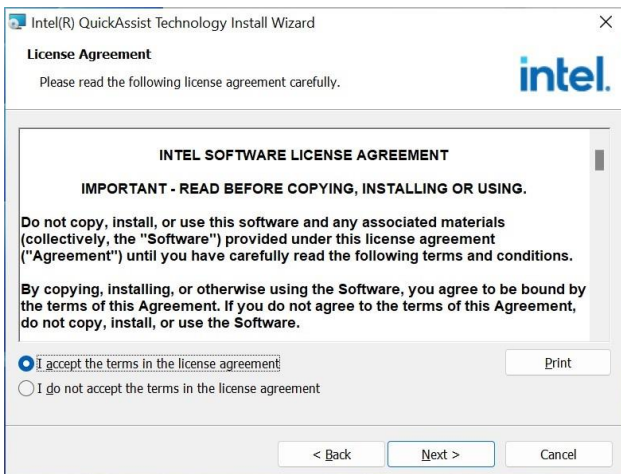
**Step 3. Click Next.**



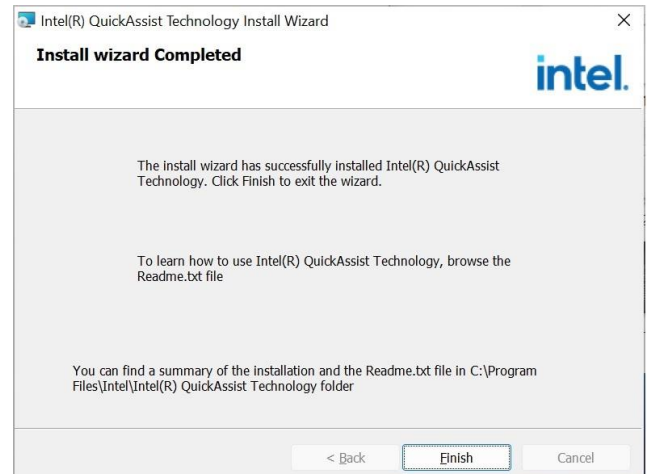
**Step 1. Click Next** to continue installation.



**Step 4. Click Install.**



**Step 2. Click Next.**



**Step 5. Click Finish** to complete setup.



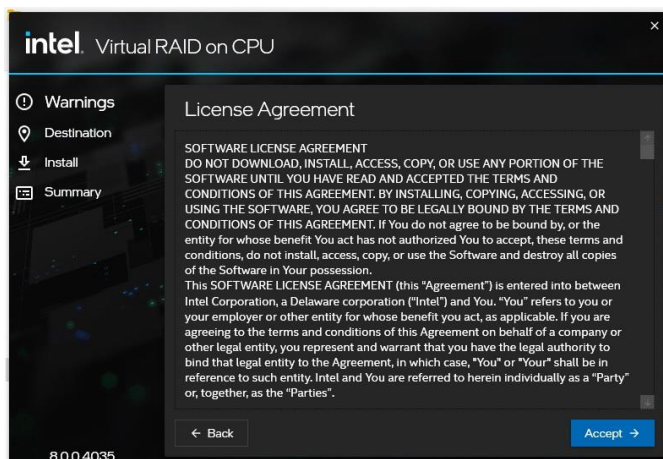
## 4.6 Install VROC Driver

All drivers can be found on the Avalue Official Website:

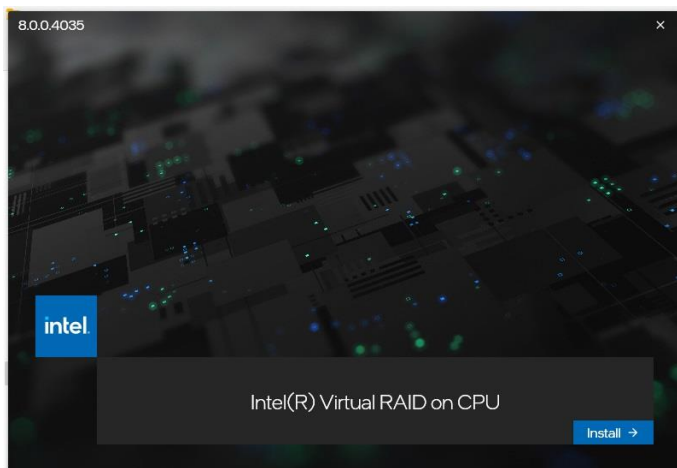
<http://www.avalue.com.tw>.



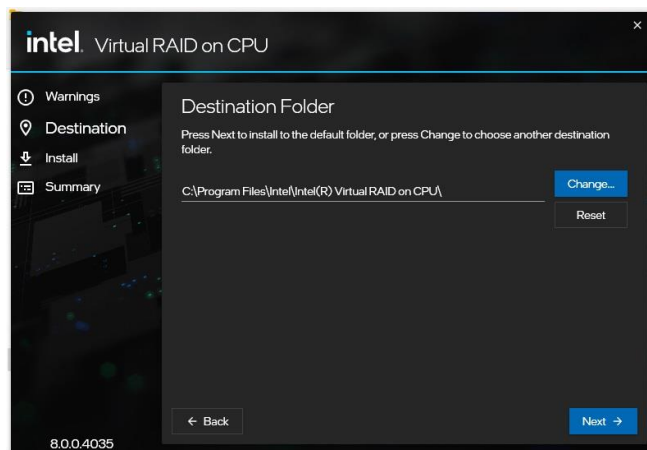
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



**Step 3. Click Accept.**



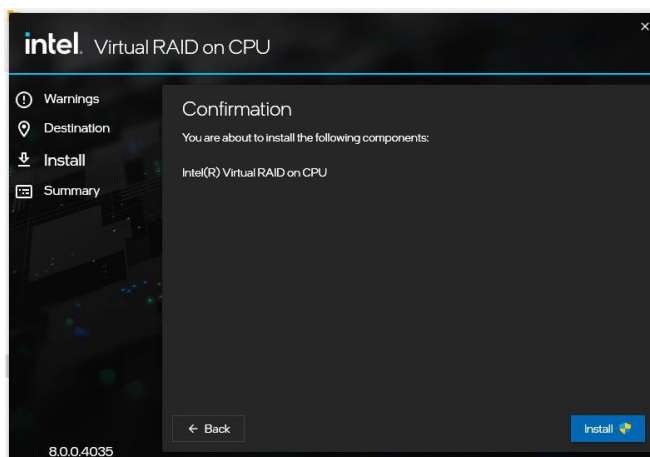
**Step 1. Click Install** to continue installation.



**Step 4. Click Next.**

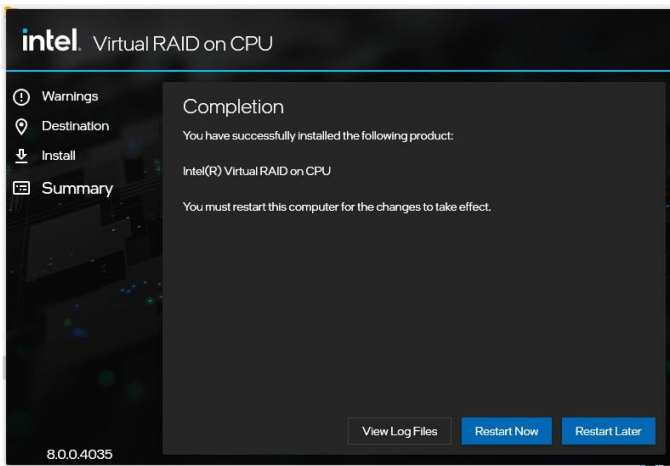


**Step 2. Click Next.**



**Step 5. Click Install.**

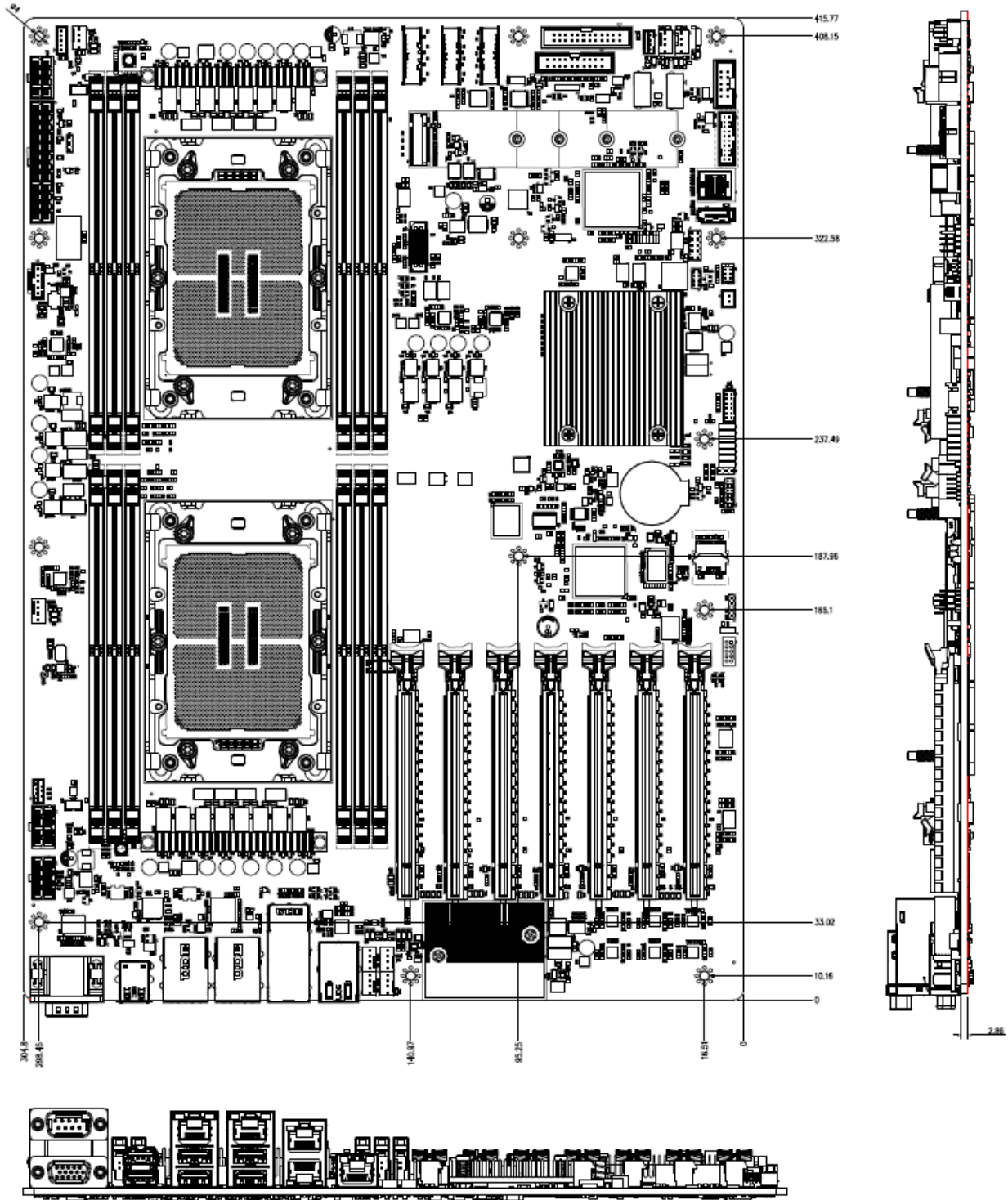
## HPM-SRSDE User's Manual



**Step 6.** Setup completed.

# 5. Mechanical Drawing

---



Unit: mm

